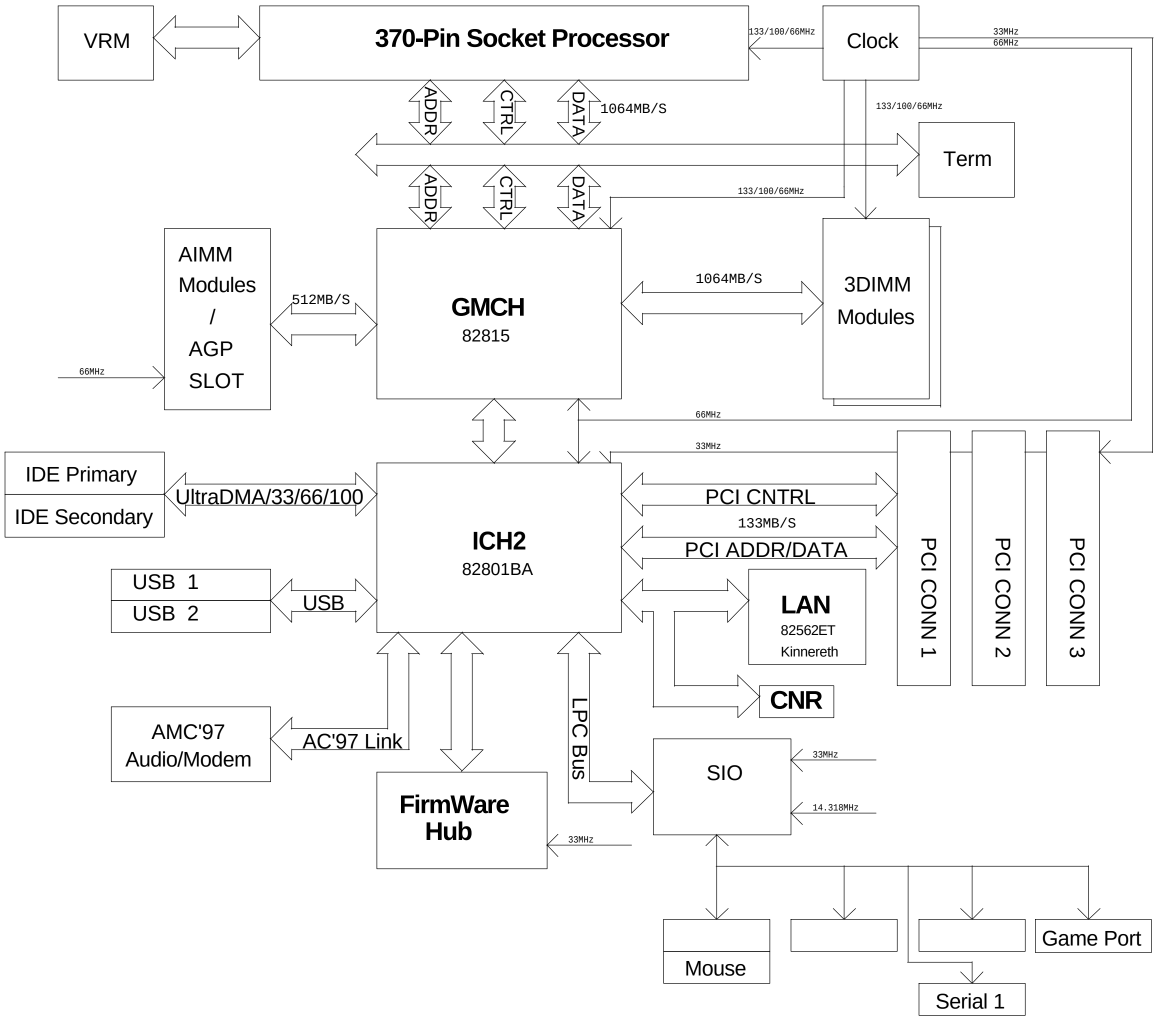
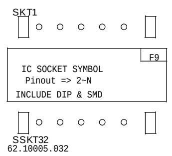
S58M
(VT5100/VT7100)
Block Diagram

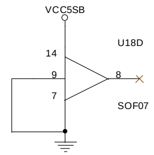


S58M-1A

Intel Solano

SHEET	THE HISTORY FROM SA TO SB	
Sheet 1	COVER PAGE	LINK
Sheet 2	SOCKET370	2.SCH
Sheet 3	CLOCK GENERATOR	3.SCH
Sheet 4	GTL+ BUS TERMINATOR	4.SCH
Sheet 5	SOLANO-GMCH	5.SCH
Sheet 6	DIMM SKT #1 AND #2	6.SCH
Sheet 7	DIMM SKT #3	7.SCH
Sheet 8	AGP SKT	8.SCH
Sheet 9 VGA out		9.SCH
Sheet 10	ICH2	10.SCH
Sheet 11	ICH2 PULLUP	11.SCH
Sheet 12	PCI SLOT 1-3 & PCI PULLUPS	12.SCH
Sheet 13	IDE CONNECTORS	13.SCH
Sheet 14	USB PORTS/LAN PORT	14.SCH
Sheet 15	FIRMWARE HUB/CNR/KINNERETH	15.SCH
Sheet 16	AC97 CODEC	16.SCH
Sheet 17	AUDIO CONNECTORS PART1 & GAME PORT	17.SCH
Sheet 18	AUDIO CONNECTORS PART2	18.SCH
Sheet 19	SUPER I/O LPC47B277 /IRDA	19.SCH
Sheet 20	PS2 KB/MOUSE/RF CNN	20.SCH
Sheet 21	PRINTER PORT & COM PORT & VGA CONN	21.SCH
Sheet 22	VCORE PWM RC5057 & REGULATORS	22.SCH
Sheet 23	HARDWARE MONITORING	23.SCH
Sheet 24	ATX POWER CONN. & STR CONTROL	24.SCH
Sheet 25	LED, FAN, PWR BUTTON, RESET	25.SCH
Sheet 26		26.SCH
Sheet 27	BULK CAP.	27.SCH
Sheet 28	BYPASS CAP. (VCC1.5, VCORE)	28.SCH
Sheet 29	BYPASS CAP.	29.SCH



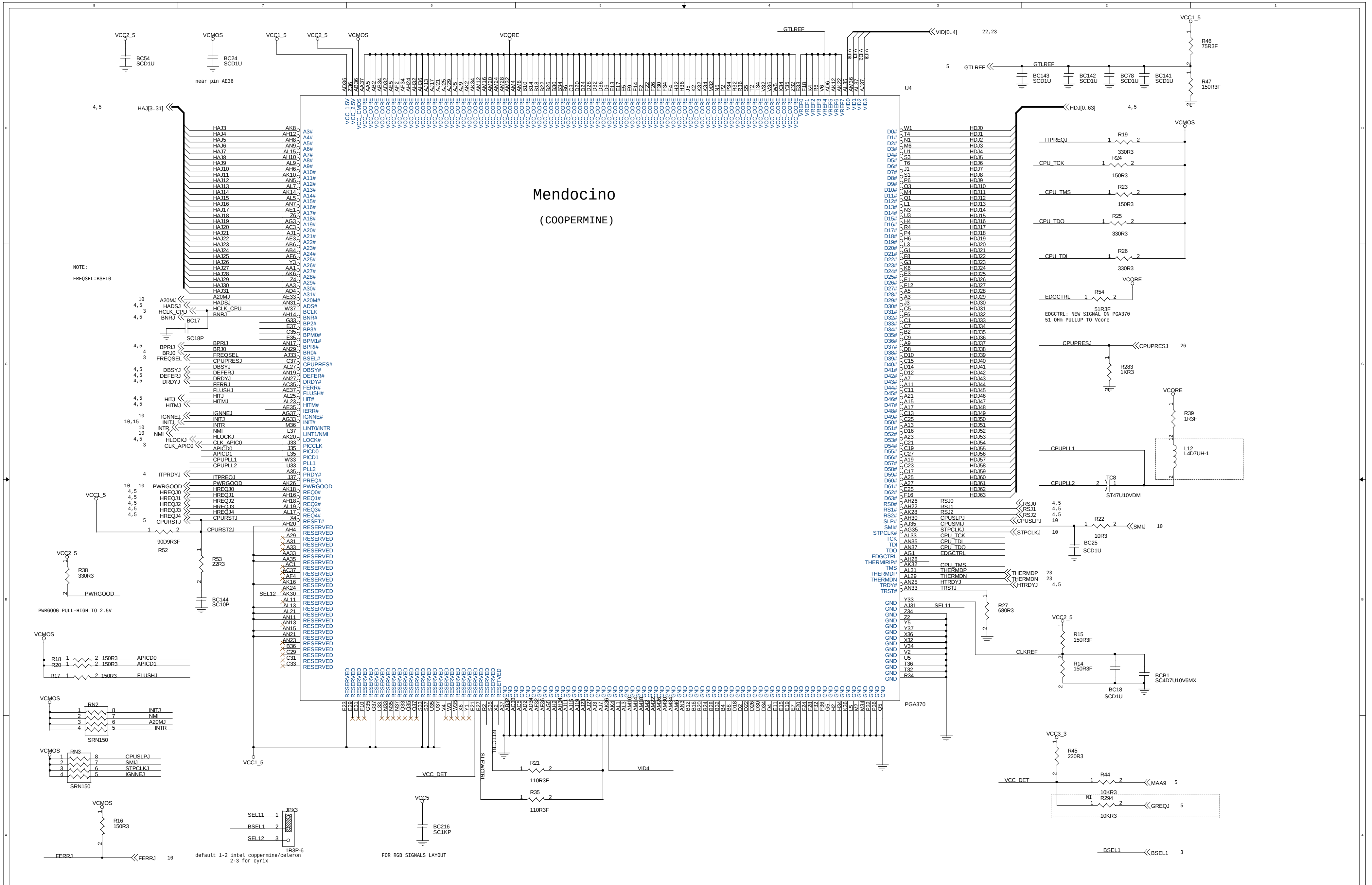


CD-IN(CN9)
FAX-VOICE(CN13)
MIC&LINE-OUT(CN11)
LINE OUT OP(JP2)

PS-ON(JP4)
RESET (JP5)
PWR&SUS-LED(CN14)
IDE-LED(CN22)
WOL (CN16)
INTRUDER (CN22)
Active/Turbo LED (JP1)
BOOTBLOCK [JPXA(2-3)]
CHECK PW [JPXB(2-3)]
EEPROM CS[JP3(1-2)]
RTC RESET [JPX1(2-3)]
CPU Speed Safe mode[JPX2(2-3)]
SUPPORT Cyrix cpu[JPX3(1-2)]
AOL (CN20)

ACER_AI_DESKTOP

Title	S58M COVER PAGE (S58M.SCH)	
Size B	Document Number	Rev 1
Date: Tuesday, June 20, 2000	Sheet 1 of 29	



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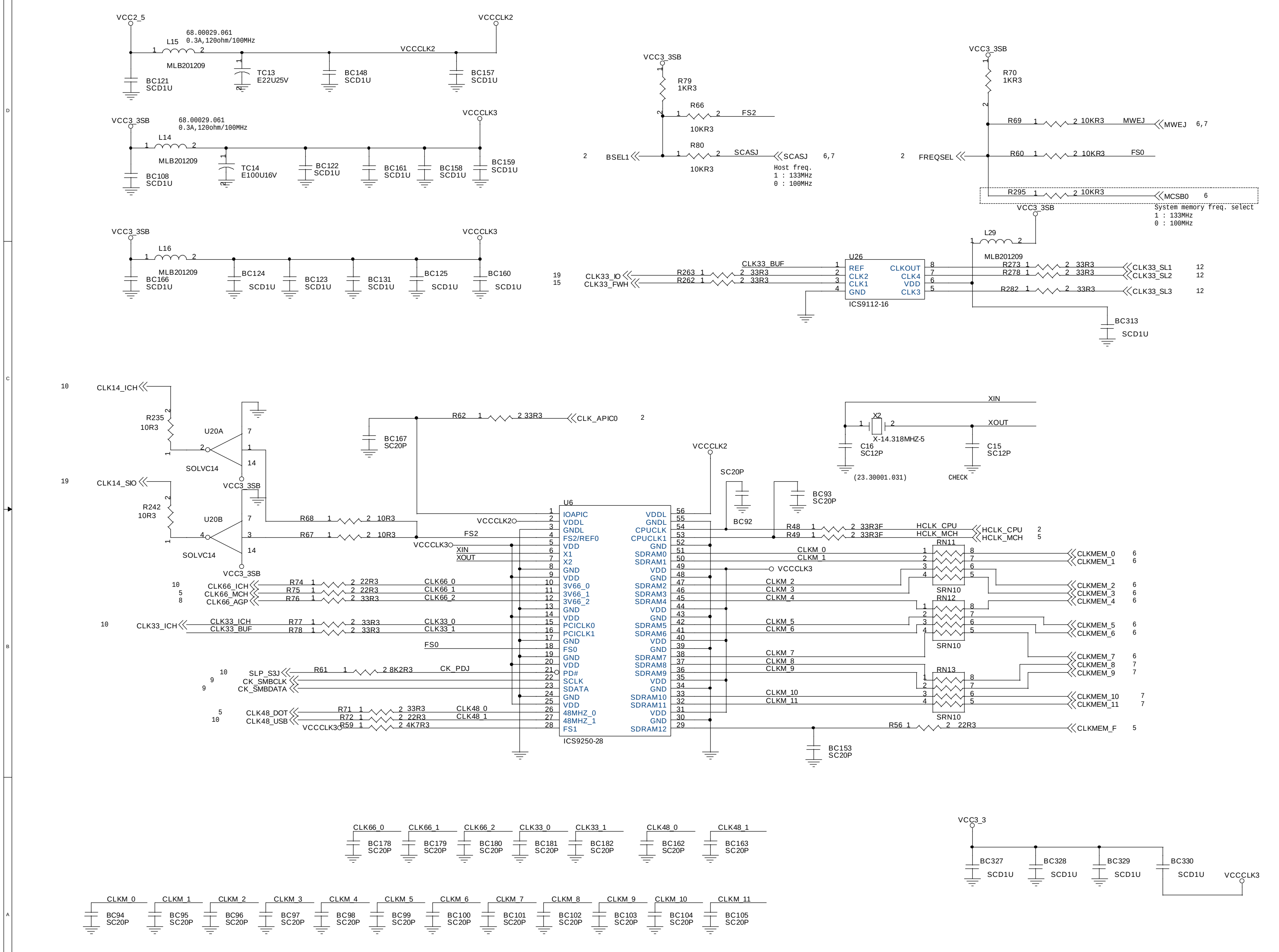
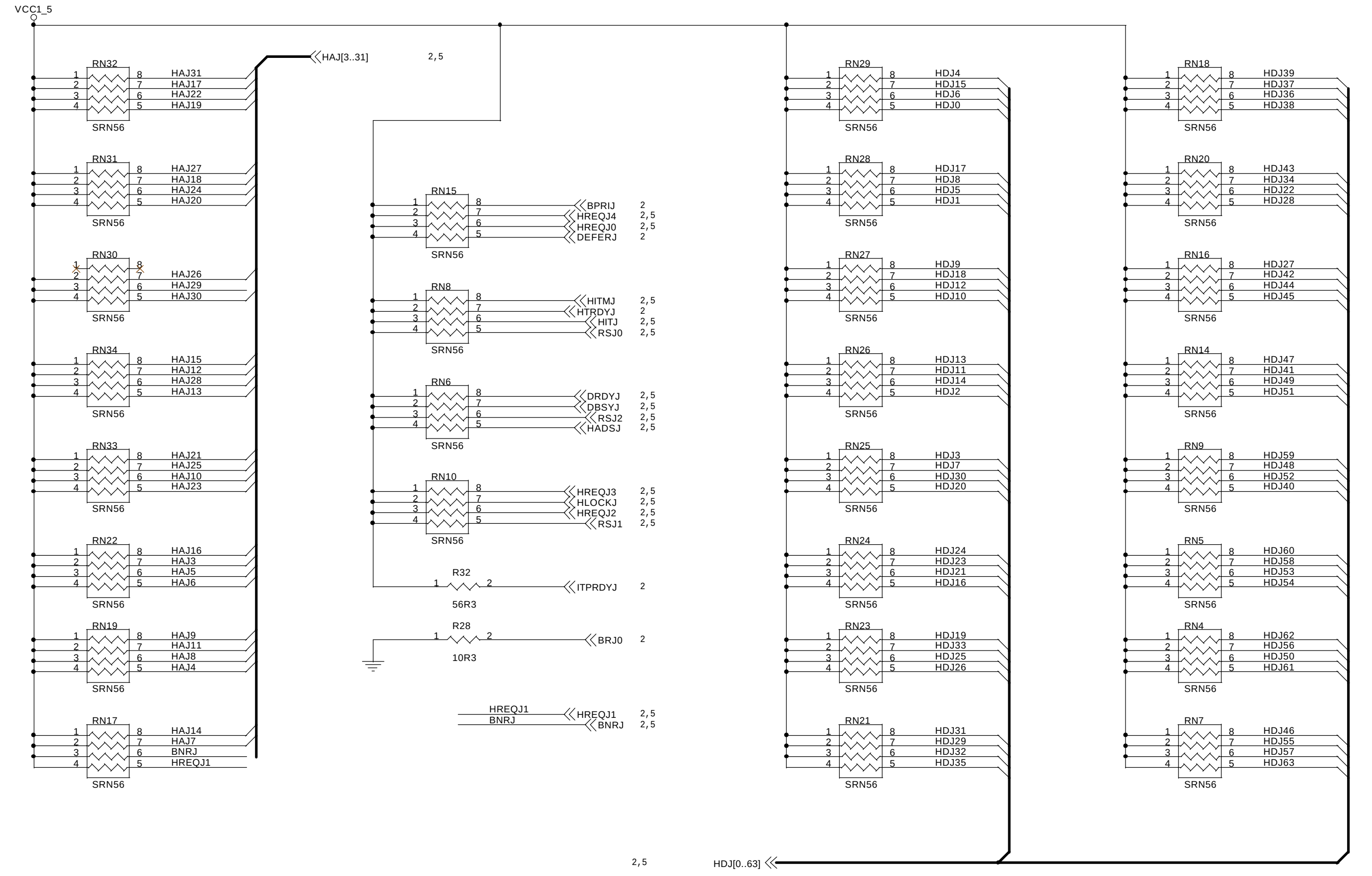
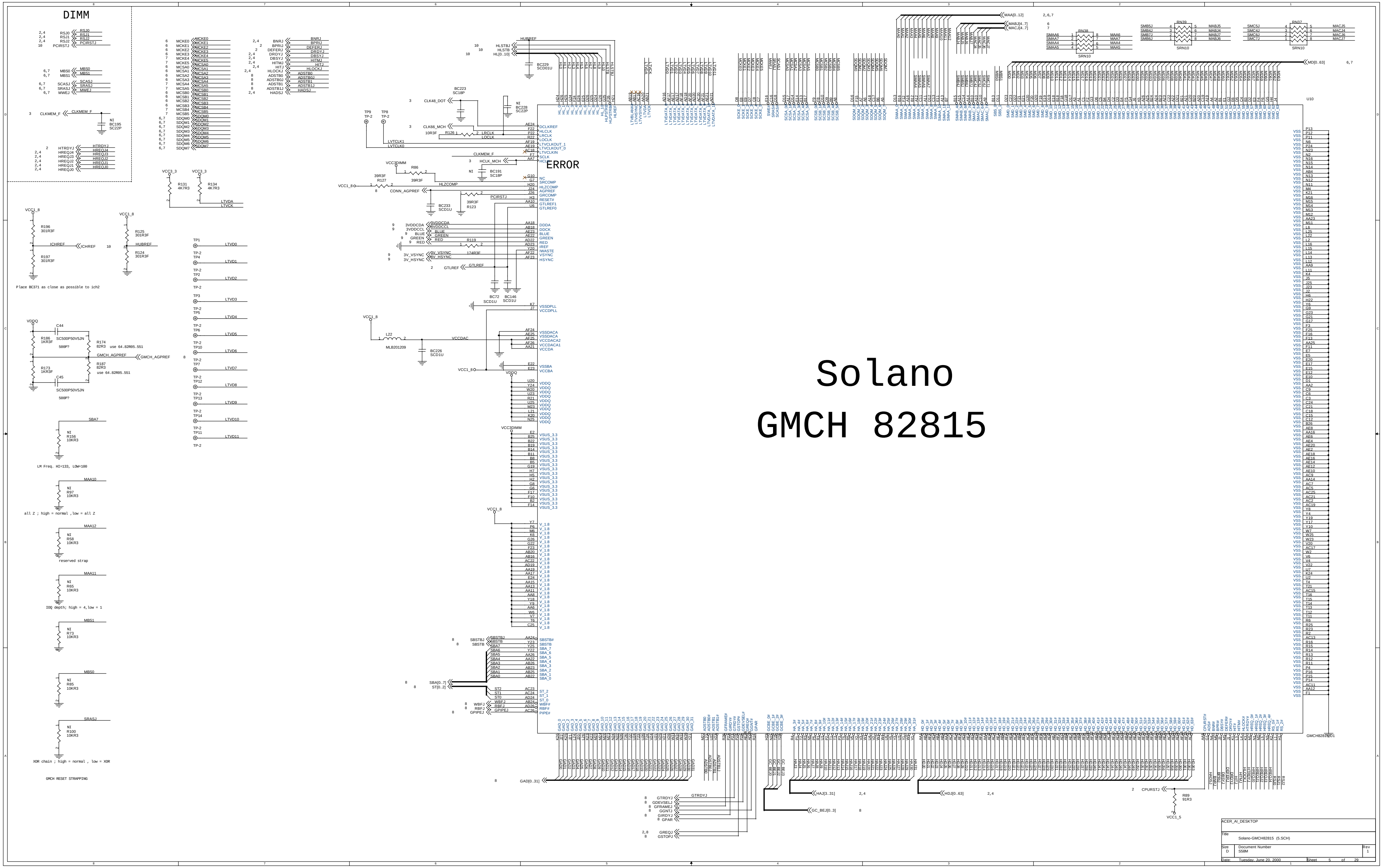
Title	SKT370 (2.SCH)	
Size C	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 2 of 29	

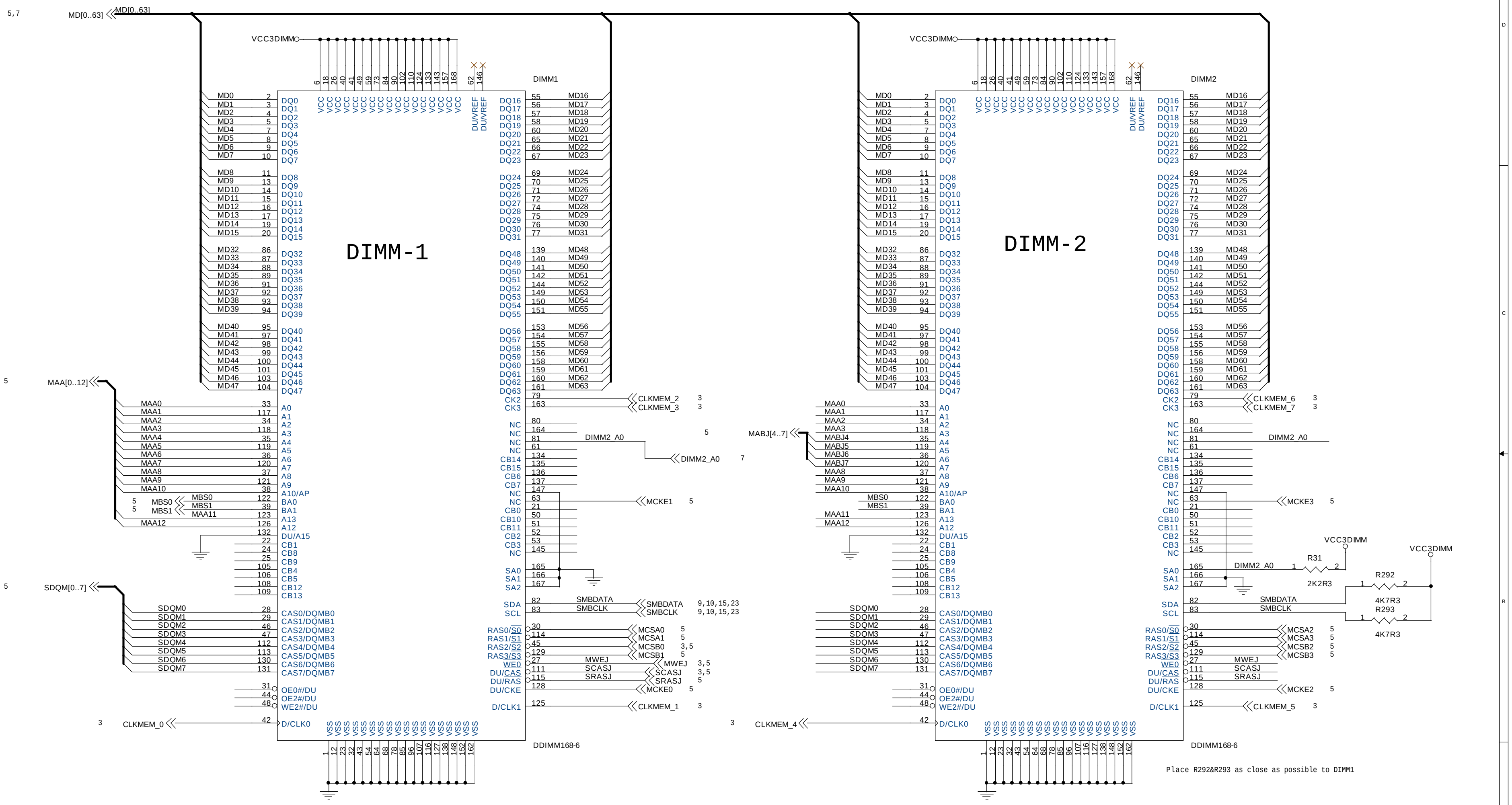


ACER_AI_DESKTOP		
Title: CLOCK GENERATOR (3.SCH)		
Size: B	Document Number: S58M	Rev: 1
Date: Tuesday, June 20, 2000	Sheet 3 of 29	



ACER_AI_DESKTOP		
Title: GTL+ Terminator, (4.SCH)		
Size: B	Document Number: S58M	Rev: 1
Date: Tuesday, June 20, 2000	Sheet 4 of 29	


DIMM
ERROR
Solano
GMCH 82815
Place BC371 as close as possible to ich2
LM Freq. HI=133, LOW=100
all Z ; high = normal ,low = all Z
reserved strap
IOQ depth; high = 4,low = 1
XOR chain ; high = normal , low = XOR
GMCH RESET STRAPPING
ACER_AI_DESKTOP
Title
Solano-GMCH82815 (S.SCH)
Size D
Document Number S58M
Rev 1
Date: Tuesday, June 20, 2000
Sheet 5 of 29

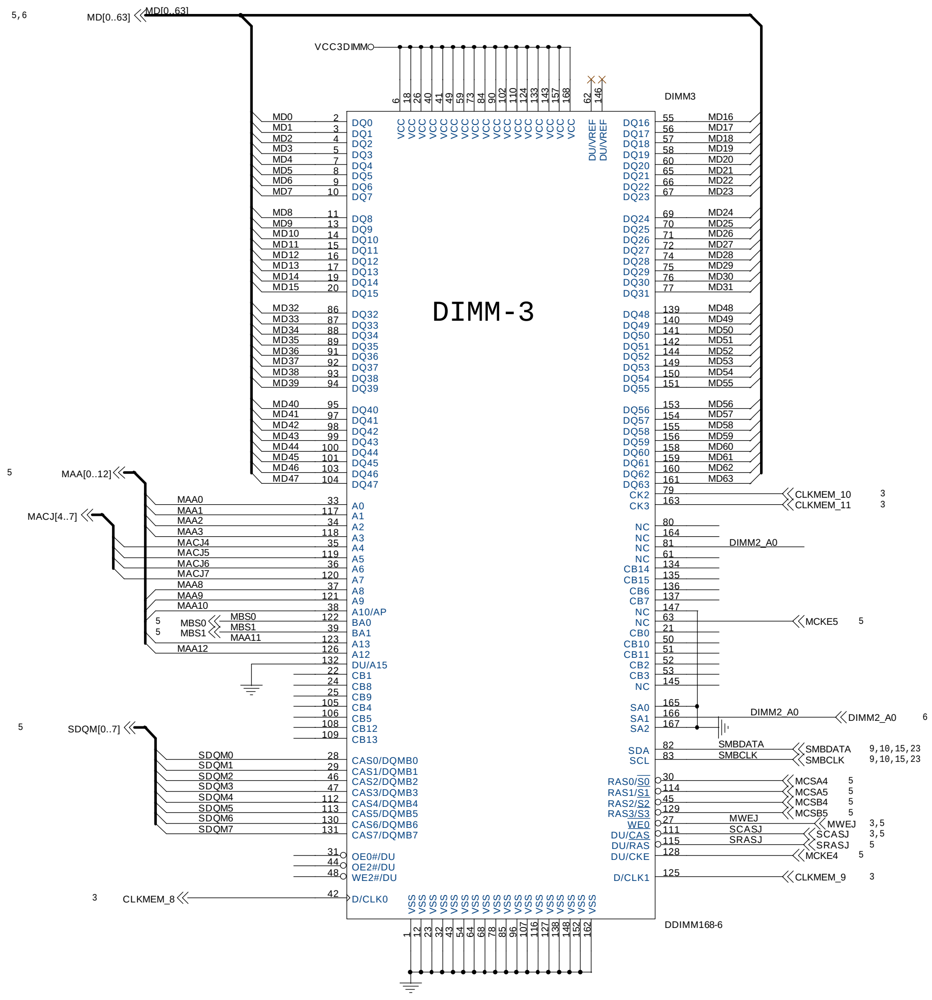


ACER_AI_DESKTOP

Title: DIMM #1 & #2 (6.SCH)

Size	Document Number	Rev
B	S58M	1

Date: Tuesday, June 20, 2000 Sheet 6 of 29

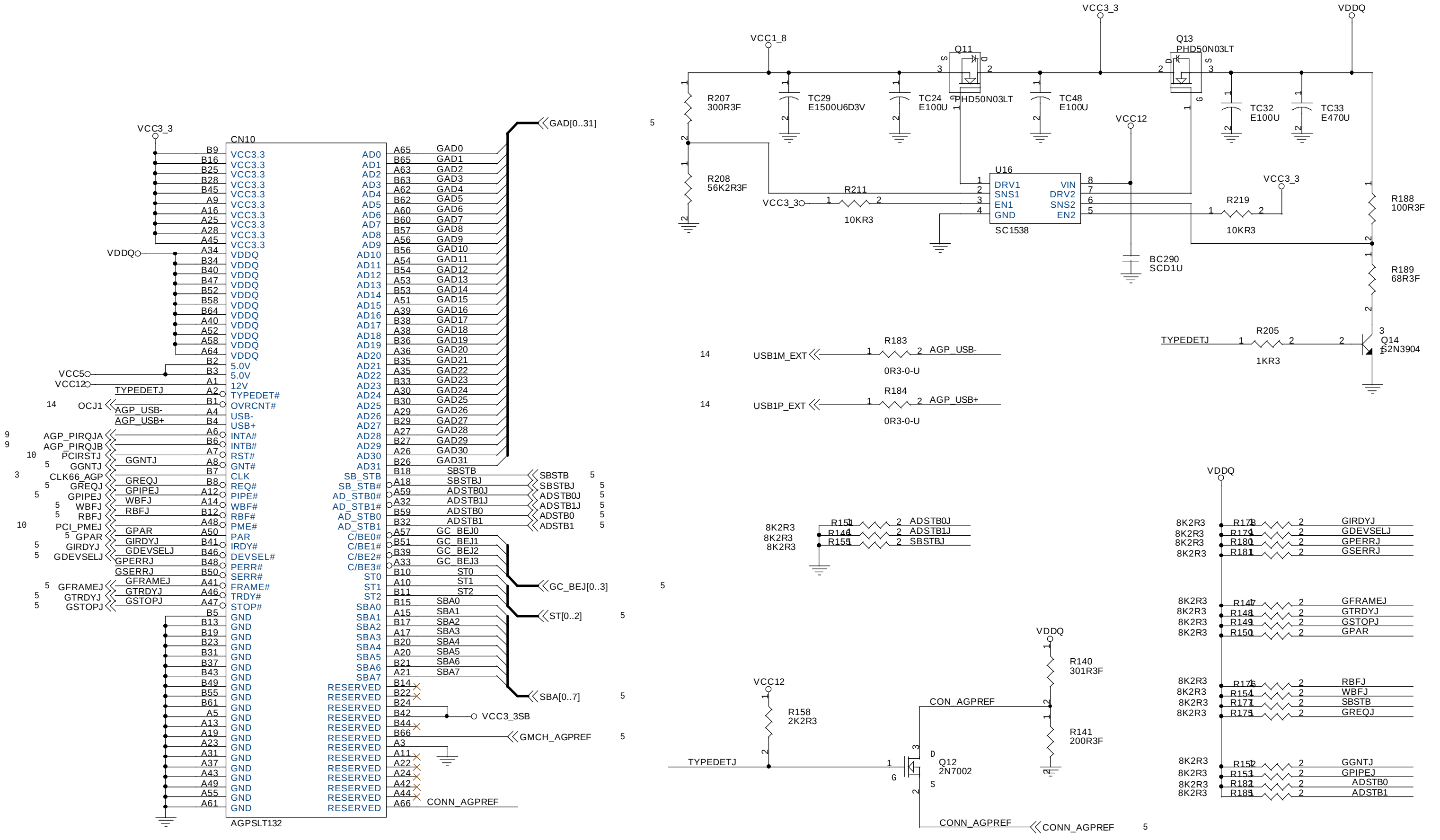


ACER_AI_DESKTOP

Title
DIMM #3 (7.SCH)

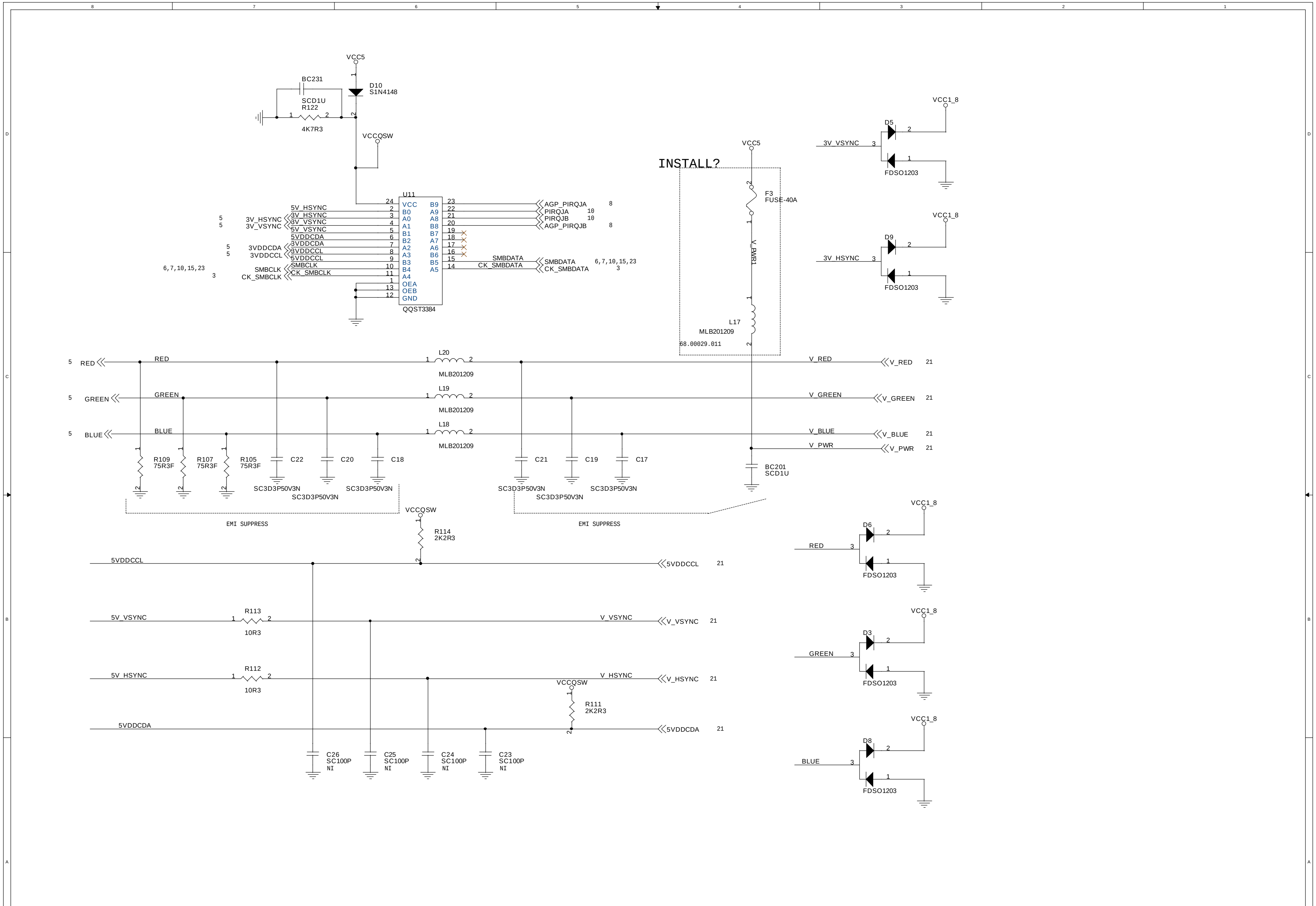
Size	Document Number	Rev
B	S58M	1

Date: Tuesday, June 20, 2000 Sheet 7 of 29



ACER_AI_DESKTOP

Title	AGP SLOT (8.SCH)		
Size B	Document Number S58M		Rev 1
Date:	Tuesday, June 20, 2000	Sheet 8	of 29

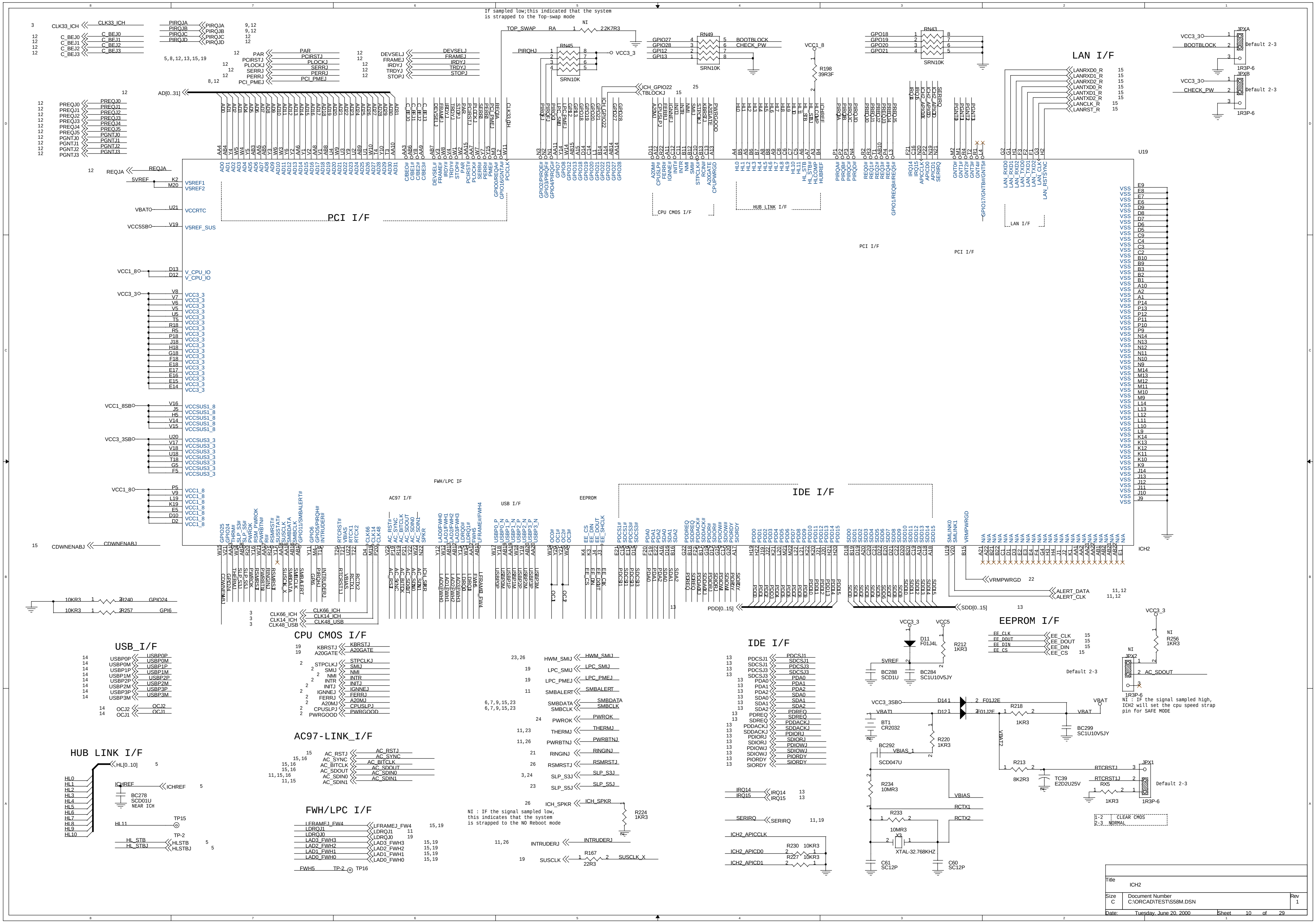


ACER_AI_DESKTOP

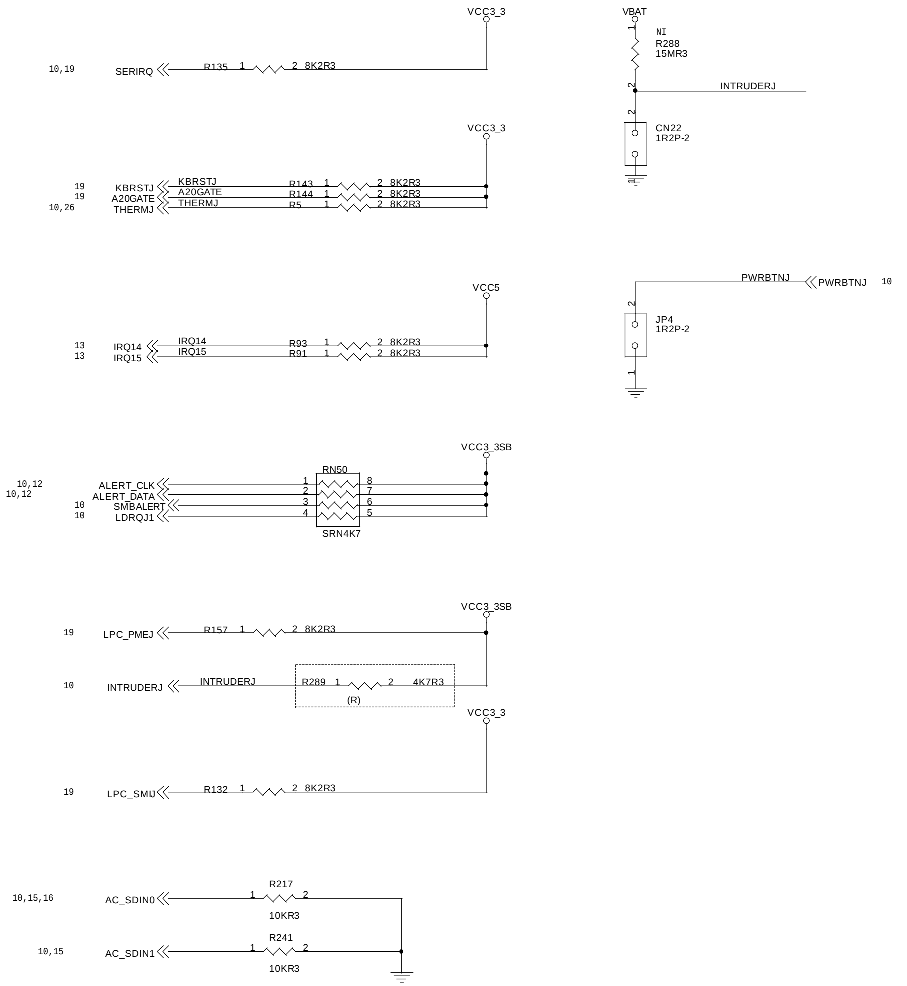
Title: VIDEO CONNECTOR AND SIGNALS(9.SCH)

Size: B | Document Number: S58M | Rev: 1

Date: Tuesday, June 20, 2000 | Sheet 9 of 29



If sampled low;this indicated that the system is strapped to the Top-swap mode
PCI I/F
LAN I/F
CPU CMOS I/F
HUB LINK I/F
IDE I/F
FWH/LPC IF
AC97 I/F
USB I/F
EEPROM
USB_I/F
CPU CMOS I/F
AC97-LINK_I/F
HUB LINK I/F
FWH/LPC I/F
IDE I/F
EEPROM I/F
NI : IF the signal sampled low, this indicates that the system is strapped to the NO Reboot mode
NI : IF the signal sampled high, ICH2 will set the cpu speed strap pin for SAFE MODE
Default 2-3
1-2 CLEAR CMOS
2-3 NORMAL
U19
ICH2
Title
ICH2
Size
C
Document Number
C:\ORCAD\TEST\S58M.DSN
Rev
1
Date: Tuesday, June 20, 2000
Sheet 10 of 29

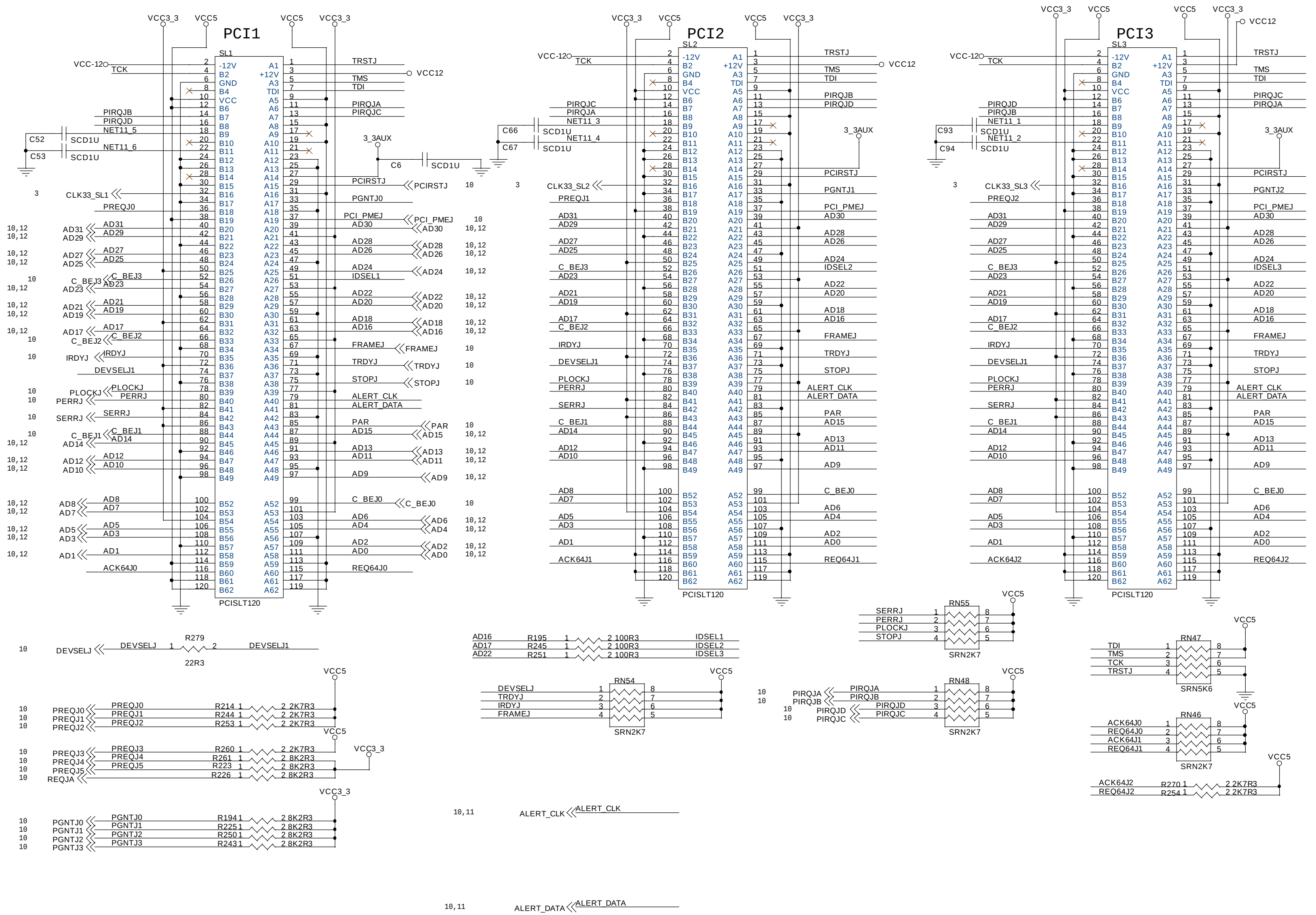


ACER_AI_DESKTOP

Title: PULLUP ON ICH, (11.SCH)

Size	Document Number	Rev
B	S58M	1

Date: Tuesday, June 20, 2000 Sheet 11 of 29



ACER_AI_DESKTOP

Title	PCI SLOT*3 (12.SCH)
Size: B	Document Number: S58M
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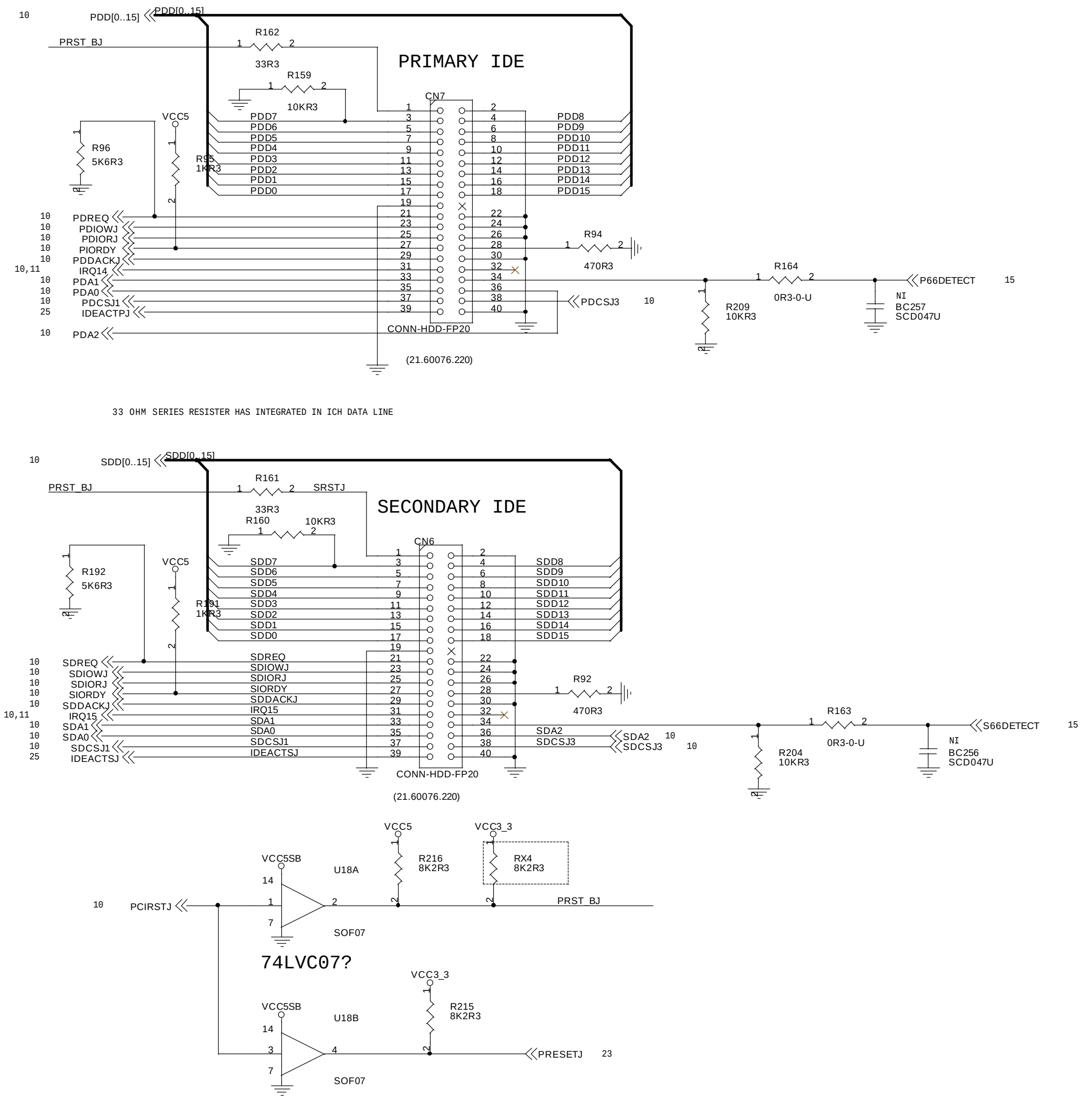
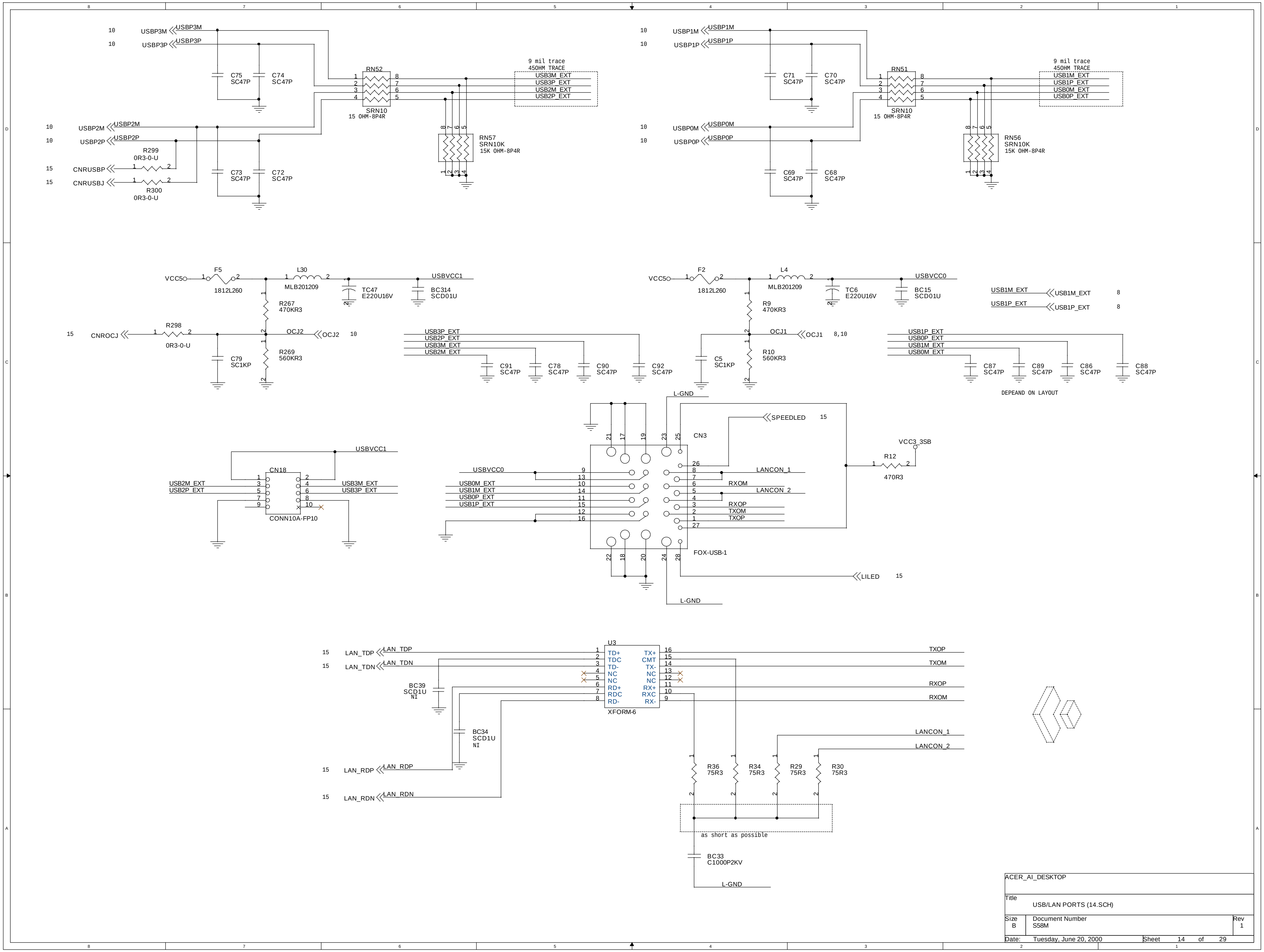
Date:	Tuesday, June 20, 2000
Sheet	12 of 29



ACER_AI_DESKTOP		
Title: IDE CONNECTORS (13.SCH)		
Size: B	Document Number: S58M	Rev: 1
Date: Tuesday, June 20, 2000	Sheet 13 of 29	

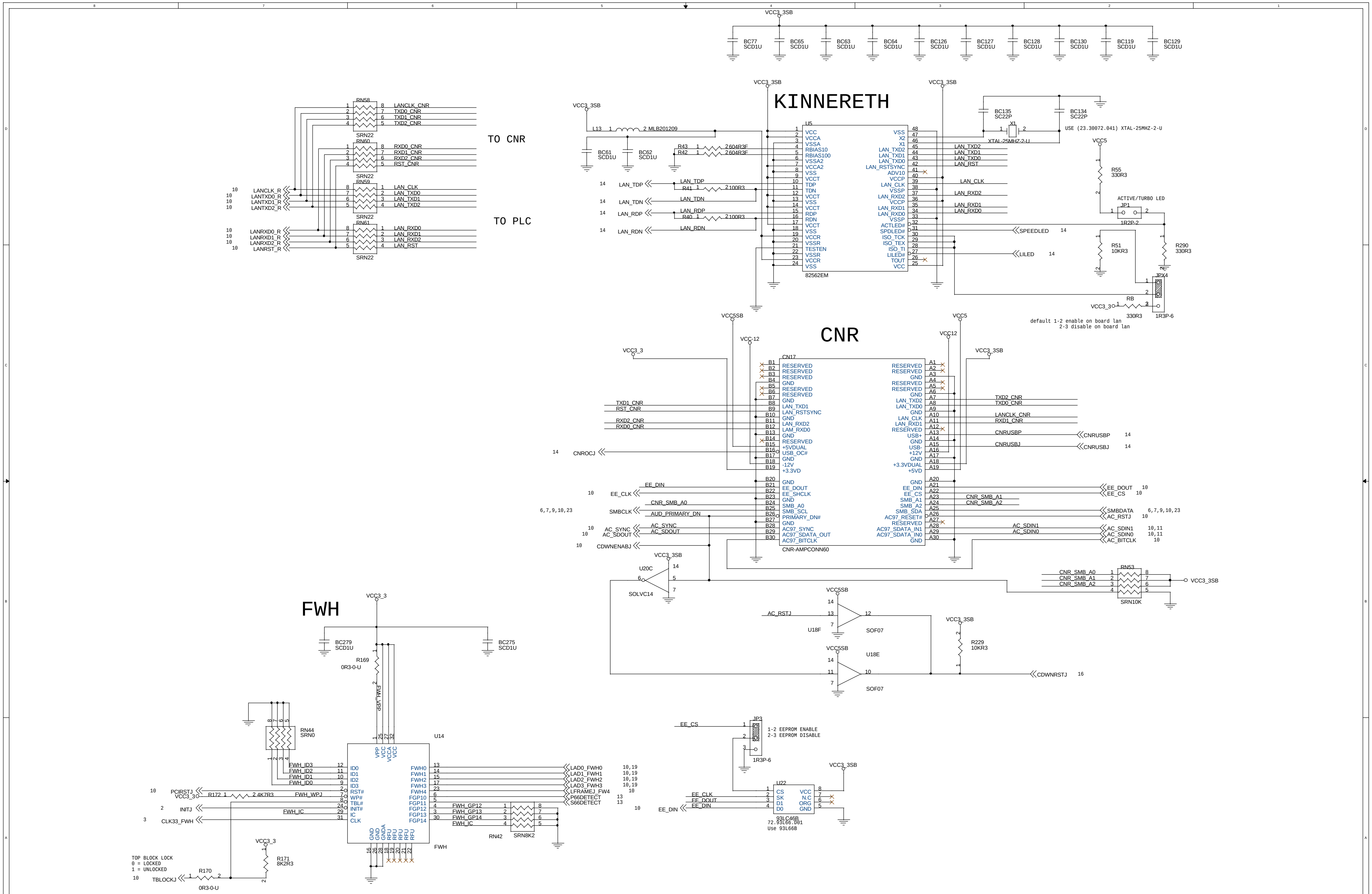


ACER_AI_DESKTOP

Title
USB/LAN PORTS (14.SCH)

Size	Document Number	Rev
B	S58M	1

Date: Tuesday, June 20, 2000 Sheet 14 of 29

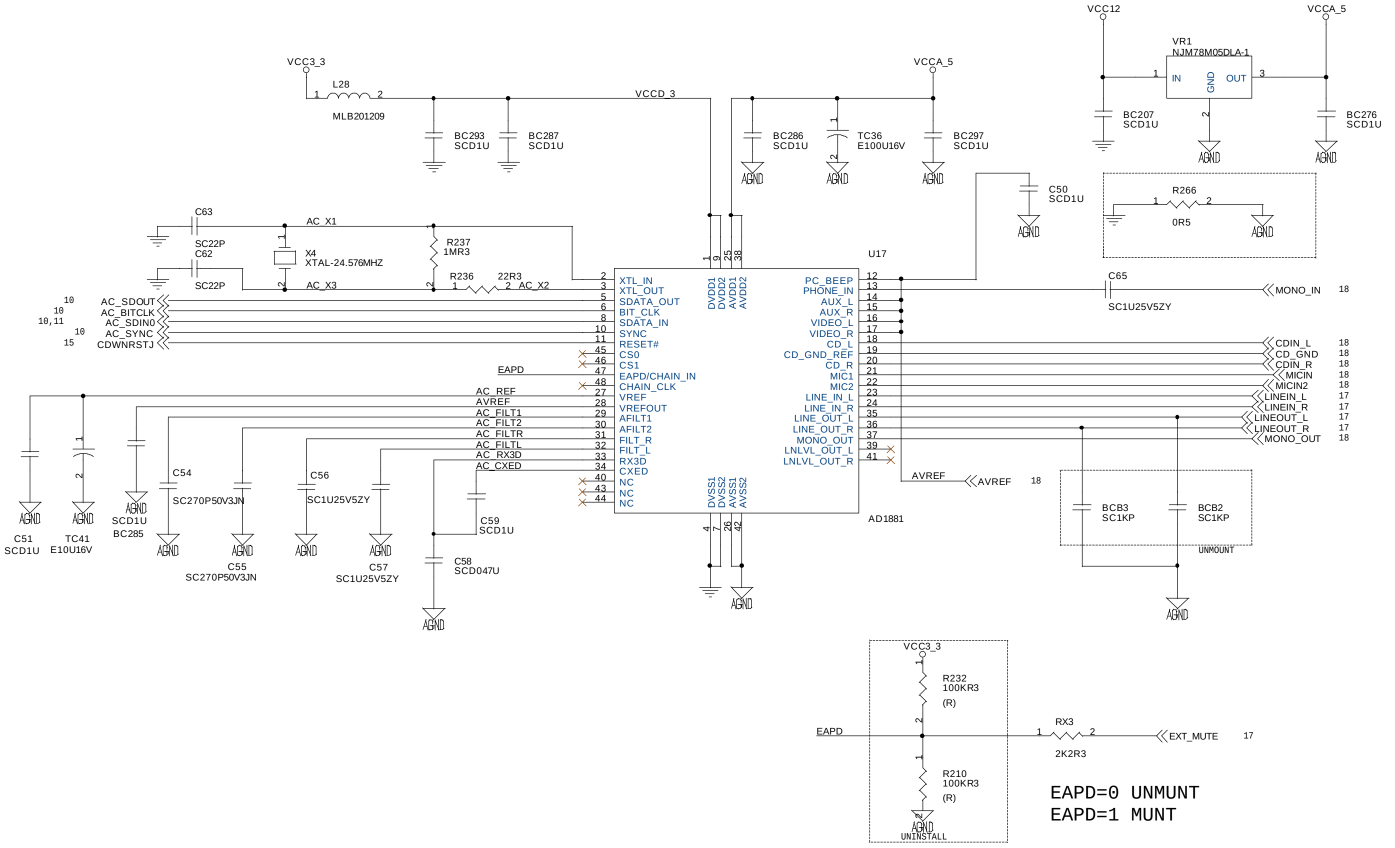


ACER_AI_DESKTOP

Title: FIRMWARE HUB, (15.SCH)

Size	Document Number	Rev
C	S58M	1

Date: Tuesday, June 20, 2000 | Sheet 15 of 29

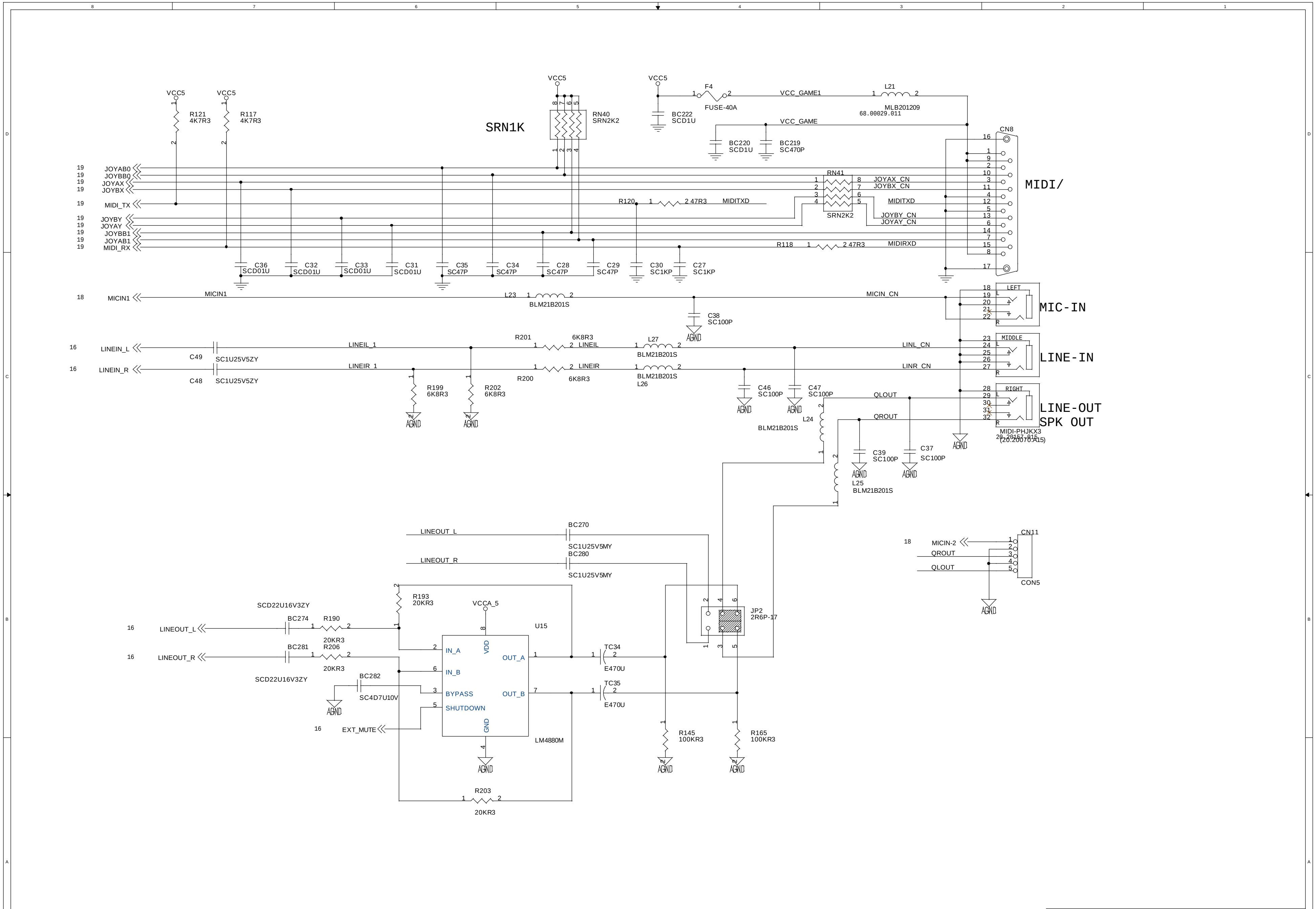


ACER_AI_DESKTOP

Title: AC97 CODEC (16.SCH)

Size: B | Document Number: S58M | Rev: 1

Date: Tuesday, June 20, 2000 | Sheet 16 of 29

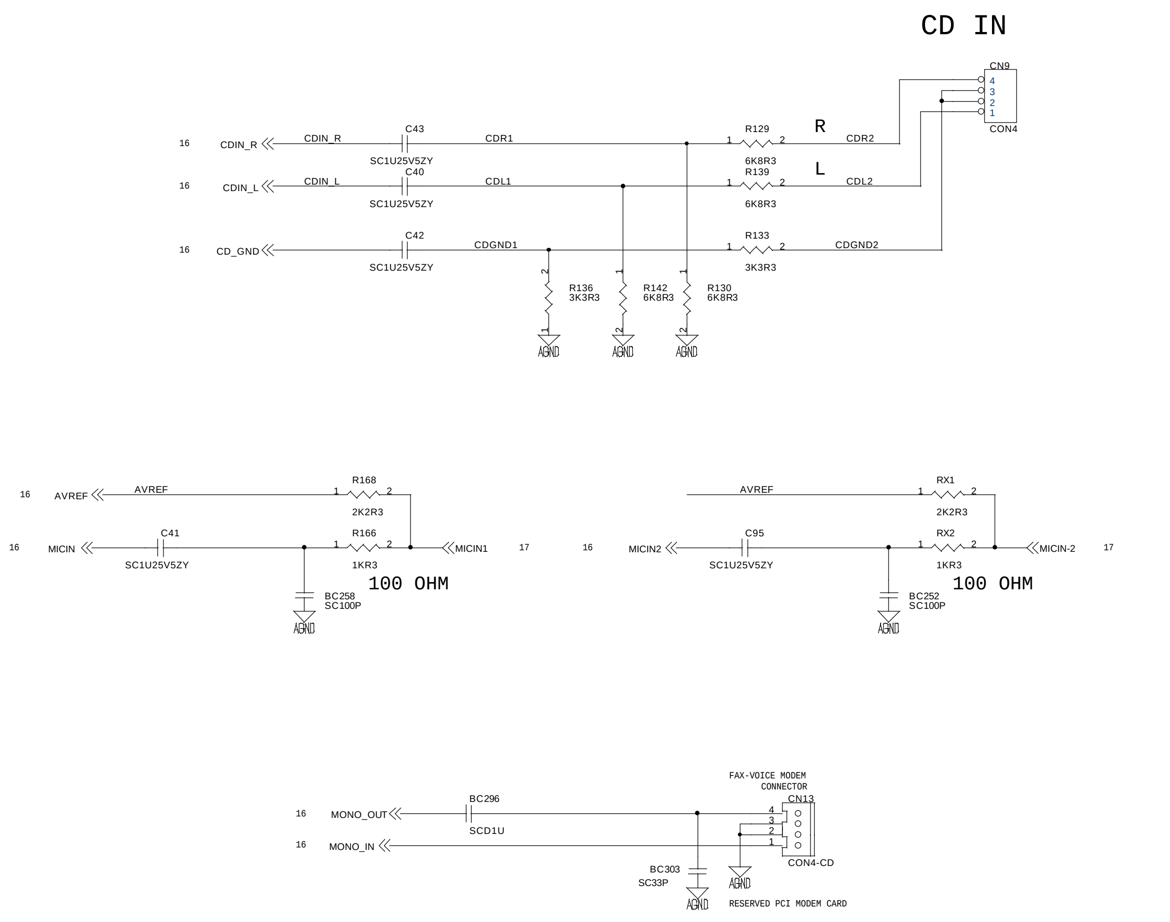


ACER_AI_DESKTOP

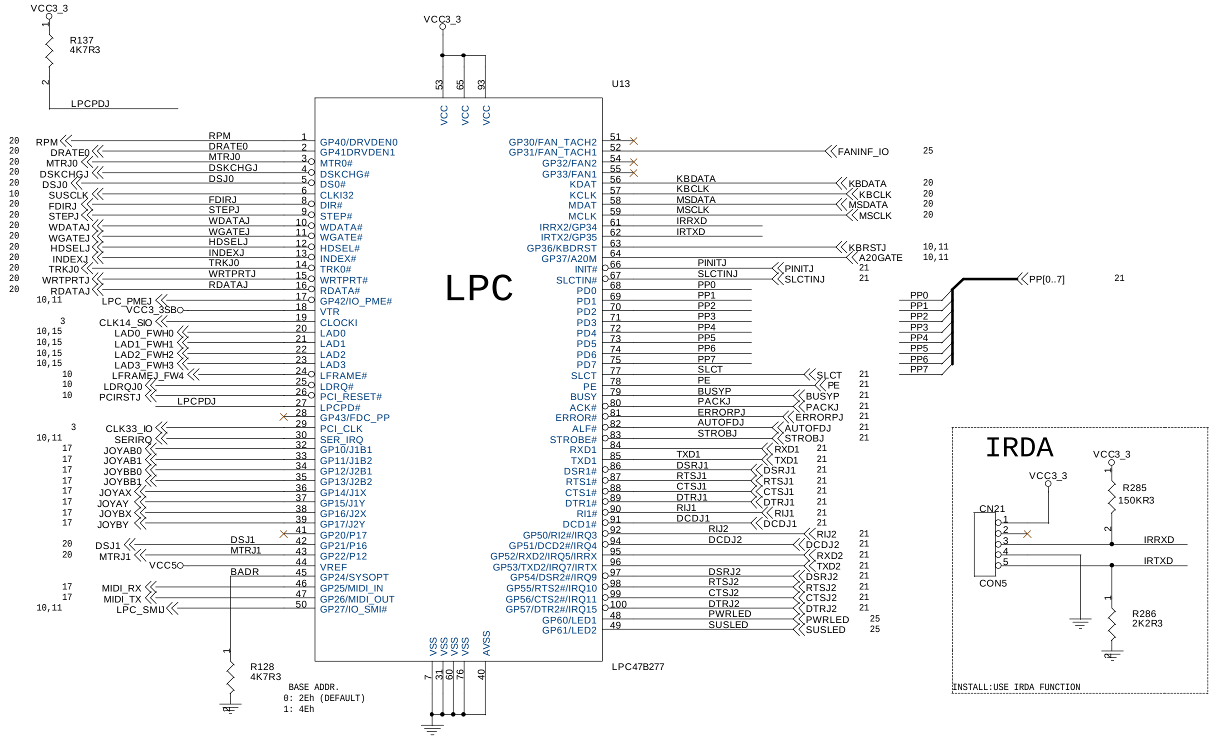
Title: AUDIO I/O & GAME PORT (17.SCH)

Size	Document Number	Rev
B	S58M	1

Date: Tuesday, June 20, 2000 — Sheet 17 of 29



ACER_AI_DESKTOP	
Title	AUDIO CONNECTORS (18.SCH)
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 18 of 29



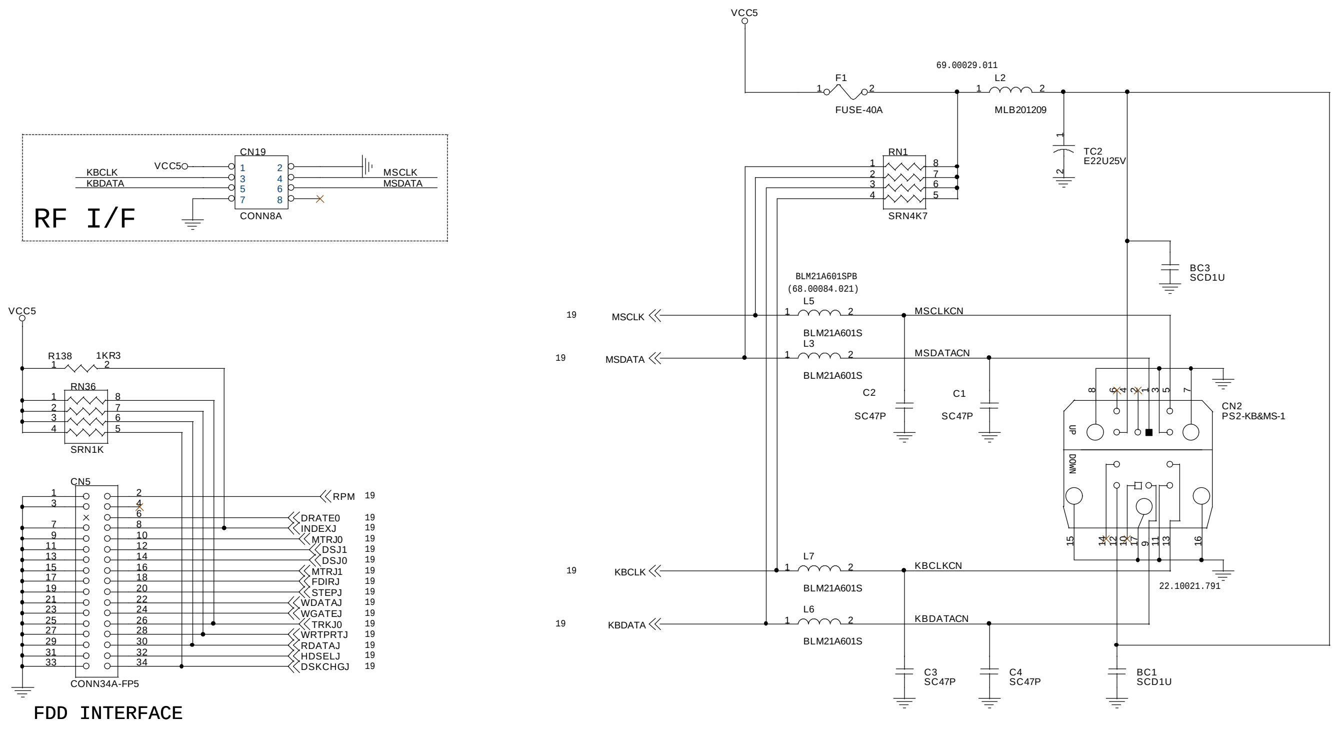
ACER_AI_DESKTOP

Title

LPC SUPER IO (19.SCH)

Size	Document Number	Rev
B	S58M	1

Date: Tuesday, June 20, 2000 Sheet 19 of 29



ACER_AI_DESKTOP

Title	PS/2 KB & MS & FDD (20.SCH)	
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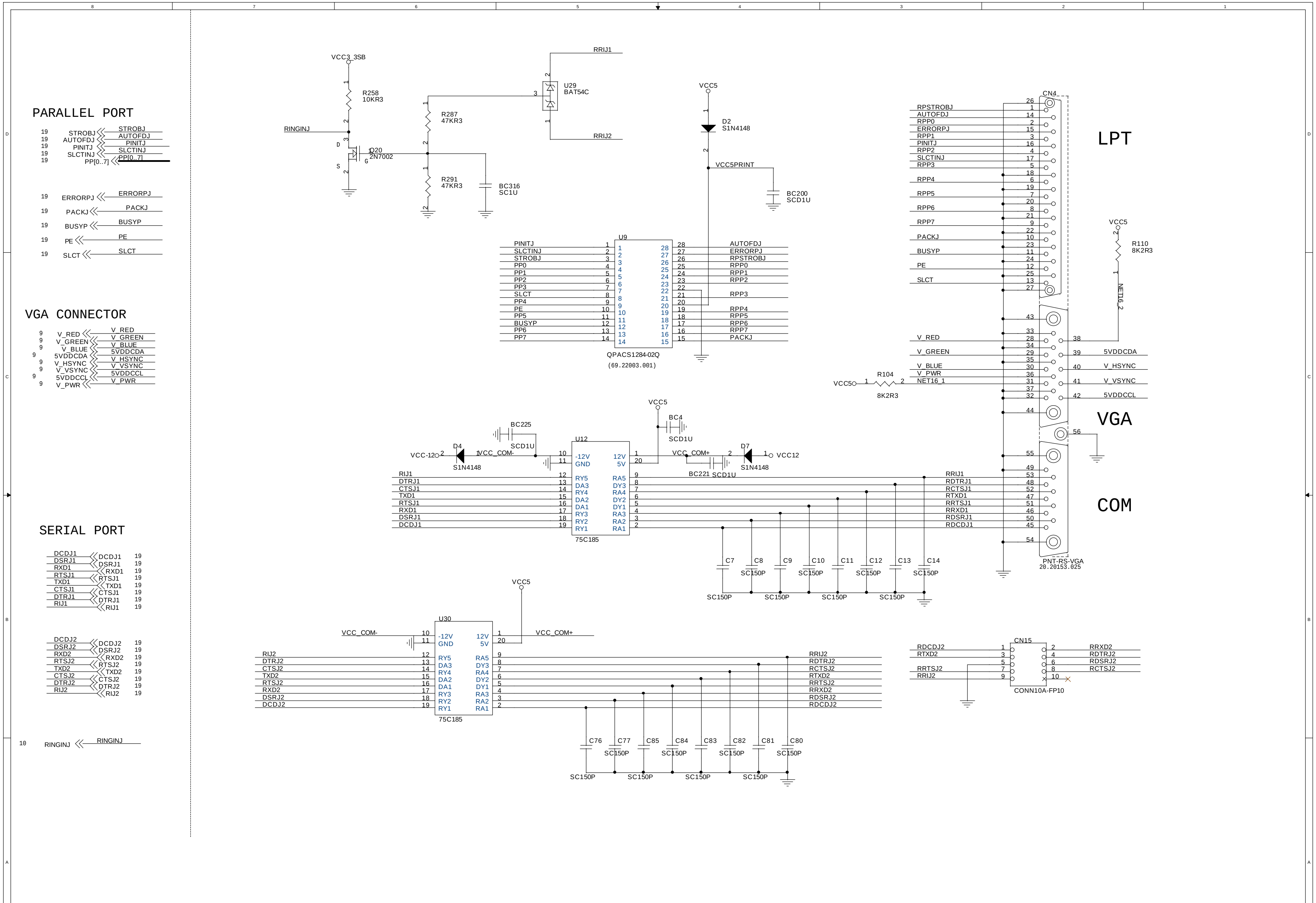
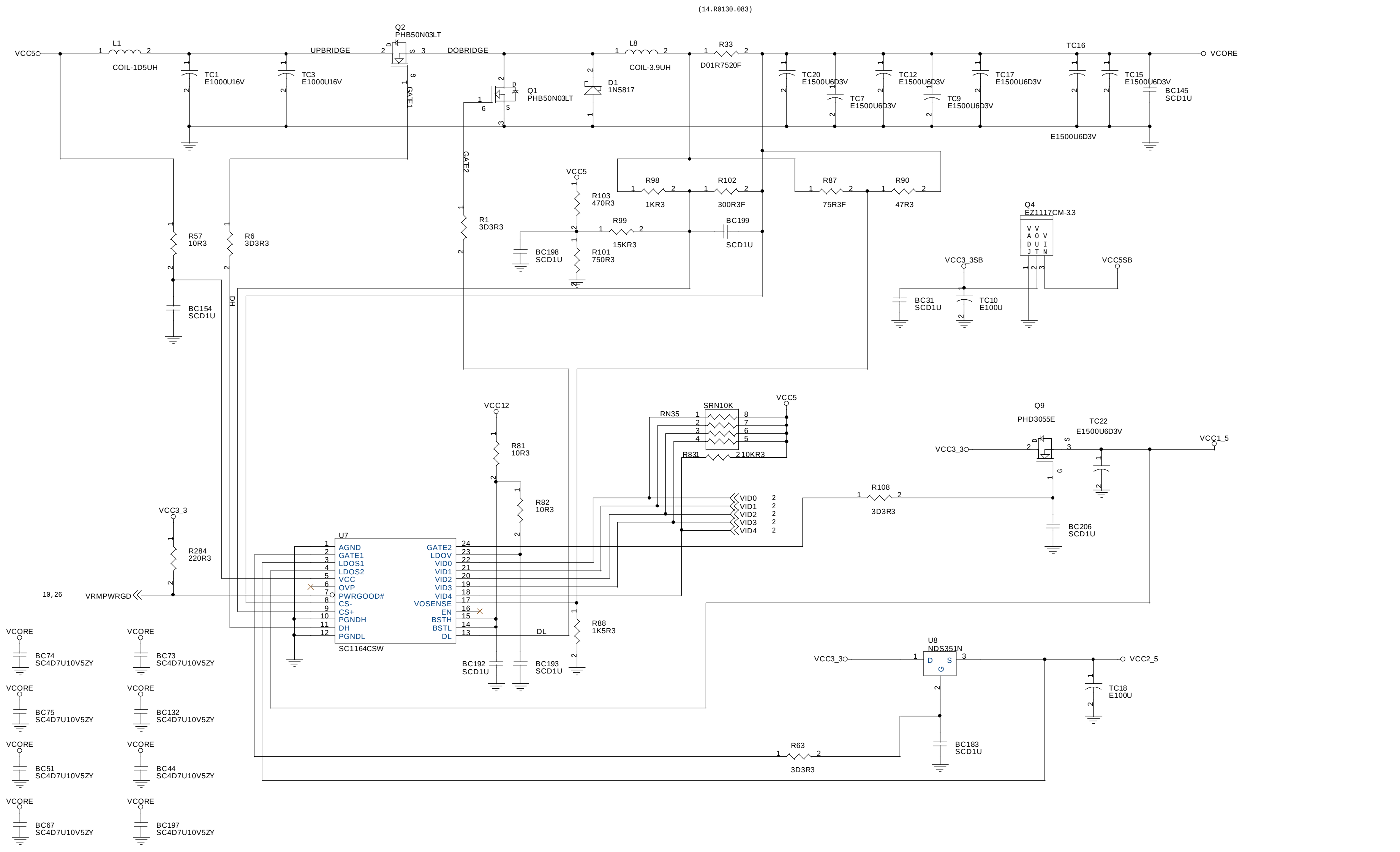
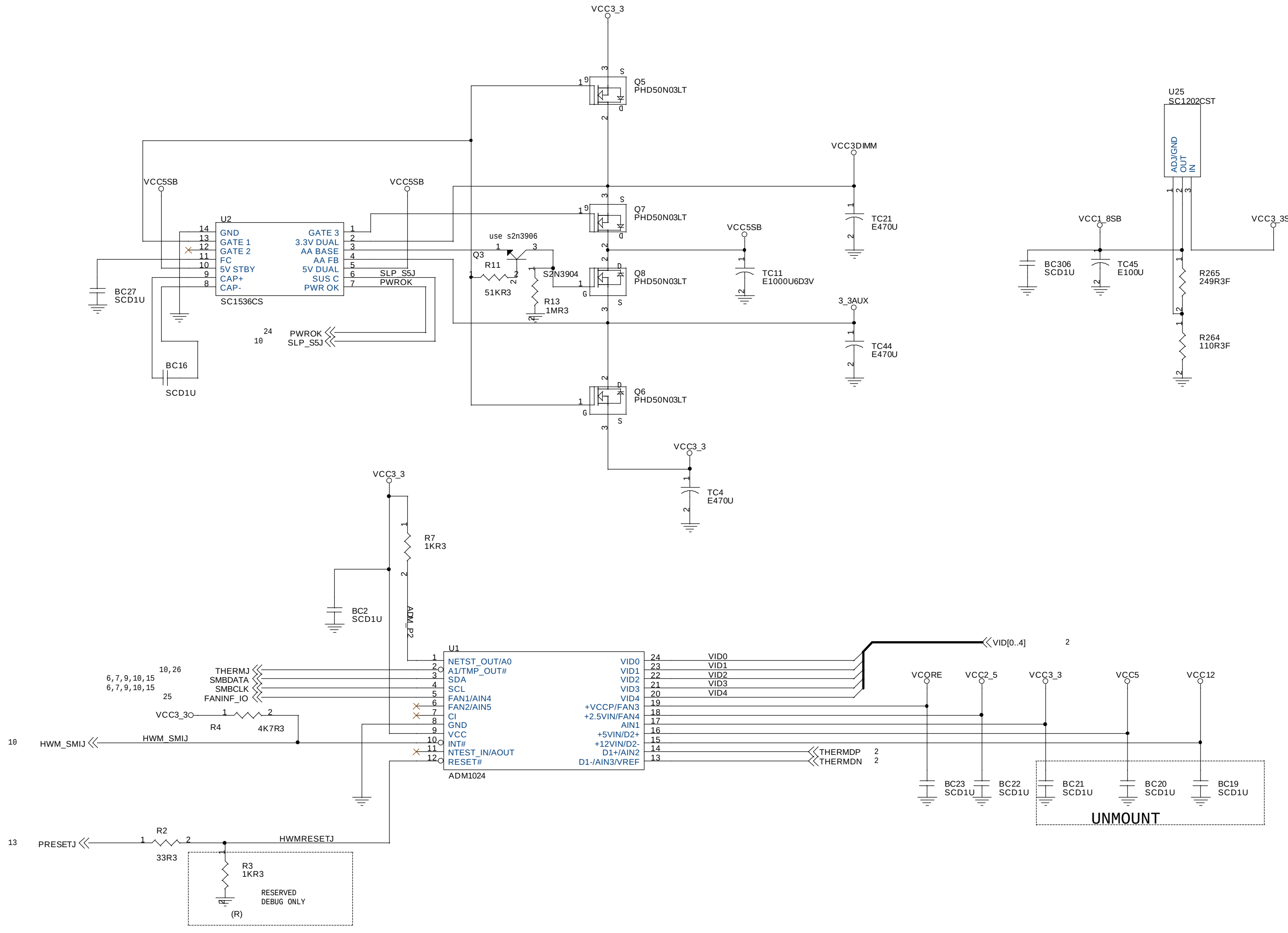


ACER_AI_DESKTOP

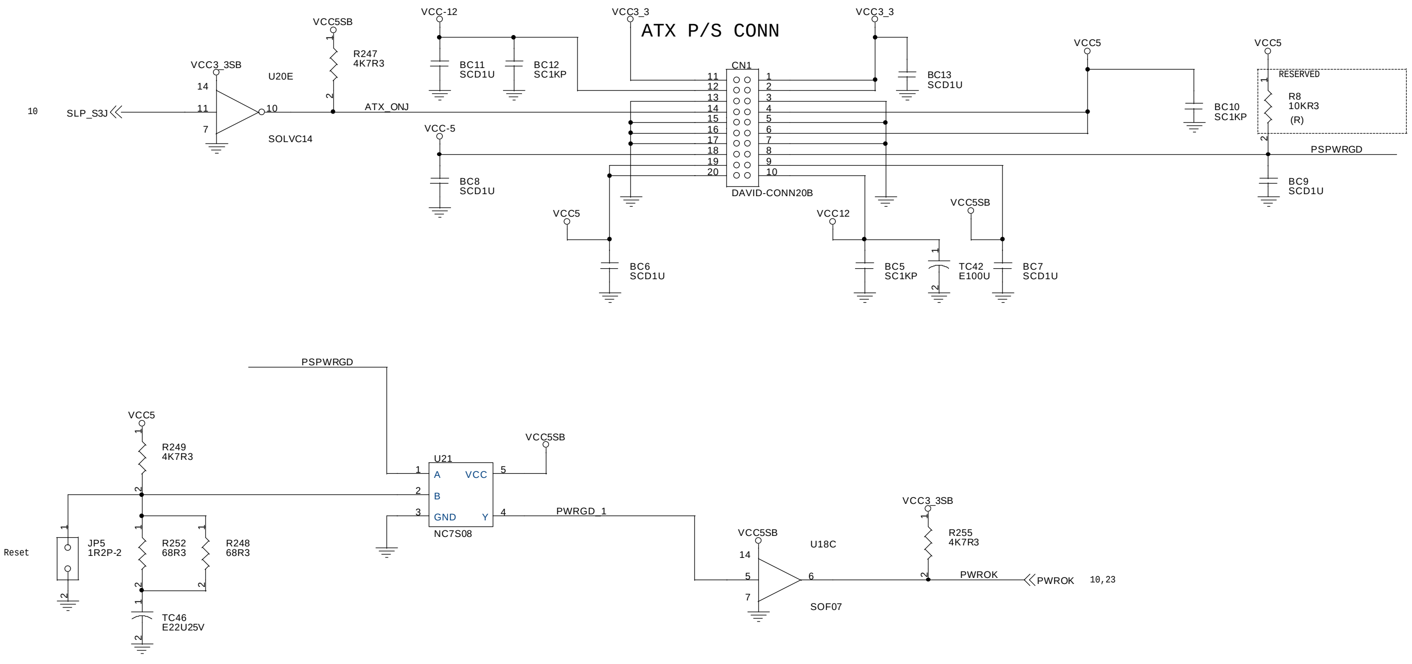
Title	Printer,COM,VGA (21.SCH)	
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 21 of 29	



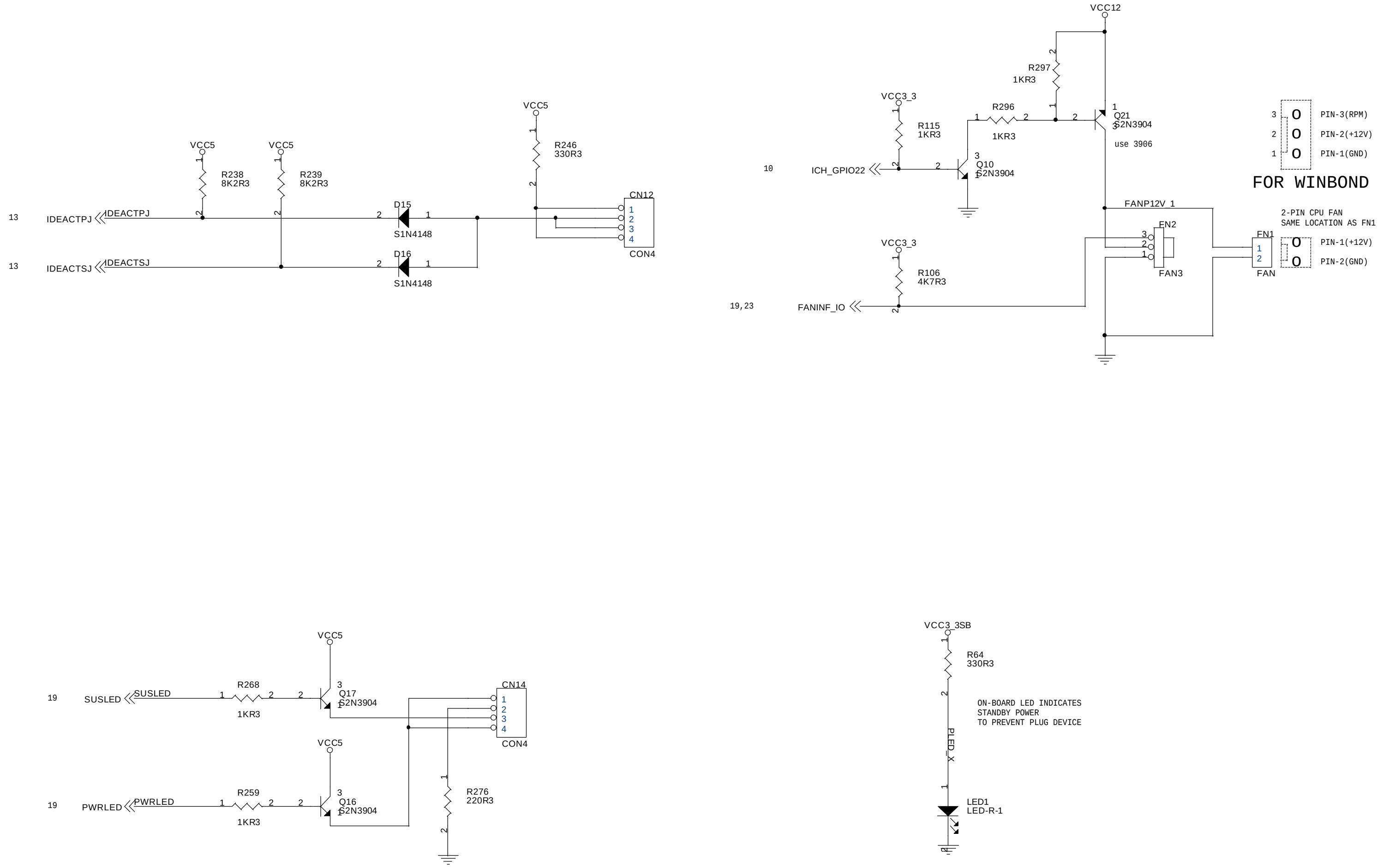
ACER DESKTOP SYSTEM		
Title	DC-DC CONVERTER	
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 22 of 29	



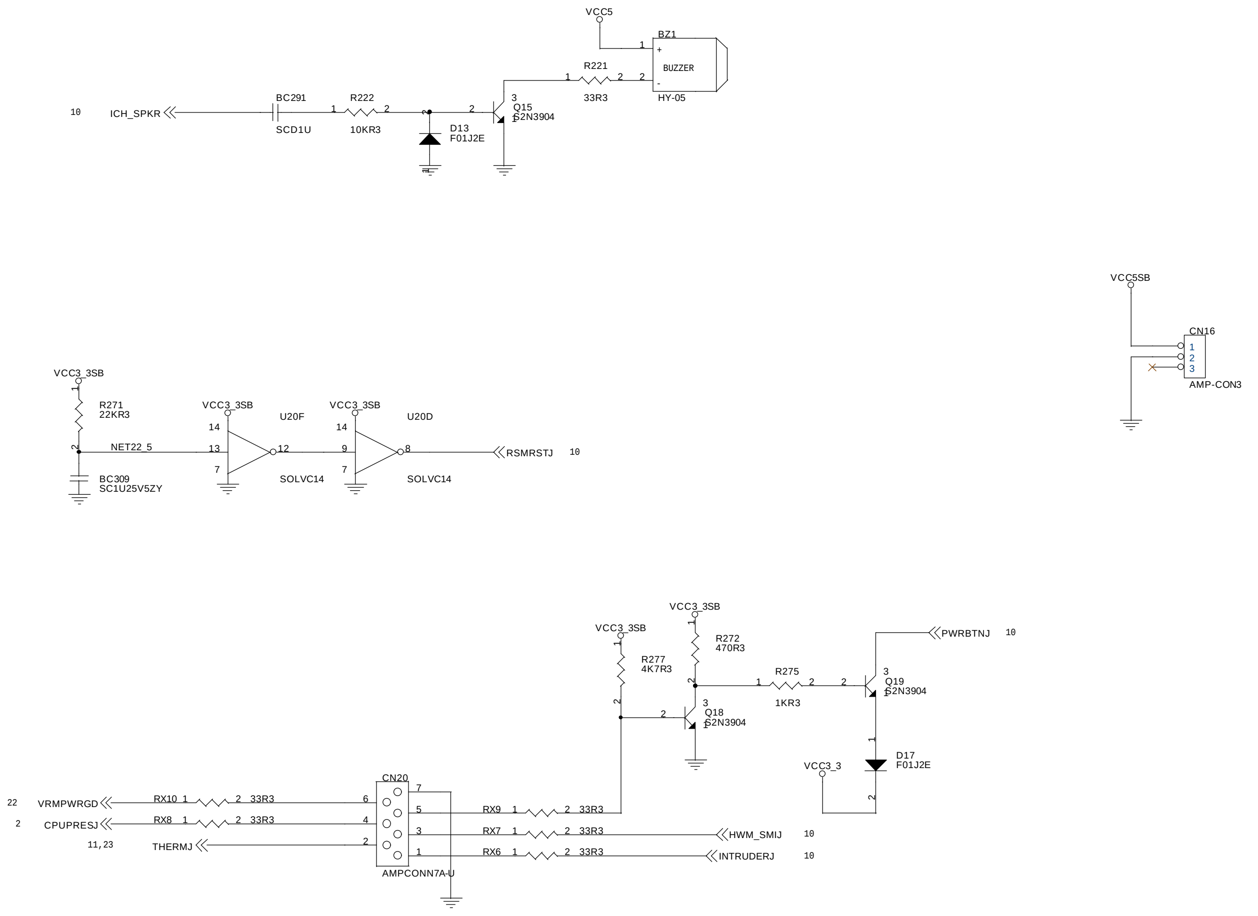
ACER_AI_DESKTOP			
Title	HARDWARE MONITOR		
Size B	Document Number S58M		Rev 1
Date:	Tuesday, June 20, 2000	Sheet 23 of 29	



ACER_AI_DESKTOP		
Title: ATX POWER CONNECTOR (24.SCH)		
Size: B	Document Number: V67M__98138-1A	Rev: 1
Date: Tuesday, June 20, 2000	Sheet 24 of 29	

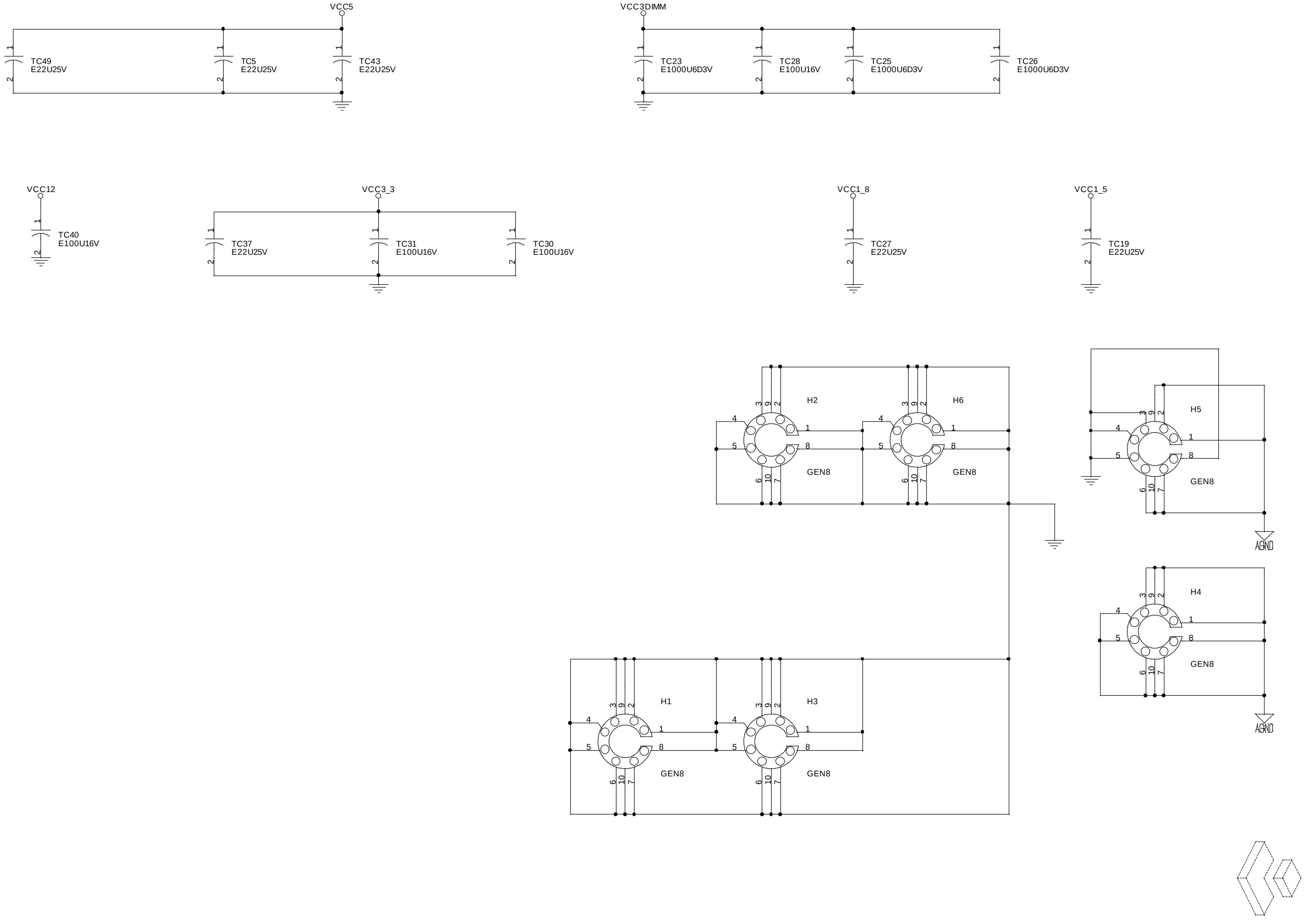


ACER_AI_DESKTOP		
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 25 of 29	

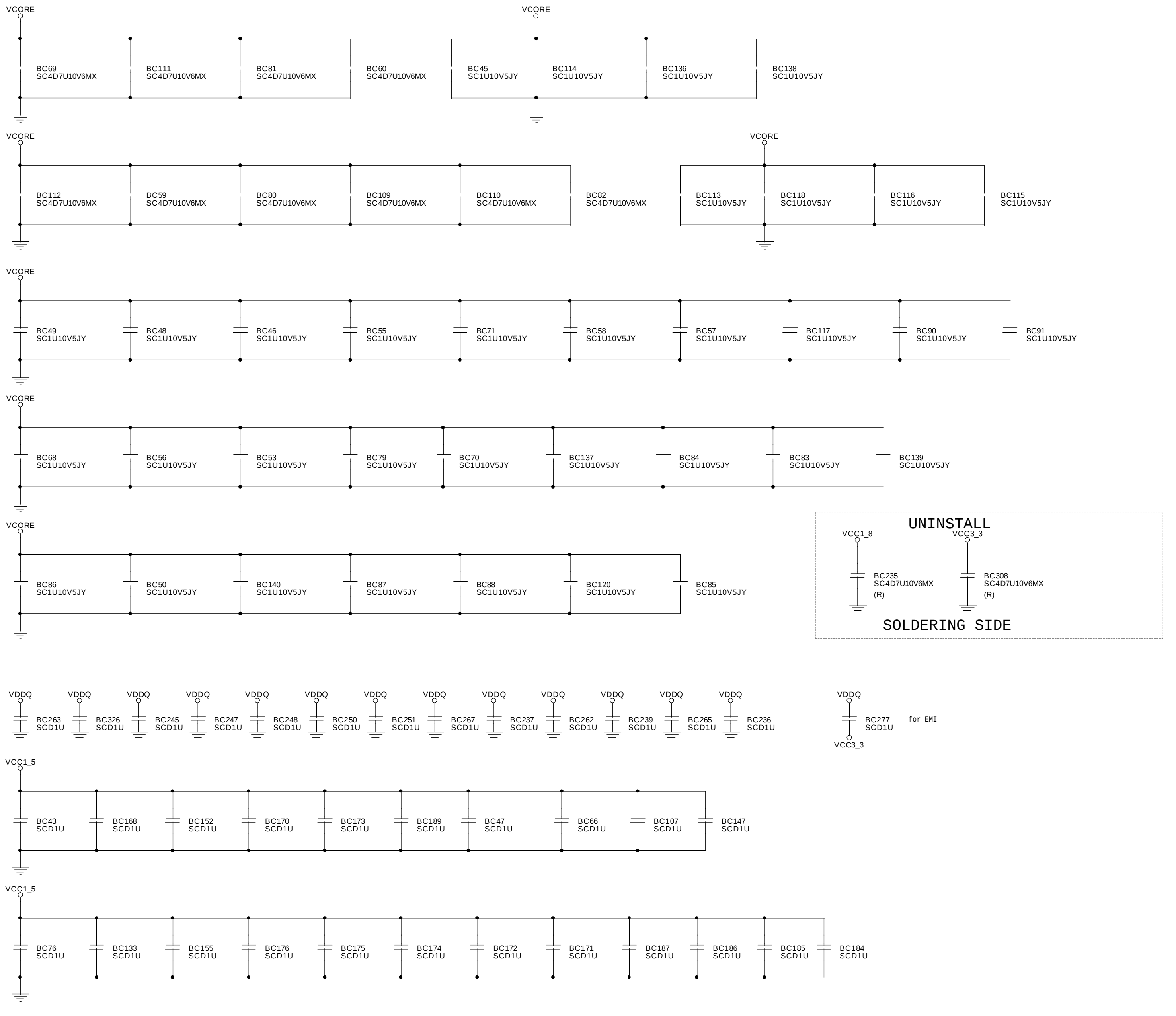


ALERT-ON LAN

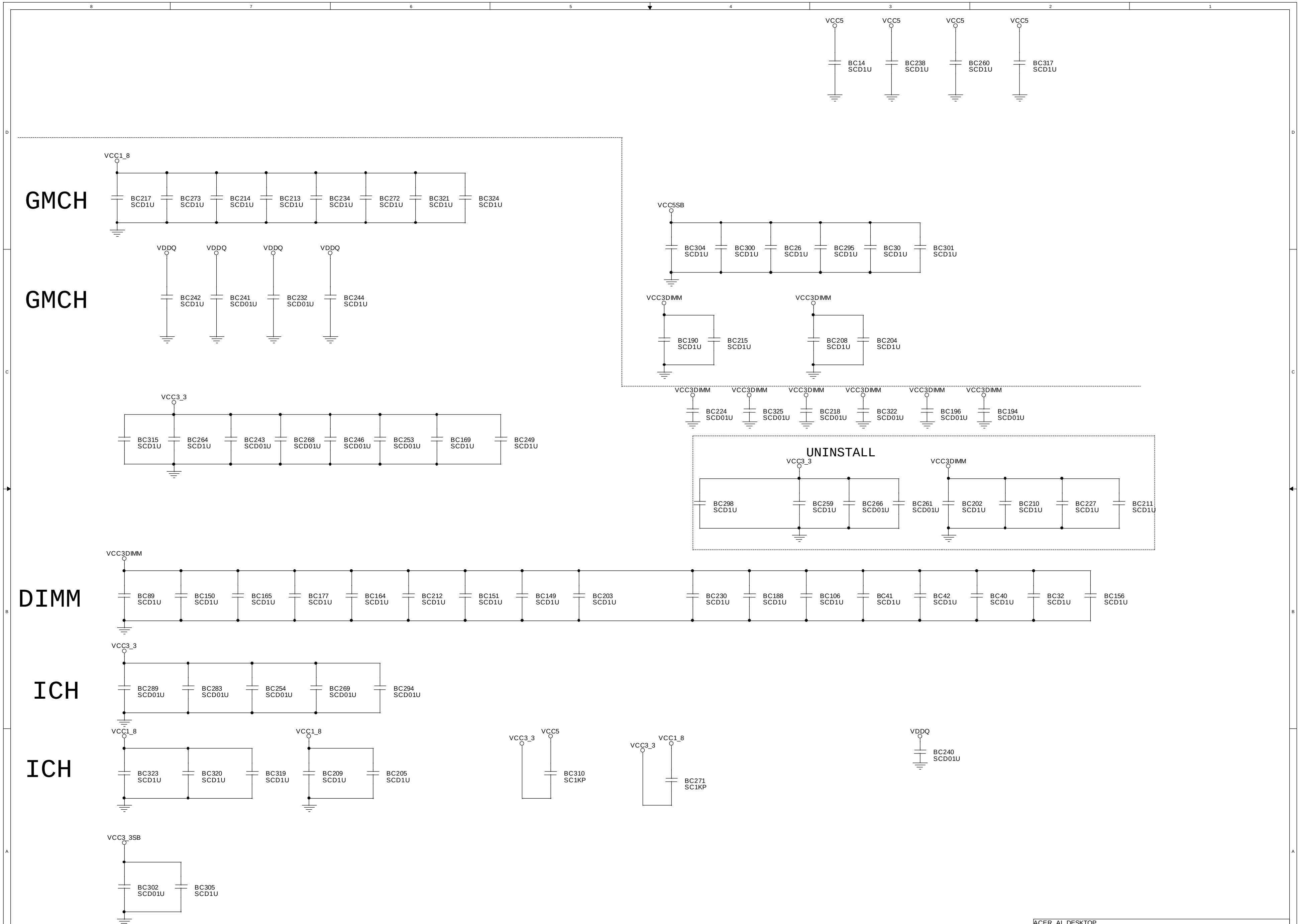
ACER_AI_DESKTOP		
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 26 of 29	



ACER_AI_DESKTOP		
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 27 of 29	



ACER_AI_DESKTOP		
Size B	Document Number S58M	Rev 1
Date: Tuesday, June 20, 2000	Sheet 28 of 29	



ACER_AI_DESKTOP

Size B | Document Number S58M | Rev 1

Date: Tuesday, June 20, 2000 | Sheet 29 of 29