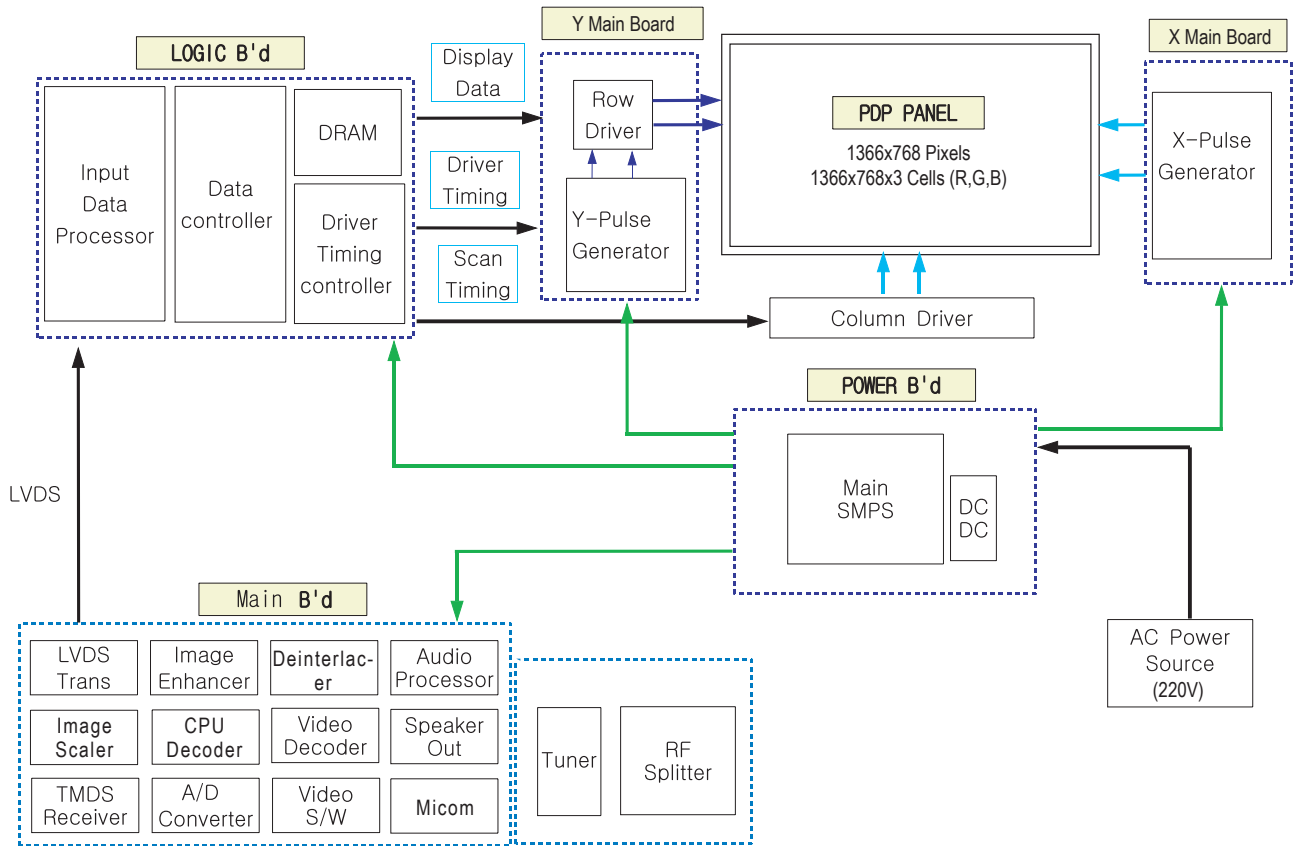


7. Block Diagram

7-1 Overall Block Diagram

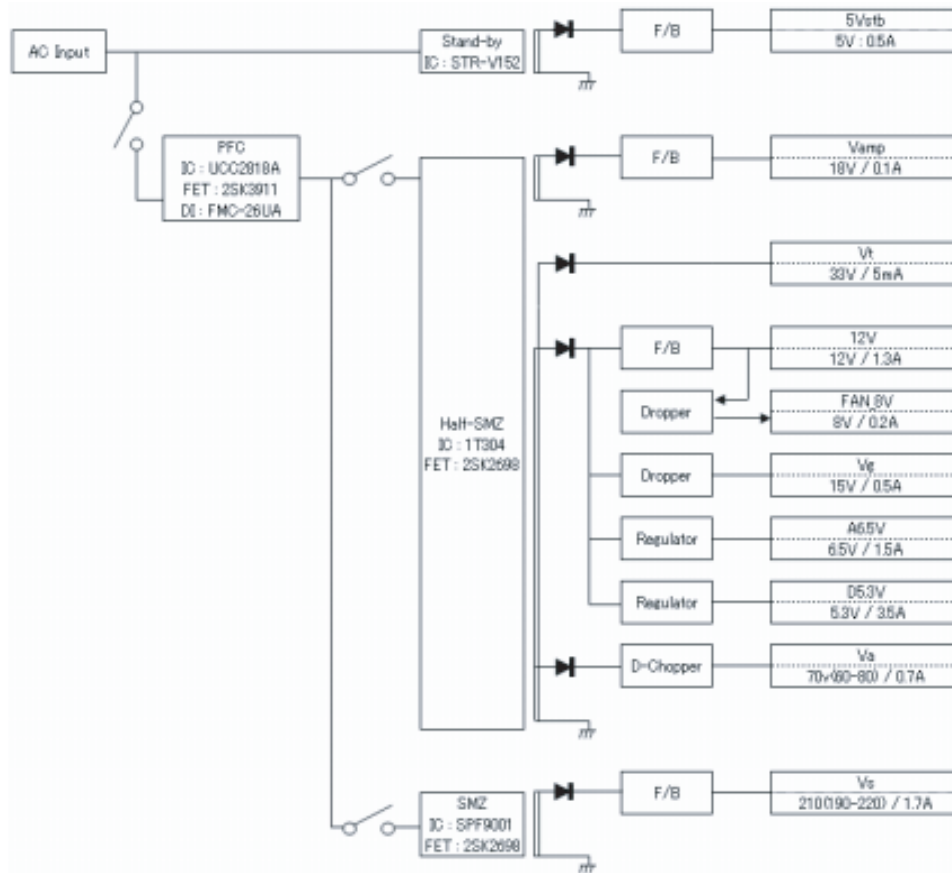


CN804-1(Main SMPS) ↔ CN102(Main B'D)		CN803(Main SMPS) ↔ CN101(Main B'D)		CN810(Main SMPS) ↔ CN2005(Logic B'D)		CN2(DC-DC SMPS) ↔ CN5007(Y B'D)		CN4(DC-DC SMPS) ↔ CN4000(X B'D)	
Pin No.	Signal	Pin No.	Signal	Pin No.	Signal	Pin No.	Signal	Pin No.	Signal
1	D5.3V	1	6.5V	1	D5.3V	1	D5.3V	1	D5.3V
2	RTN	2	RTN	2	D5.3V	2	Vg	2	Vg
3	VCS	3	A12V	3	RTN	3	RTN	3	RTN
4	VCA	4	RTN	4	RTN	4	Vscan	4	RTN
5	RTN	5	18V-AMP	5	D5.3V	5	RTN	5	Ve
6	RTN	6	18V-AMP	6	RTN	6	Vset	6	RTN
7	D12V	7	RTN-AMP	7	PS_ON	7	RTN	7	RTN
8	PS_ON	8	RTN-AMP	8	N.C	8	RTN	8	Vs
9	RTN	9	VT	9	VS_ON	9	Vs	9	Vs
10	STB-5V	10	RTN	10	STB-5V	10	Vs		
11	FAN_ON								
12	FAN_D								

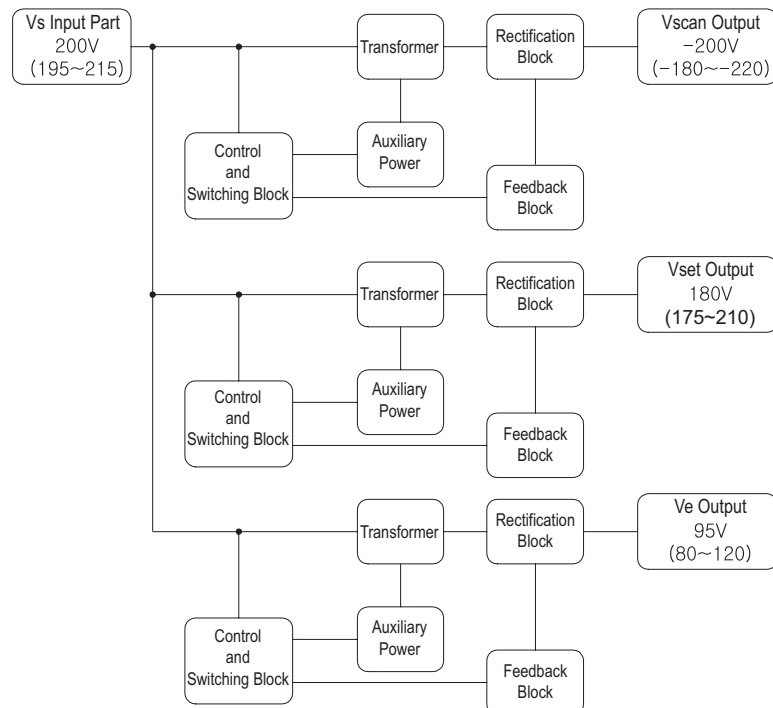
CN5(DC-DC SMPS) ↔ CN2710(F-Buffer)	
Pin No.	Signal
1	RTN
2	N.C
3	D5.3V
4	N.C
5	Va

7-2 Partial Block Diagram

7-2-1 Main SMPS Block Diagram

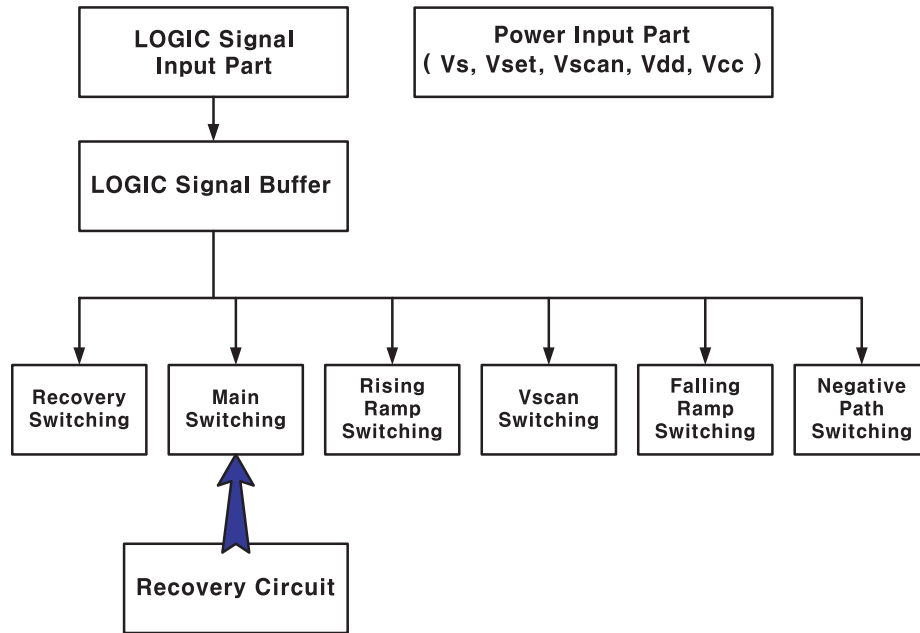


7-2-2 DC-DC SMPS Block Diagram

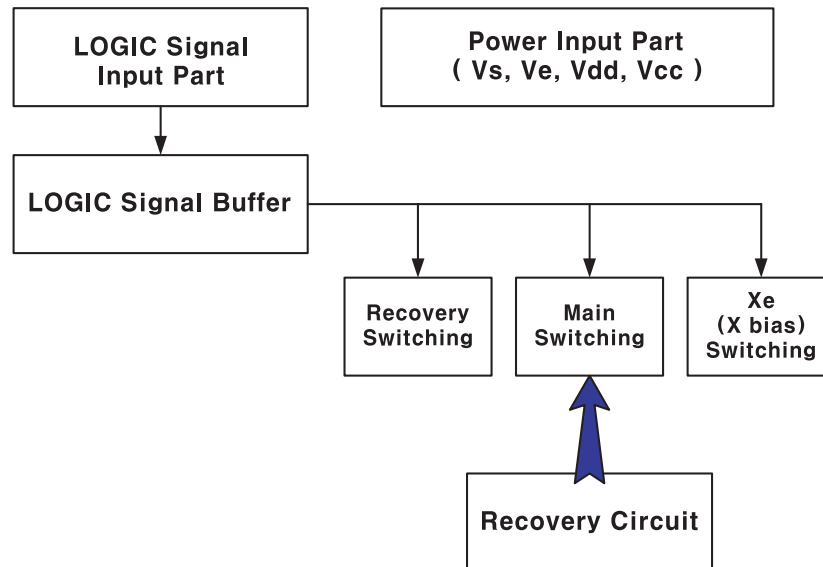


7-2-3 Module Driver Board Block Diagram

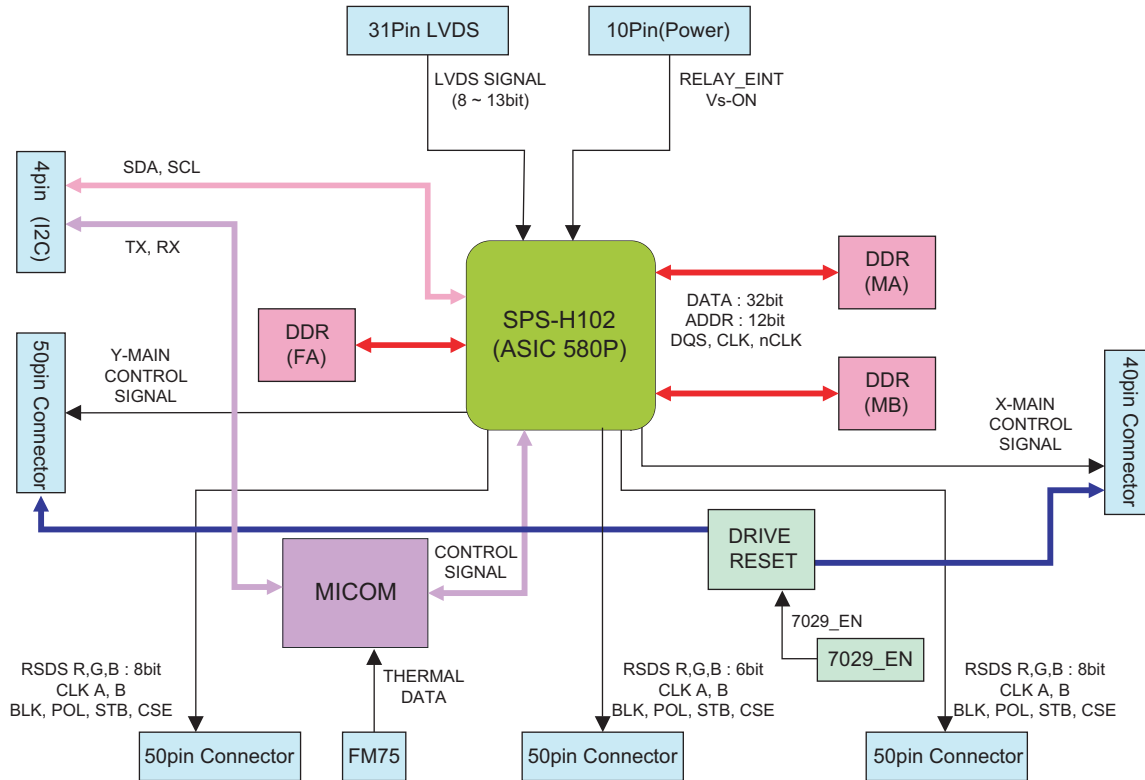
1. Y Main Board



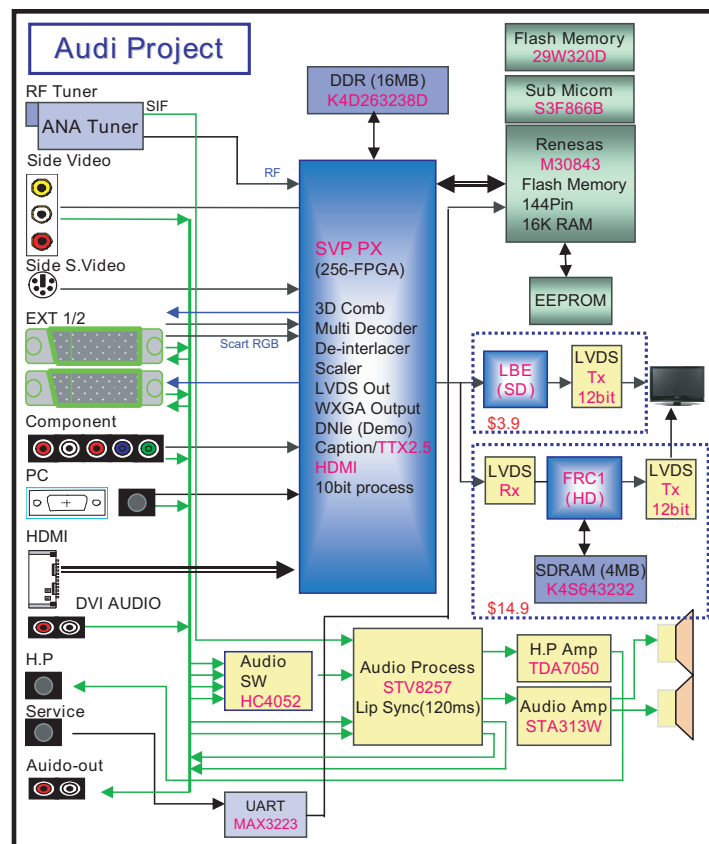
2. X Main Board



7-2-4 Logic Board Block Diagram



7-2-5 Main Board Block Diagram



MEMO