CAN TUNER

TU100 TDTC-G101D (LG Tuner)

I2C Address
R:0xC4
W:0xC5

DIF1	11	CAN_IF_N
DIF2	10	CAN_IF_P
IF_AGC	9	IF_AGC_CAN
AS	8	ADD_SEL_TUN
NC	7	
B2	6	TUNER_SUPPLY
SDA	5	SDA_CAN_TUNER
SCL	4	SCL_CAN_TUNER
RF_AGC	3	RF_AGC_CAN
B1	2	33V_TUNER
ANT_PWR	1	ACT_ANT
	12	ANALOG_IF

33V_TUNER → F160 (NC) → C439 1u
TUNER_SUPPLY → F150 (NC) → S187 (NC) → ADD_SEL_TUN
33V_VCC → S186

5V_TUN → R887 1k → R878 10k → AGC_PWM
C871 100n
IF_AGC_EN/AGC_COMP
AGC_RF → R951 100k, C869 100n 16V, Q171 BC858B
3V3_DVBT_VDD → R876 10k
R886 1k, R877 10k, Q154 BC848B

IF_AGC_CAN → R462 10k → AGC_IF
C316 100n 16V
C1045 100n
Q148 BC848B
IF_AGC_EN/AGC_COMP
R859 1k → NC

1V25_VCC → F145 → 1V25_DVBT_VDD
C985 220u 6V3 SPCAP Option
Pin9: C334 100n 50V
Pin11: C365 100n 16V, C348 100n 16V
Pin31: C361 100n 16V

1V25_VCC → F146 → 1V25_DVBT_ADC
Pin48: C335 100n 50V, C370 100n 16V

3V3_VCC → F147 60R → 3V3_DVBT_ADC
Pin1: C336 100n 50V, C372 100n 16V

3V3_VCC → F148 60R → 3V3_DVBT_VDD
C989 220u 6V3 SPCAP Option
Pin28: C337 100n 50V, C364 100n 16V

1V8_VCC → F150 60R → 1V8_DVBC_VDD
C986 220u 6V3 SPCAP Option
Pin1&7: C350 10u 10V, C355 100n 16V
Pin61: C351 100n 16V

3V3_VCC → F153 60R → 3V3_DVBC_VDD
Pin60&55: C368 10u 10V, C352 100n 16V
Pin41: C332 100n 16V

DVB-T DEMODULATOR

U140 TDA10048

1V25_DVBT_ADC	48	1V2_ADC	
1V25_DVBT_VDD	9	1V2_XTAL	
	11	1V2_PLL	
	12	1V2_VDD_1	
	31	1V2_VDD_2	
	45	1V2_VDD_3	
	36	TRSTN	
	37	TMS	
	38	TDI	
	39	TCK	
	40	TDO	
	2	VIM	
	3	VIP	
	41	RST	
	33	SADDR	
	7	XTALIN	
	8	XTALOUT	
	32	GPIO0	
	42	AGC_IF	
	43	AGC_TUN	
	34	SDA R: 0x10 W: 0x11	
	35	SCL	
	15	SDA_TUN	
	16	SCL_TUN	
	46	3V3_VDD_2	3V3_DVBT_VDD
	28	3V3_VDD_1	
	5	3V3_ADC_2	3V3_DVBT_ADC
	1	3V3_ADC_1	
	4	GNDADC	
	6	GNDXTAL	
	10	GNDPLL	
	13	GND1	
	14	GND2	
	29	GND3	
	30	GND4	
	44	GND5	
	47	GND6	
	27	TSDATA7	
	26	TSDATA6	
	25	TSDATA5	
	24	TSDATA4	
	23	TSDATA3	
	22	TSDATA2	
	21	TSDATA1	
	20	TSDATA0	
	19	TSCLK	
	18	TSVALID	
	17	TSSYNC	

CAN_IF_N → C394 100n → R533 390R → C872 39p 50V, C873 39p 50V
CAN_IF_P → C393 100n → R532 390R
RESET_IC → R508 100R
5V_TUN → R860 1k8
C373 18p, X106 16MHz, R524 1M, C374 18p
IF_AGC_EN/AGC_COMP
AGC_IF → R509 47k
AGC_PWM
SDA → R506 100R, R505 100R
SCL → R504 100R, R980 100R
SDA_CAN_TUNER → R981 100R
SCL_CAN_TUNER
3V3_VCC → R559 4k7, R558 4k7

R493 56R, R495 56R, R496 56R, R497 56R, R494 56R, R492 56R: TS[0:10]

DVB-C DEMODULATOR

U136 TDA10024HN

3V3_DVBC_VDD	18	SCL_TUN	
	17	SDA_TUN	
SCL	15	SCL R: 0x18	
SDA	16	SDA W: 0x19	
AGC_IF	9	AGCTUNR: 0x1C	
NC	11	AGCIF W: 0x1D	
	25	TDO	
	20	TCK	
	21	TDI	
	24	TMS	
	23	TRSTN	
	19	ENSERI	
	2	XTALIN	
	3	XTALOUT	
	43	NC_1	
	46	NC_2	
	47	NC_3	
	48	NC_4	
	55	NC_5	
	56	NC_6	
	50	VIM	
	51	VIP	
	29	UNCOR	
	5	SACLK	
	6	TEST	
	10	IICDIV	
	12	ADDSEL	
	14	RST	
	30	TSSYNC	
	32	TSVALID	
	31	TSCLK	
	42	TSDATA0	
	41	TSDATA1	
	40	TSDATA2	
	39	TSDATA3	
	36	TSDATA4	
	35	TSDATA5	
	34	TSDATA6	
	33	TSDATA7	
	4	GND_XTAL	
	44	GND_PLL	
	52	GND_ADCD	
1V8_DVBC_VDD	1	1V8_XTAL	
	7	1V8_VDD_1	
	8	1V8_VDD_2	
	22	1V8_VDD_3	
	37	1V8_VDD_4	
	45	1V8_ADC	
	54	1V8_PLL	
	13	3V3_VDD_1	
	27	3V3_VDD_2	
3V3_DVBC_VDD	38	3V3_VDD_3	
	49	3V3_ADCA	
	53	3V3_ADCD	
	26	GPIO	
TP144	28	CTRL	

R866 3k3, R865 3k3, R471 100R, R470 100R, R486 47k
C347 18p, X105 16MHz, R501 1M, C325 18p
CAN_IF_N → S178 (NC) → C395 18p (NC), C392 18p (NC), C357 100n → R511 390R → C874 27p 50V, C875 27p 50V
CAN_IF_P → S177 → C354 100n → R507 390R
RESET_IC → R473 100R

U119 PNX8543

TS7	A10	TNR_TSDI7
TS6	E11	TNR_TSDI6
TS5	D11	TNR_TSDI5
TS4	C11	TNR_TSDI4
TS3	A11	TNR_TSDI3
TS2	A9	TNR_TSDI2
TS1	E10	TNR_TSDI1
TS0	D10	TNR_TSDI0
TS8	B10	TNR_MICLK
TS9	C10	TNR_MIVAL
TS10	B9	TNR_MISTRT
NC C340 18p 50V	C12	TNR_ERROR
3V3_VCC R835 47k	U1	VSYNCOUT
	C2	IF_AGC

ANALOG DEMODULATOR

ANALOG_IF → C315 1n → S301
D108 BA782, R447 22k
S302 → Z100 K9656M (IN1 OUT1 GND IN2 OUT2): SAW_A2, SAW_A1
C292 1n, R414 10k → 5V_TUN
Q121 BC848B → R425 10k → SAW_SW
R424 10k → 5V_TUN
R413 1k → 5V_TUN

ANALOG_IF → C314 1n → Z101 K3958M (IN1 OUT1 GND IN2 OUT2): SAW_V2, SAW_V1
L148 2n2

S-VIDEO & Side CVBS SWITCH

JK104: SVHS_C (F185), SVHS_Y (F178), C477 NC 150p
D196, C541, D195

CVBS_SW → R262 100R → U121 FSA3157 (S VCC GND A B1 B0)
5V_TUN
R1114 1k → R1113 1k → 5V_TUN
S116 → SVHS_Y
S120 → SIDE_CVBS
R1128 1k → 5V_TUN
R1127 1k

SVHS_C → R308 18R, C1091 NC 150p, R306 56R, C239 22n → NXP543_SEL_C
SEL_CVBS/Y_IN → R934 18R, R924 56R, C881 NC, C233 150p22n → NXP543_SEL_Y/CVBS

5V_TUN → L115 4u7, C281 22u 16V, C282 1n → R395 47R → Q120 BC848B
R397 100R → TUNER_CVBS → TP131, R398 56R
R284 18R → C225 150p 50V, R283 56R → C222 22n 16V → NXP543_TUNER_CVBS
R431 1k, R925 47R → CVBS_TUNER
L149 10u R1141 10k, L150 10u R1142 10k
C307 1n5

IF_AGC_CAN → R463 1k, C317 100n

QSS_N: C284 10n, C901 4p7 50V, C248 100n, R312 390R, C247 22p → NXP543_SI_SIF_N
QSS_P: C285 10n, C252 100n, R324 390R, C251 22p → NXP543_SI_SIF_P

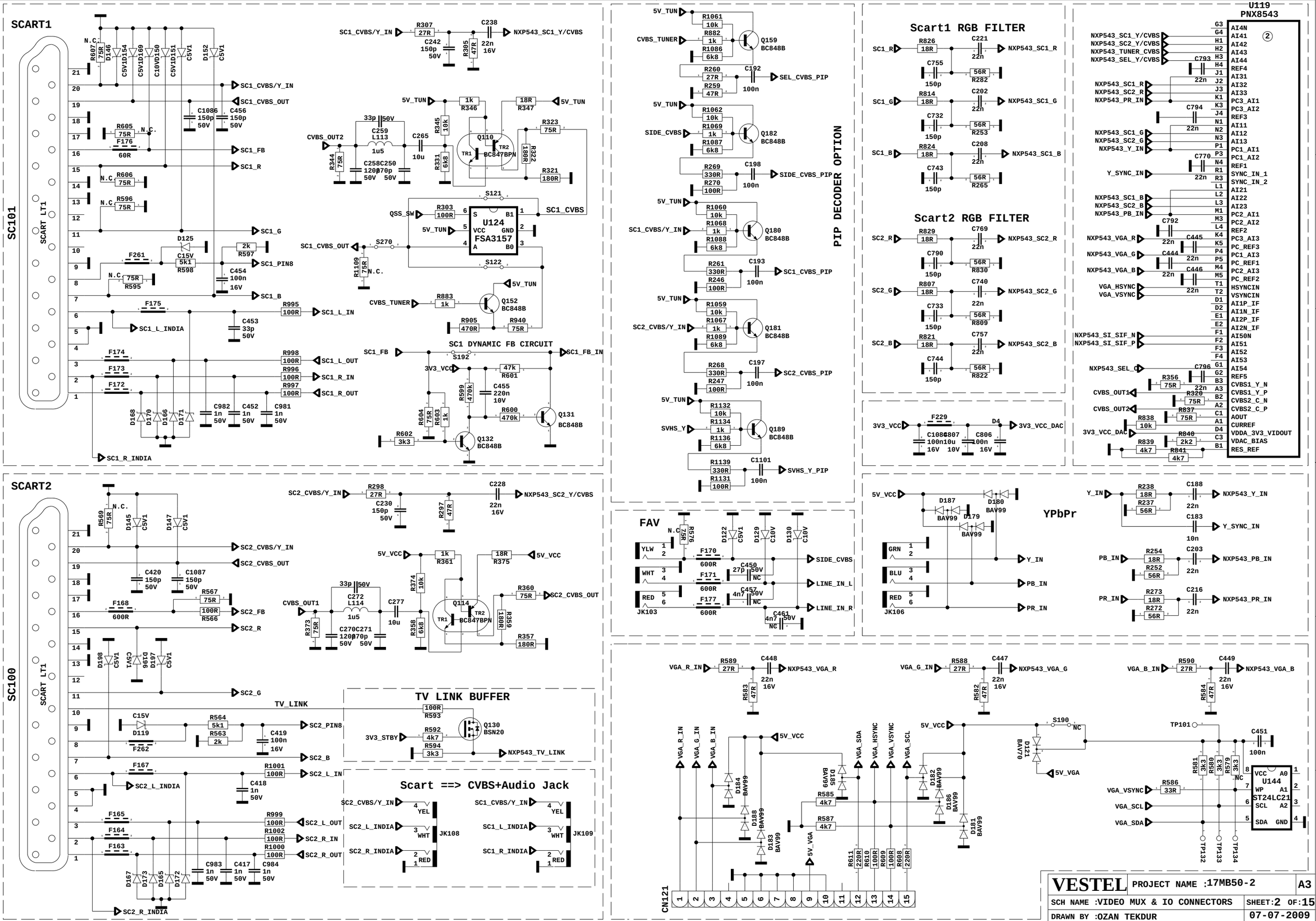
U133 TDA9898HL

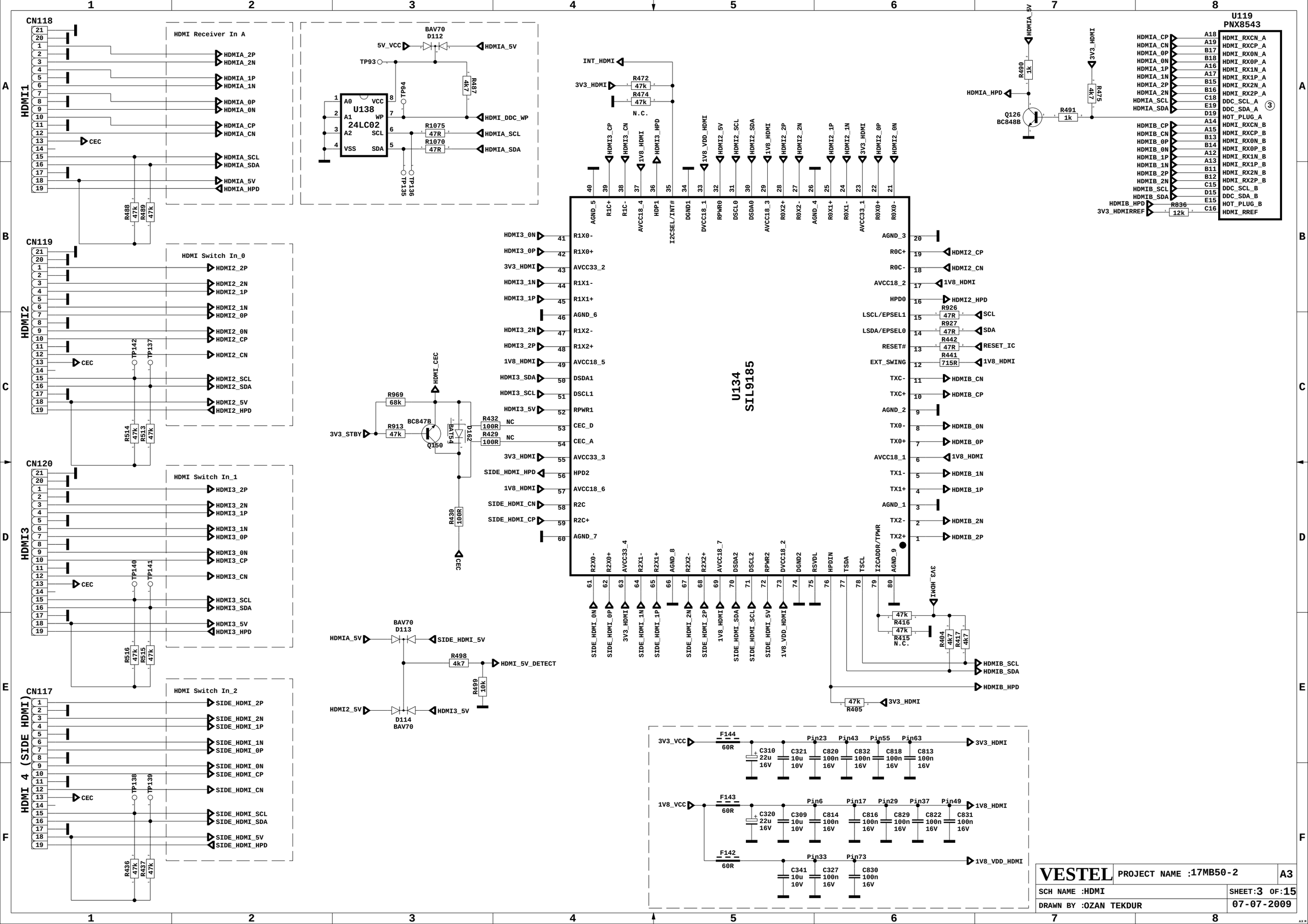
	37	NC4	
	38	LFSYN1	
X104 C311 4MHz22p	39	OPTXTAL	
	40	GND2	
	41	GND3	
	42	NC5	
5V_TUN F235, C886 22u, C865 100n 16V	43	5V_VDA1	
	44	5V_VDA2	
	45	IC2	
	46	FREF	
	47	TAGC	
	48	GND4	
	24	SCL	
	23	SDA	
	22	GND1	
	21	EXTFMI	
	20	CDEEM	
	19	LFFM	
	18	NC2	
	17	EXTFILI	
R:0x86h W:0x87h	16	MPP2	AGC_RF
	15	EXTFILO	
	14	IC1	
	13	LFVIF	
	1	LFSYN2	
	2	NC1	
	3	IF3A	
	4	IF3B	
	5	CTFAGC	
	6	IF1A	
	7	IF1B	
	8	CTAGC	
	9	IF2A	
	10	IF2B	
	11	TOP2	
	12	MPP1	
	36	AGCDIN	
	35	NC3	
	34	CAF2	
	33	CVBS	
	32	BVS	
	31	AUD	
	30	OUT2B	
	29	OUT2A	
	28	CAF1	
	27	OUT1B	
	26	OUT1A	
	25	ADRSEL	

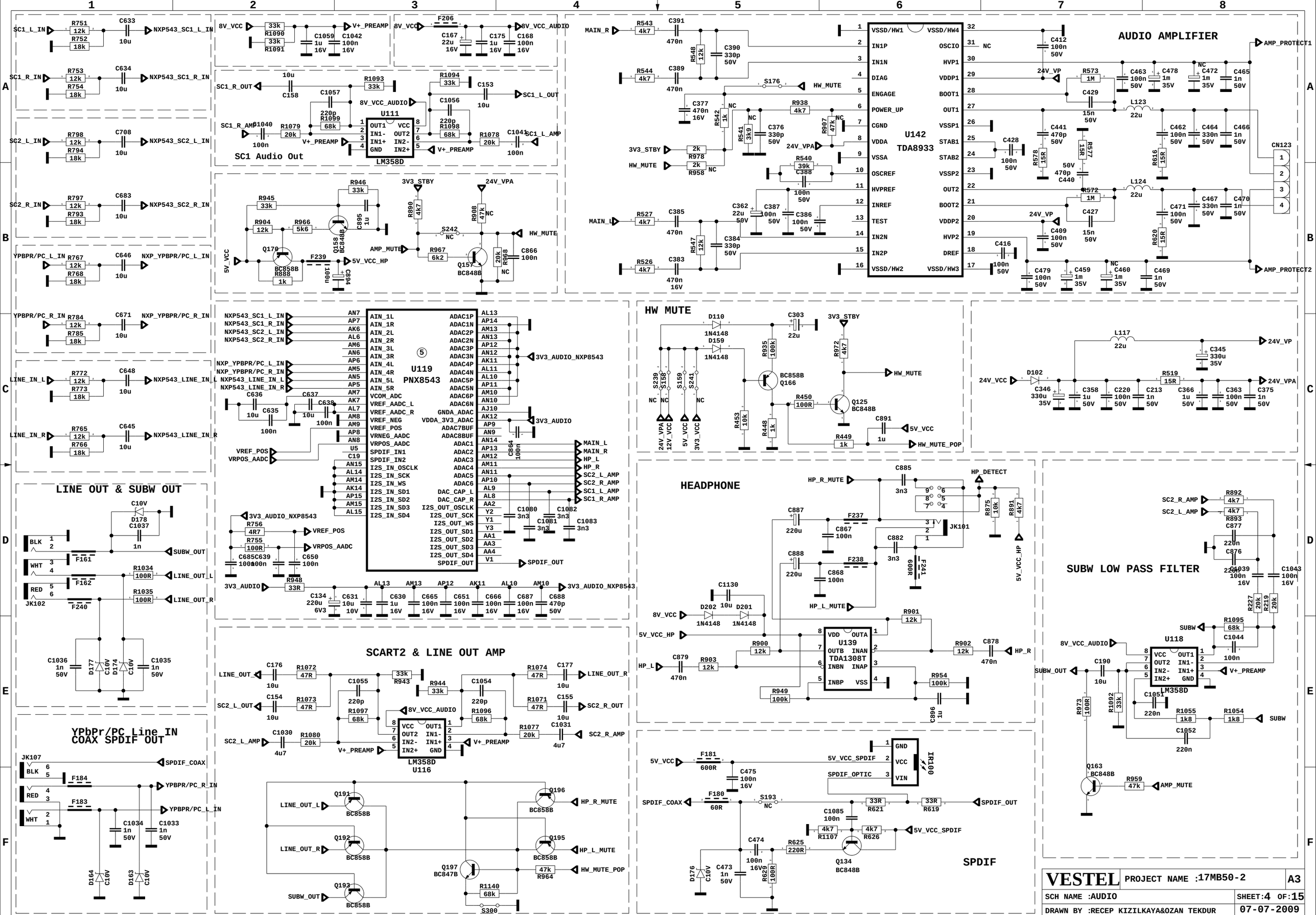
R464 470R, C318 100n
C313 22p, C305 4n7, R446 220R, R445 1k6
C308 470n 16V, C294 100n 16V
C892 1n, R427 2k2 → SAW_SW
SAW_V1, SAW_V2, SAW_A1, SAW_A2

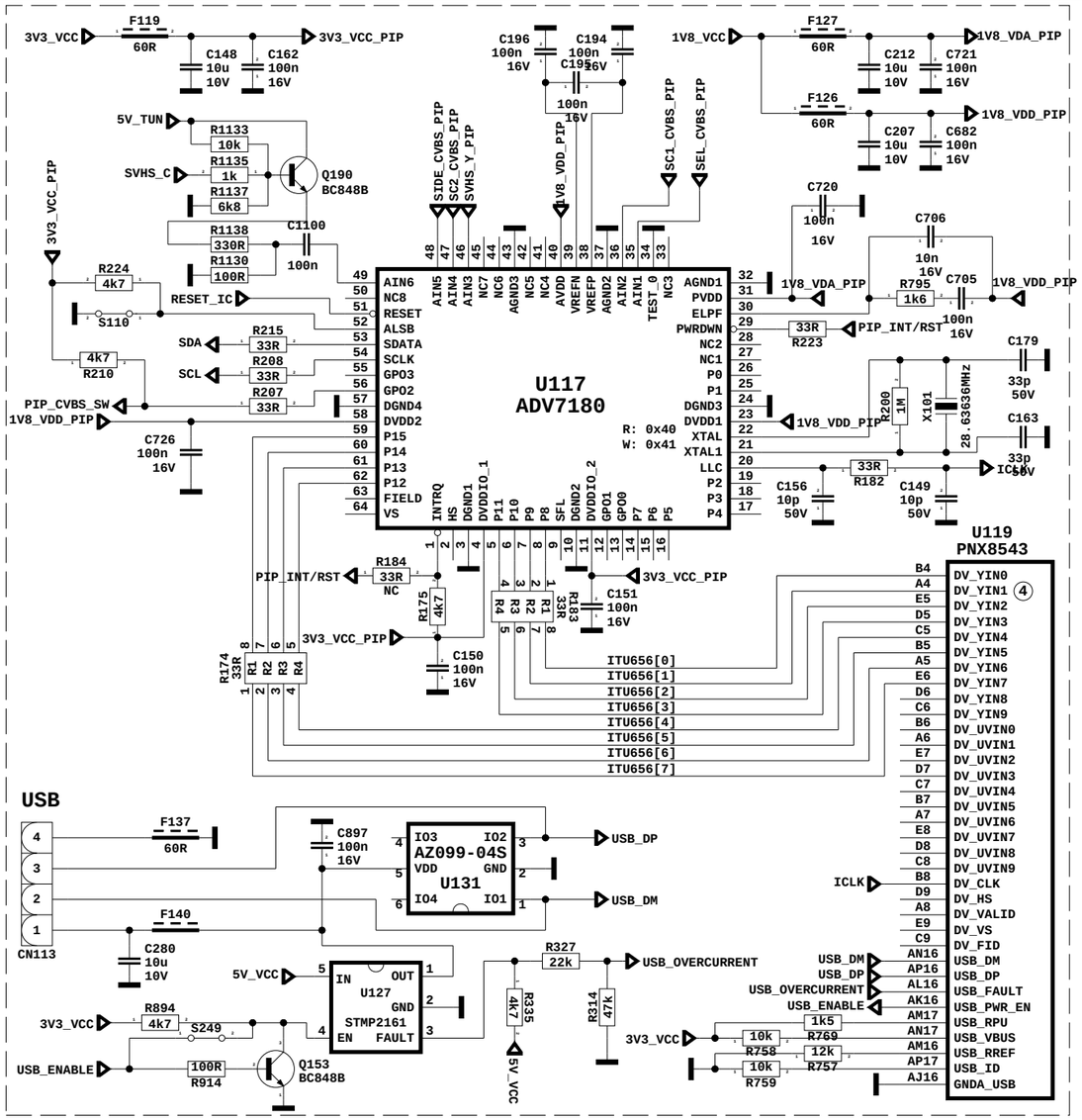
SCL → R410 100R
SDA → R411 100R
C286 2n2, R412 5k6, C287 470p
C290 220n, C893 1n 50V, R423 330R
RF_AGC_CAN → C1131 220n 10V, S271, R444 1k → C312 220n 10V

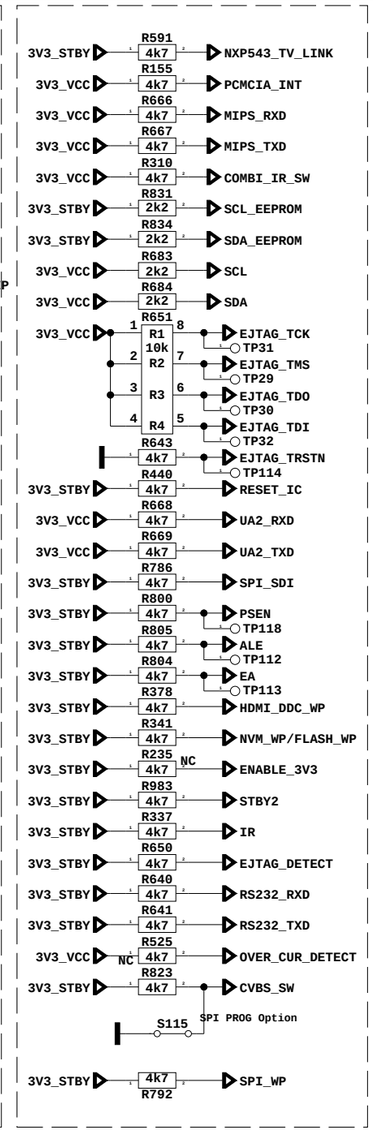
VESTEL	PROJECT NAME :17MB50-2		A3
	SCH NAME :FRONT END	SHEET:1	OF:15
	DRAWN BY :OZAN TEKDUR	07-07-2009	

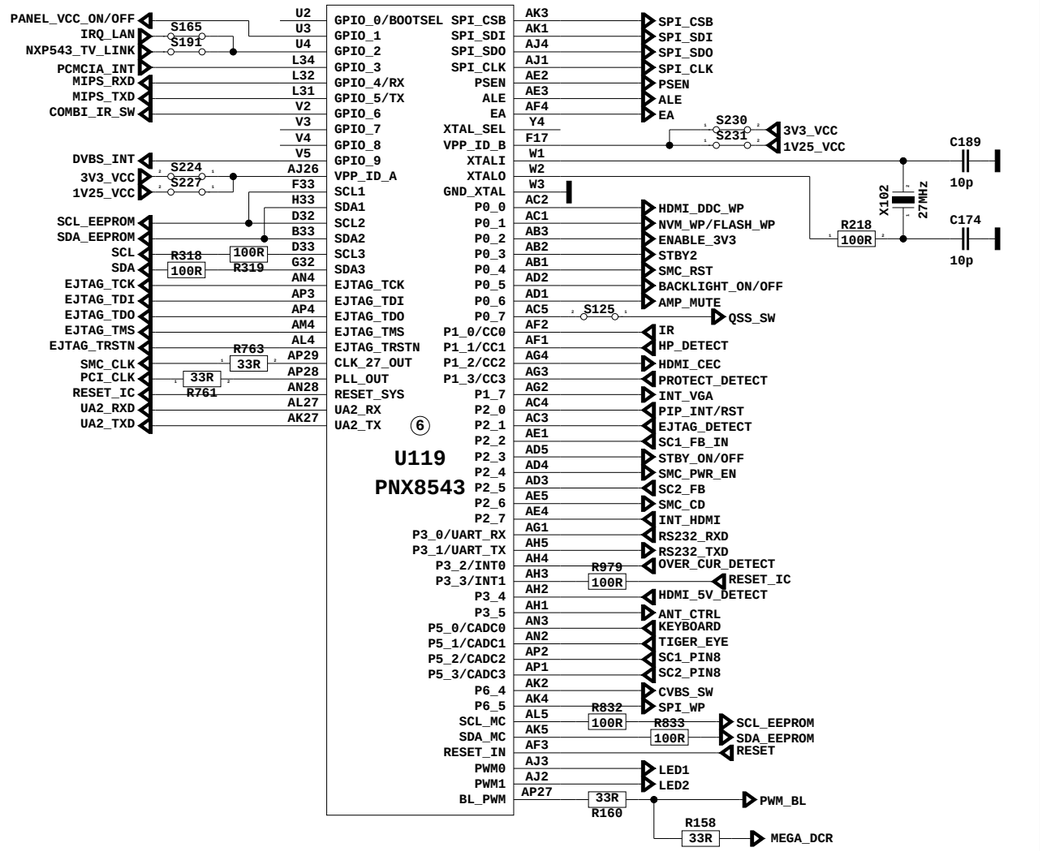

SCART1
SC101
SCART LT1
SC1_CVBS/Y_IN
SC1_CVBS_OUT
SC1_FB
SC1_R
SC1_G
SC1_PIN8
SC1_B
SC1_L_IN
SC1_L_INDIA
SC1_L_OUT
SC1_R_IN
SC1_R_OUT
SC1_R_INDIA
SC1_CVBS
SC1_FB_IN
SC1 DYNAMIC FB CIRCUIT
PIP DECODER OPTION
SCART2
SC100
SC2_CVBS/Y_IN
SC2_CVBS_OUT
SC2_FB
SC2_R
SC2_G
SC2_PIN8
SC2_B
SC2_L_IN
SC2_L_INDIA
SC2_L_OUT
SC2_R_IN
SC2_R_OUT
SC2_R_INDIA
TV_LINK
TV LINK BUFFER
Scart ==> CVBS+Audio Jack
JK108
JK109
Scart1 RGB FILTER
Scart2 RGB FILTER
FAV
JK103
YPbPr
JK106
U119
PNX8543
U124
FSA3157
U144
ST24LC21
CN121
PROJECT NAME :17MB50-2
SCH NAME :VIDEO MUX & IO CONNECTORS
DRAWN BY :OZAN TEKDUR
SHEET:2 OF:15
07-07-2009
A3

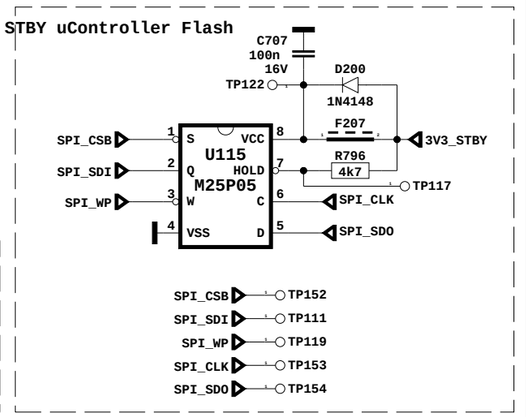

CN118
HDMI1
HDMI Receiver In A
HDMIA_2P
HDMIA_2N
HDMIA_1P
HDMIA_1N
HDMIA_0P
HDMIA_0N
HDMIA_CP
HDMIA_CN
CEC
HDMIA_SCL
HDMIA_SDA
HDMIA_5V
HDMIA_HPD
R488 47k
R489 47k
BAV70
D112
5V_VCC
HDMIA_5V
TP93
TP94
TP135
TP136
R487 4k7
U138
24LC02
A0
A1
A2
VSS
VCC
WP
SCL
SDA
R1075 47R
R1070 47R
HDMI_DDC_WP
HDMIA_SCL
HDMIA_SDA
CN119
HDMI2
HDMI Switch In_0
HDMI2_2P
HDMI2_2N
HDMI2_1P
HDMI2_1N
HDMI2_0P
HDMI2_0N
HDMI2_CP
HDMI2_CN
CEC
TP142
TP137
HDMI2_SCL
HDMI2_SDA
HDMI2_5V
HDMI2_HPD
R514 47k
R513 47k
CN120
HDMI3
HDMI Switch In_1
HDMI3_2P
HDMI3_2N
HDMI3_1P
HDMI3_1N
HDMI3_0P
HDMI3_0N
HDMI3_CP
HDMI3_CN
CEC
TP140
TP141
HDMI3_SCL
HDMI3_SDA
HDMI3_5V
HDMI3_HPD
R516 47k
R515 47k
CN117
HDMI 4 (SIDE HDMI)
HDMI Switch In_2
SIDE_HDMI_2P
SIDE_HDMI_2N
SIDE_HDMI_1P
SIDE_HDMI_1N
SIDE_HDMI_0P
SIDE_HDMI_0N
SIDE_HDMI_CP
SIDE_HDMI_CN
CEC
TP138
TP139
SIDE_HDMI_SCL
SIDE_HDMI_SDA
SIDE_HDMI_5V
SIDE_HDMI_HPD
R436 47k
R437 47k
INT_HDMI
3V3_HDMI
R472 47k
R474 47k
N.C.
HDMI3_CP
HDMI3_CN
1V8_HDMI
HDMI3_HPD
1V8_VDD_HDMI
HDMI2_5V
HDMI2_SCL
HDMI2_SDA
HDMI2_1P
HDMI2_1N
3V3_HDMI
HDMI2_0P
HDMI2_0N
U134
SIL9185
AGND_5
R1C+
R1C-
AVCC18_4
HDP1
I2CSEL/INT#
DGND1
DVCC18_1
RPWR0
DSCL0
DSDA0
AVCC18_3
R0X2+
R0X2-
AGND_4
R0X1+
R0X1-
AVCC33_1
R0X0+
R0X0-
HDMI3_0N
HDMI3_0P
3V3_HDMI
HDMI3_1N
HDMI3_1P
AGND_6
HDMI3_2N
HDMI3_2P
1V8_HDMI
HDMI3_SDA
HDMI3_SCL
HDMI3_5V
R969 68k
HDMI_CEC
3V3_STBY
R913 47k
BC847B
Q150
BAT54
D162
R432 100R
R429 100R
NC
R430 100R
CEC
AGND_3
R0C+
R0C-
AVCC18_2
HPD0
LSCL/EPSEL1
LSDA/EPSEL0
RESET#
EXT_SWING
TXC-
TXC+
AGND_2
TX0-
TX0+
AVCC18_1
TX1-
TX1+
AGND_1
TX2-
TX2+
HDMI2_CP
HDMI2_CN
HDMI2_HPD
SCL
SDA
RESET_IC
1V8_HDMI
HDMIB_CN
HDMIB_CP
HDMIB_0N
HDMIB_0P
HDMIB_1N
HDMIB_1P
HDMIB_2N
HDMIB_2P
R926 47R
R927 47R
R442 47R
R441 715R
R0X0-
R0X0+
AVCC33_4
R2X1-
R2X1+
AGND_8
R2X2-
R2X2+
AVCC18_7
DSDA2
DSCL2
RPWR2
DVCC18_2
DGND2
RSVDL
HPDIN
TSDA
TSCL
I2CADDR/TPWR
AGND_9
SIDE_HDMI_0N
SIDE_HDMI_0P
3V3_HDMI
SIDE_HDMI_1N
SIDE_HDMI_1P
1V8_HDMI
SIDE_HDMI_2N
SIDE_HDMI_2P
SIDE_HDMI_SDA
SIDE_HDMI_SCL
SIDE_HDMI_5V
1V8_VDD_HDMI
3V3_HDMI
47k R416
47k R415
N.C.
R404 4k7
R417 4k7
HDMIB_SCL
HDMIB_SDA
HDMIB_HPD
47k R405
HDMIA_5V
R490 1k
HDMIA_HPD
Q126
BC848B
R491 1k
R475 4k7
U119
PNX8543
HDMIA_CP
HDMIA_CN
HDMIA_0P
HDMIA_0N
HDMIA_1P
HDMIA_1N
HDMIA_2P
HDMIA_2N
HDMIA_SCL
HDMIA_SDA
HDMIB_CP
HDMIB_CN
HDMIB_0P
HDMIB_0N
HDMIB_1P
HDMIB_1N
HDMIB_2P
HDMIB_2N
HDMIB_SCL
HDMIB_SDA
HDMIB_HPD
3V3_HDMIRREF
R836 12k
HDMI_RXCN_A
HDMI_RXCP_A
HDMI_RX0N_A
HDMI_RX0P_A
HDMI_RX1N_A
HDMI_RX1P_A
HDMI_RX2N_A
HDMI_RX2P_A
DDC_SCL_A
DDC_SDA_A
HOT_PLUG_A
HDMI_RXCN_B
HDMI_RXCP_B
HDMI_RX0N_B
HDMI_RX0P_B
HDMI_RX1N_B
HDMI_RX1P_B
HDMI_RX2N_B
HDMI_RX2P_B
DDC_SCL_B
DDC_SDA_B
HOT_PLUG_B
HDMI_RREF
HDMIA_5V
SIDE_HDMI_5V
R498 4k7
HDMI_5V_DETECT
R499 10k
HDMI2_5V
HDMI3_5V
D114
BAV70
F144 60R
F143 60R
F142 60R
3V3_VCC
1V8_VCC
Pin23
Pin43
Pin55
Pin63
Pin6
Pin17
Pin29
Pin37
Pin49
Pin33
Pin73
C310 22u 16V
C321 10u 10V
C820 100n 16V
C832 100n 16V
C818 100n 16V
C813 100n 16V
C320 22u 16V
C309 10u 10V
C814 100n 16V
C816 100n 16V
C829 100n 16V
C822 100n 16V
C831 100n 16V
C341 10u 10V
C327 100n 16V
C830 100n 16V
VESTEL
PROJECT NAME :17MB50-2
A3
SCH NAME :HDMI
SHEET:3 OF:15
DRAWN BY :OZAN TEKDUR
07-07-2009

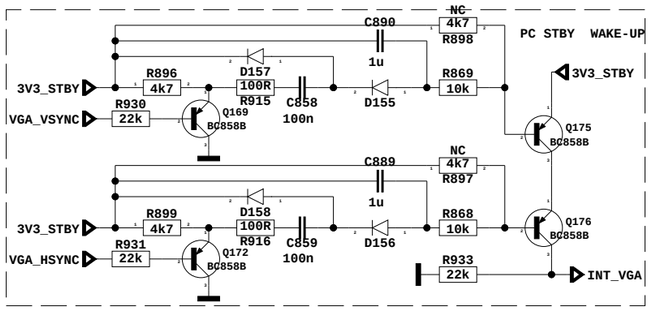

AUDIO AMPLIFIER
U142
TDA8933
HW MUTE
SC1 Audio Out
U111
LM358D
U119
PNX8543
LINE OUT & SUBW OUT
SCART2 & LINE OUT AMP
YPbPr/PC Line IN
COAX SPDIF OUT
HEADPHONE
SUBW LOW PASS FILTER
SPDIF
U139
TDA1308T
U118
VESTEL
PROJECT NAME :17MB50-2
A3
SCH NAME :AUDIO
SHEET:4 OF:15
DRAWN BY :RECEP KIZILKAYA&OZAN TEKDUR
07-07-2009

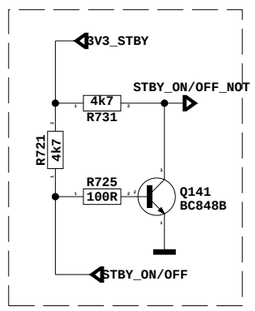


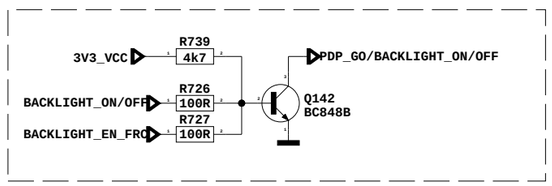


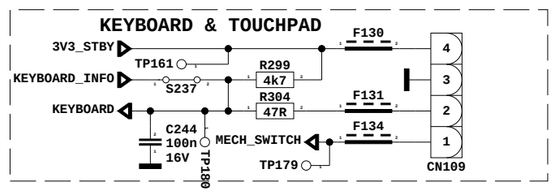


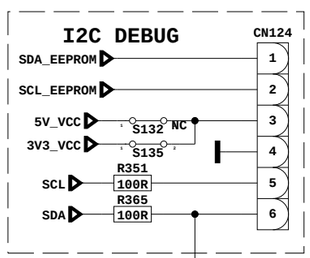


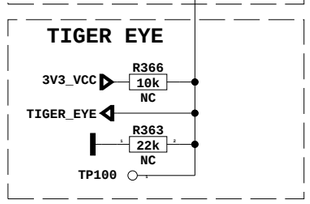


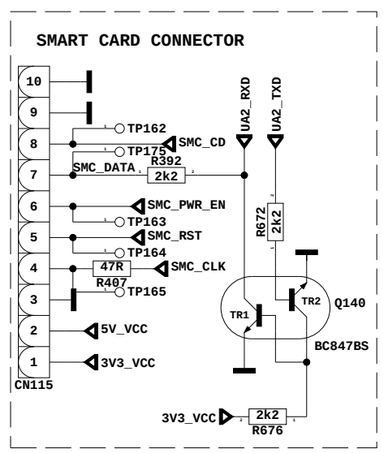


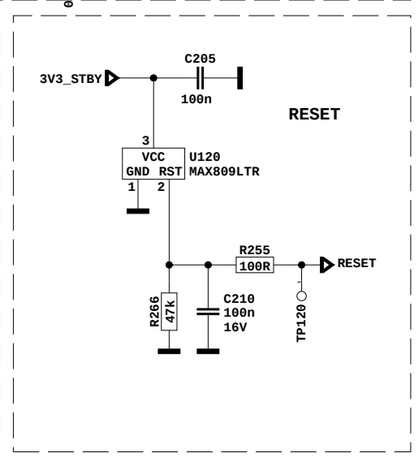


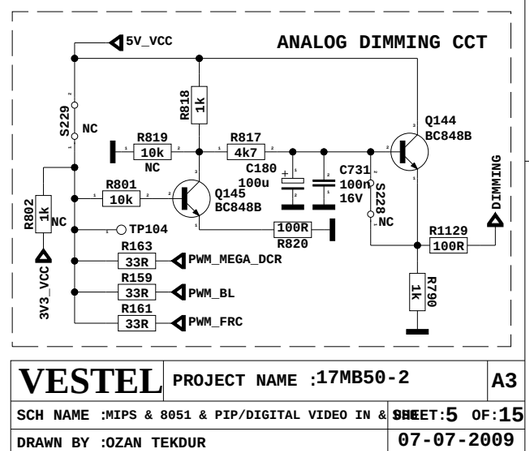




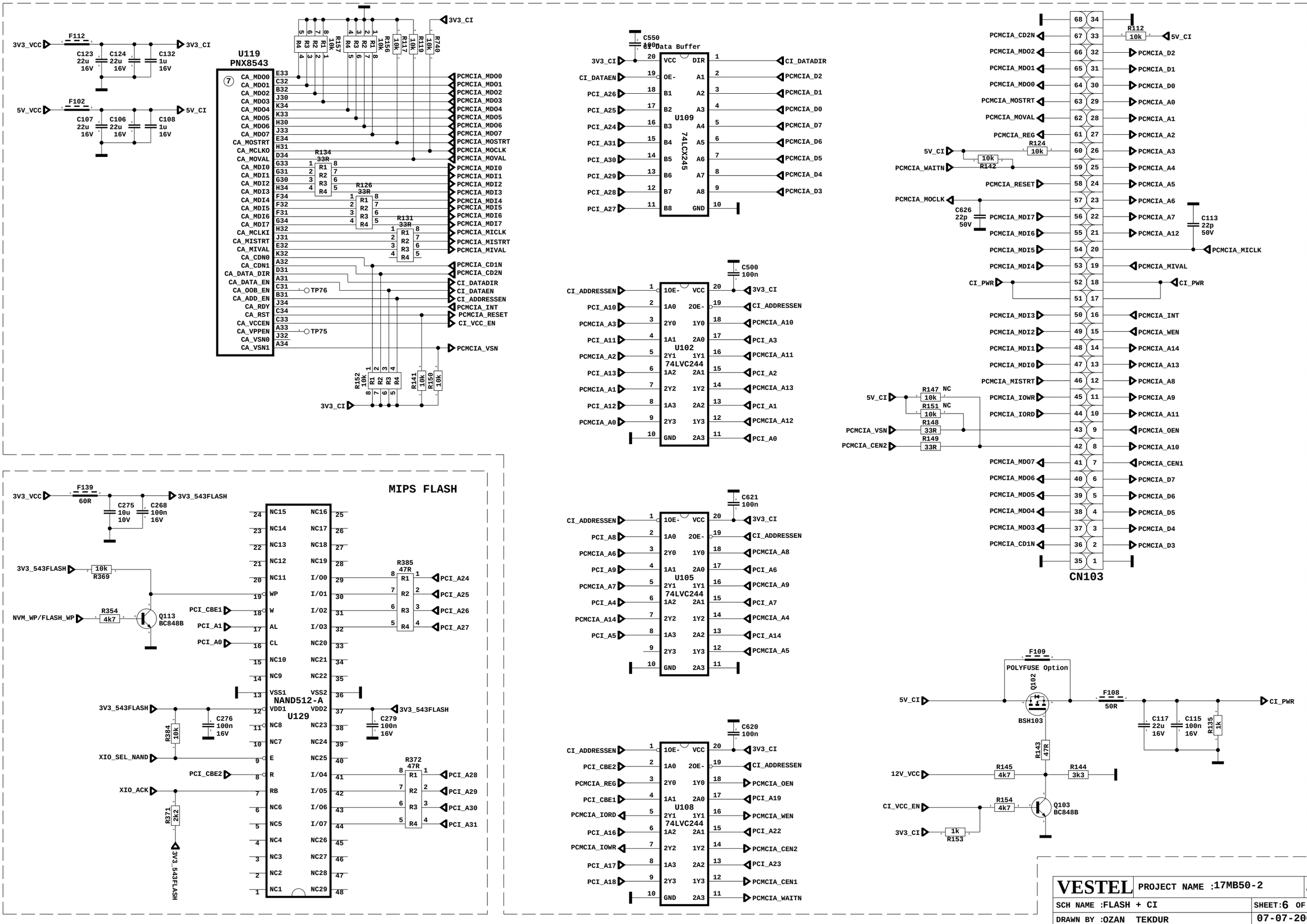




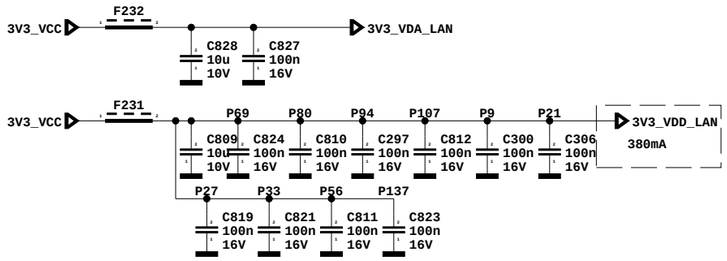


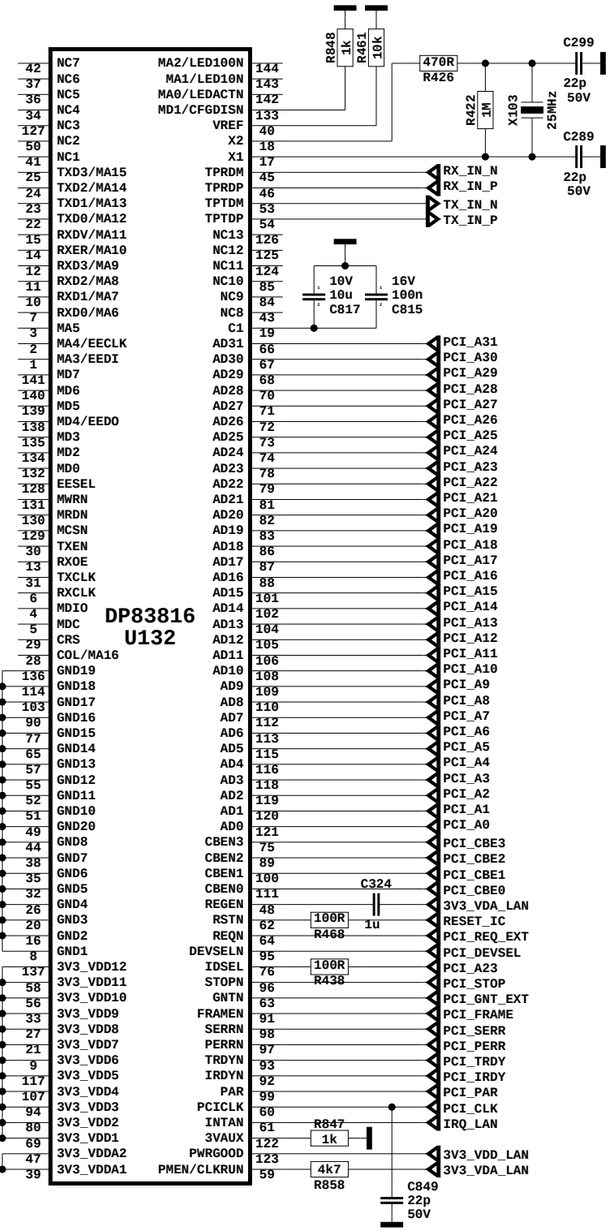


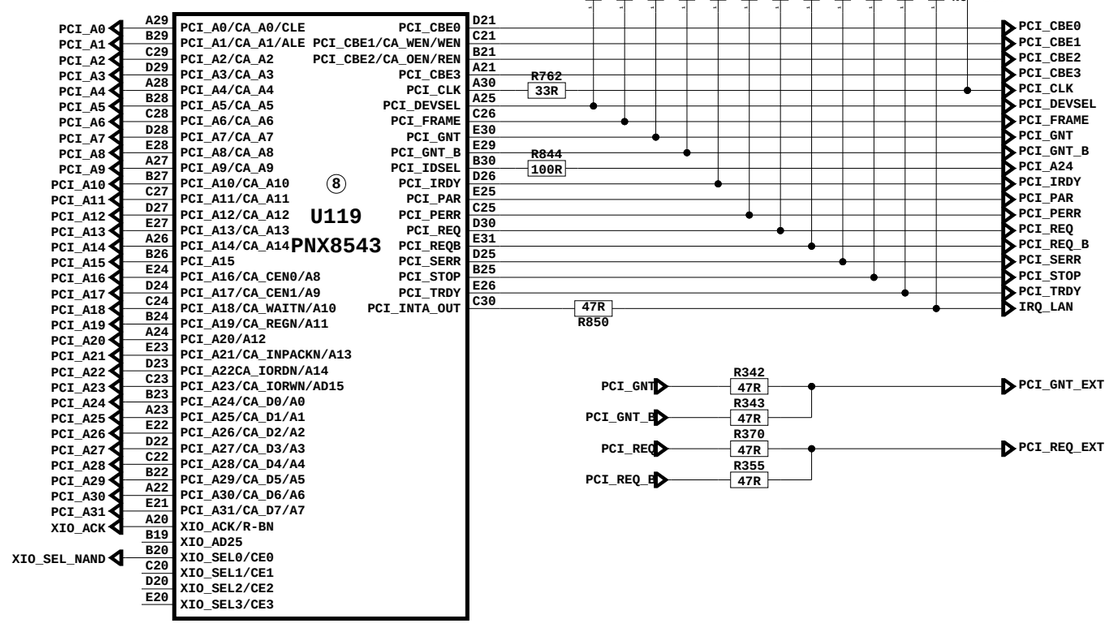
VESTEL	PROJECT NAME :17MB50-2		A3
SCH NAME :MIPS & 8051 & PIP/DIGITAL VIDEO IN & SHE		SHEET:5	OF:15
DRAWN BY :OZAN TEKDUR		07-07-2009	

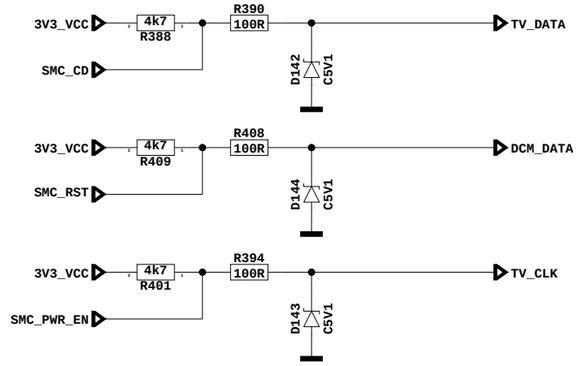


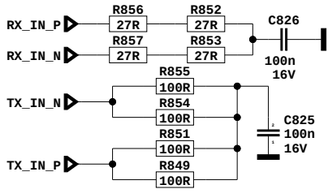
VESTEL	PROJECT NAME :17MB50-2		A3
SCH NAME :FLASH + CI		SHEET:6	OF:15
DRAWN BY :OZAN TEKDUR		07-07-2009	

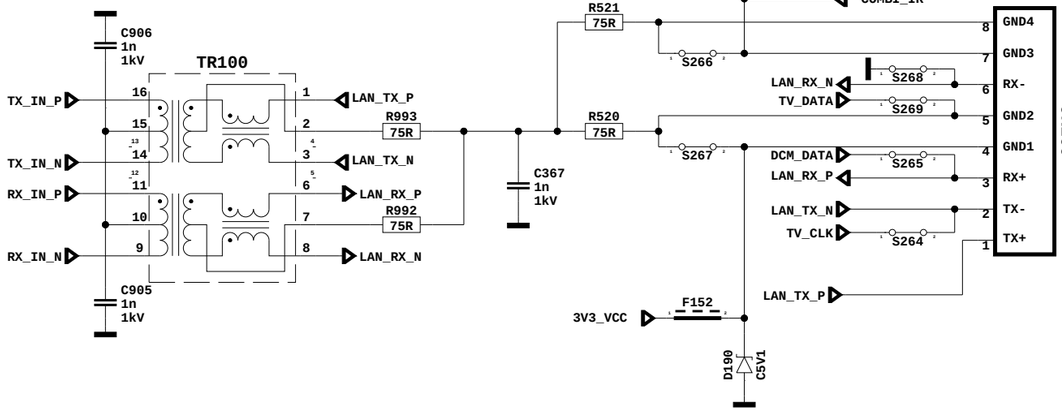




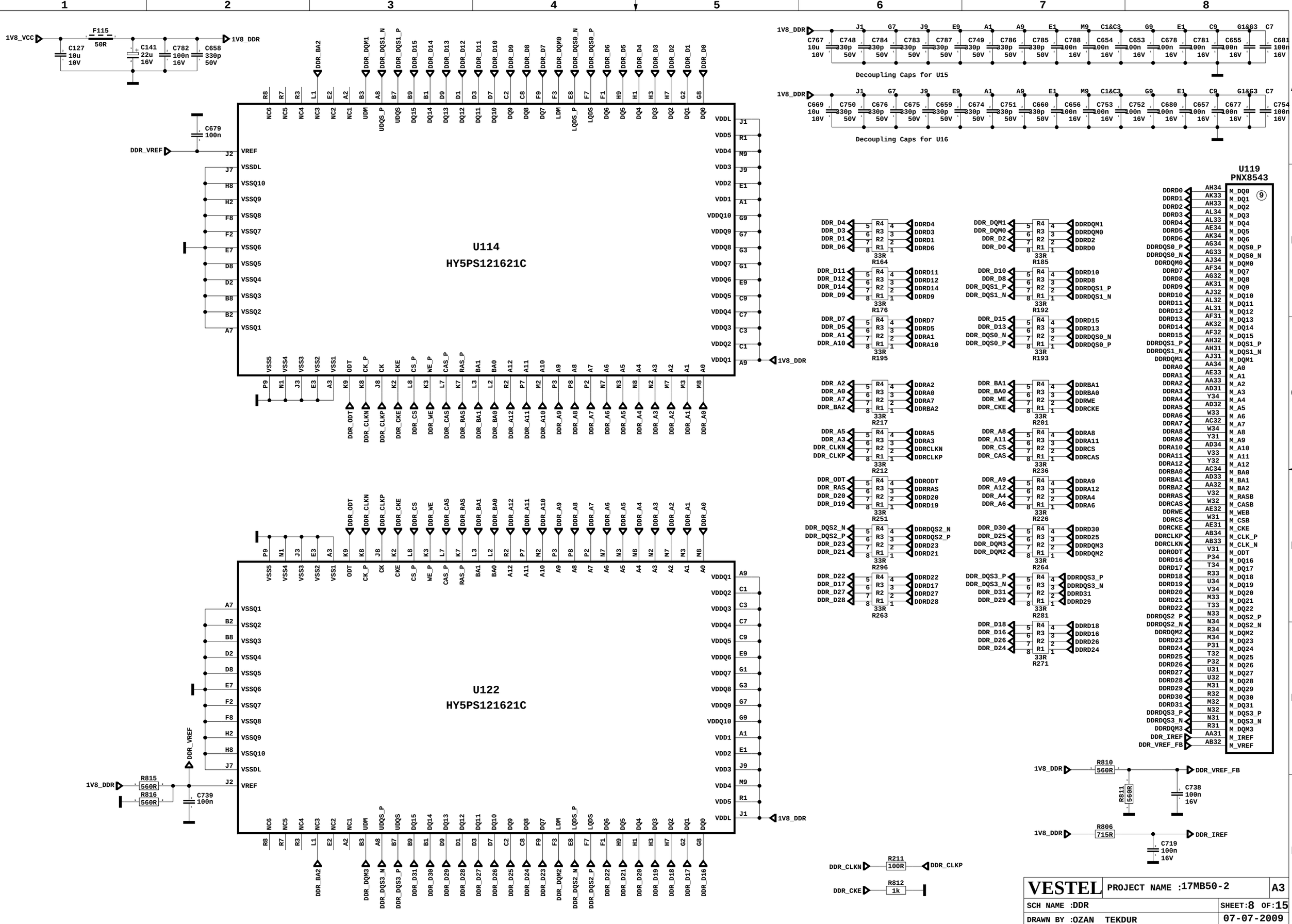








VESTEL	PROJECT NAME :17MB50-2	A3
SCH NAME :ETHERNET - SMARTPORT	SHEET:7 OF:15	
DRAWN BY :OZAN TEKDUR	07-07-2009	


1V8_VCC
F115
50R
C127
10u
10V
C141
22u
16V
C782
100n
16V
C658
330p
50V
1V8_DDR
C679
100n
DDR_VREF
U114
HY5PS121621C
U122
HY5PS121621C
Decoupling Caps for U15
Decoupling Caps for U16
C767
10u
10V
C669
10u
10V
U119
PNX8543
R164
R176
R195
R217
R212
R251
R296
R263
R185
R192
R193
R201
R236
R226
R264
R281
R271
R815
560R
R816
560R
C739
100n
R211
100R
R812
1k
R810
560R
R811
560R
C738
100n
16V
R806
715R
C719
100n
16V
VESTEL
PROJECT NAME :17MB50-2
A3
SCH NAME :DDR
SHEET:8 OF:15
DRAWN BY :OZAN TEKDUR
07-07-2009

NXP543 LVDS OUTPUT

U119
PNX8543

10 LOUT1_AN AN18 RX_B_0_P
LOUT1_AP AP18 RX_B_0_N
LOUT1_BN AK18 RX_B_1_P
LOUT1_BP AL18 RX_B_1_N
LOUT1_CN AN19 RX_B_2_P
LOUT1_CP AP19 RX_B_2_N
LOUT1_CLKN AL19 RX_B_CLK_P
LOUT1_CLKP AM19 RX_B_CLK_N
LOUT1_DN AN20 RX_B_3_P
LOUT1_DP AP20 RX_B_3_N
LOUT1_EN AL20 RX_B_4_P
LOUT1_EP AM20 RX_B_4_N

LOUT2_AN AN22 RX_A_0_P
LOUT2_AP AP22 RX_A_0_N
LOUT2_BN AK22 RX_A_1_P
LOUT2_BP AL22 RX_A_1_N
LOUT2_CN AN23 RX_A_2_P
LOUT2_CP AP23 RX_A_2_N
LOUT2_CLKN AL23 RX_A_CLK_P
LOUT2_CLKP AM23 RX_A_CLK_N
LOUT2_DN AN24 RX_A_3_P
LOUT2_DP AP24 RX_A_3_N
LOUT2_EN AL24 RX_A_4_P
LOUT2_EP AM24 RX_A_4_N
IREF_LVDS AK19 12k R782 3V3_LVDS

PANEL SUPPLY SWITCH

12V_VCC S101 S250
5V_VCC S100 S251
C109 100u 16V
R110 30k
R109 330R
R108 10k
Q101 FDC642P
1500mA
PANEL_VCC
TP148
C100 22u 16V
R120 47k
C104 100n 16V
C102 10u 16V
PANEL_VCC_ON/OFF
R101 47R
Q100 BC848B
C103 100n 16V
R895 4k7
R100 4k7
TP115
NC
3V3_VCC

U104 PNX5100

RX_A_0_N AF17 LIN1_AN
RX_A_0_P AE17 LIN1_AP
RX_A_1_N AD17 LIN1_BN
RX_A_1_P AC17 LIN1_BP
RX_A_2_N AF16 LIN1_CN
RX_A_2_P AE16 LIN1_CP
RX_A_3_N AF15 LIN1_DN
RX_A_3_P AE15 LIN1_DP
RX_A_4_N AD15 LIN1_EN
RX_A_4_P AC15 LIN1_EP
RX_A_CLK_N AD16 LIN1_CLKN
RX_A_CLK_P AC16 LIN1_CLKP
RX_B_0_N AF20 LIN2_AN
RX_B_0_P AE20 LIN2_AP
RX_B_1_N AD20 LIN2_BN
RX_B_1_P AC20 LIN2_BP
RX_B_2_N AF19 LIN2_CN
RX_B_2_P AE19 LIN2_CP
RX_B_3_N AF18 LIN2_DN
RX_B_3_P AE18 LIN2_DP
RX_B_4_N AD18 LIN2_EN
RX_B_4_P AC18 LIN2_EP
RX_B_CLK_N AD19 LIN2_CLKN
RX_B_CLK_P AC19 LIN2_CLKP
3V3_LVDSD_IN AB15 VDD_3V3_LVDSIN_1
AB17 VDD_3V3_LVDSIN_2
AB18 VDDA_3V3_LVDSIN
AB19 VSS_LVDSIN

3V3_VCC L135 4u7
C577 10u 10V
C597 100n 16V

PNX5100 DUAL LVDS IN/QUAD LVDS OUT

RX_A_0_N TP24
RX_A_0_P TP22
RX_A_1_N TP20
RX_A_1_P TP18
RX_A_2_N TP16
RX_A_2_P TP14
RX_A_3_N TP12
RX_A_3_P TP10
RX_A_4_N TP8
RX_A_4_P TP6
RX_A_CLK_N TP4
RX_A_CLK_P TP3
RX_B_0_N TP5
RX_B_0_P TP7
RX_B_1_N TP9
RX_B_1_P TP11
RX_B_2_N TP13
RX_B_2_P TP15
RX_B_3_N TP17
RX_B_3_P TP19
RX_B_4_N TP21
RX_B_4_P TP23
RX_B_CLK_N TP25
RX_B_CLK_P TP26

Test Points for Automation

LVDS CONNECTORS W/PNX5100 OPTION

F187 PWM_FRC
S203 S202
LVDS_P50
MEGA_DCR OPTION1
OPTION2
OPTION3
LVDS_P32 LVDS_P34 LVDS_P36 LVDS_P38
R107 R4 R3 R2 R1 47R
PNX_CNTRL4 PNX_CNTRL3 PNX_CNTRL2 PNX_CNTRL1
PANEL_VCC
C101 100n 16V
SDA_PANEL
SCL_PANEL

TX_1_AP 49 50 TP34
TX_1_AN 47 48 TP36
TX_1_BN 45 46 TP38
TX_1_BP 43 44 TP40
TX_1_CLKN 41 42 TP42
TX_1_CLKP 39 40 TP44
TX_1_CN 37 38 TP46
TX_1_CP 35 36 TP48
TX_1_DN 33 34 TP50
TX_1_DP 31 32 TP52
TX_1_EN 29 30 TP54
TX_1_EP 27 28 TP56
25 26
TX_2_AN 23 24 TP55
TX_2_AP 21 22 TP53
TX_2_BN 19 20 TP51
TX_2_BP 17 18 TP49
TX_2_CLKP 15 16 TP47
TX_2_CLKN 13 14 TP45
TX_2_CN 11 12 TP43
TX_2_CP 9 10 TP41
TX_2_DN 7 8 TP39
TX_2_DP 5 6 TP37
TX_2_EN 3 4 TP35
TX_2_EP 1 2 TP33
CN102

TX_3_AP 49 50 LVDS_P50 TP58
TX_3_AN 47 48 MEGA_DCR TP60
TX_3_BN 45 46 OPTION1 TP62
TX_3_BP 43 44 OPTION2 TP64
TX_3_CLKN 41 42 OPTION3 TP66
TX_3_CLKP 39 40 TP68
TX_3_CN 37 38 LVDS_P38 TP70
TX_3_CP 35 36 LVDS_P36 TP72
TX_3_DN 33 34 LVDS_P34 TP74
TX_3_DP 31 32 LVDS_P32 TP78
TX_3_EN 29 30 TP80
TX_3_EP 27 28 TP82
25 26
TX_4_AN 23 24 TP81
TX_4_AP 21 22 TP79
TX_4_BN 19 20 TP77
TX_4_BP 17 18 PANEL_VCC TP73
TX_4_CLKN 15 16 TP71
TX_4_CLKP 13 14 TP69
TX_4_CN 11 12 TP67
TX_4_CP 9 10 SDA_PANEL TP65
TX_4_DN 7 8 SCL_PANEL TP63
TX_4_DP 5 6 TP61
TX_4_EN 3 4 TP59
TX_4_EP 1 2 TP57
CN101

LVDS CONNECTORS WO/PNX5100 OPTION

MEGA_DCR
S104
RX_A_3_P 29 30 RX_A_4_N
RX_A_3_N 27 28 RX_A_4_P
RX_A_CLK_P 25 26 OPTION3
RX_A_CLK_N 23 24 OPTION2
RX_A_2_P 21 22 OPTION1
RX_A_2_N 19 20
RX_A_1_P 17 18 SCL_PANEL
RX_A_1_N 15 16 SDA_PANEL
RX_A_0_P 13 14 STBY_ON/OFF
RX_A_0_N 11 12 PDP_GO/BACKLIGHT_ON/OFF
RX_B_3_P 9 10 3V3_VCC
1 2 PANEL_VCC
CN107

S201 S221 10k R162
10k R747 3V3_VCC
S220 PWM_MEGA_DCR
S105
10k R760 3V3_VCC
S219

RX_A_3_P 49 50 RX_A_4_N
RX_A_3_N 47 48 RX_A_4_P
RX_A_CLK_P 45 46 OPTION3
RX_A_CLK_N 43 44 OPTION2
RX_A_2_P 41 42 OPTION1
RX_A_2_N 39 40
RX_A_1_P 37 38
RX_A_1_N 35 36
RX_A_0_P 33 34
RX_A_0_N 31 32
RX_B_3_P 29 30 PANEL_VCC
RX_B_3_N 27 28
RX_B_CLK_P 25 26
RX_B_CLK_N 23 24
21 22 RX_B_4_P
19 20 RX_B_4_N
RX_B_2_P 17 18
RX_B_2_N 15 16
RX_B_1_P 13 14
RX_B_1_N 11 12
RX_B_0_P 9 10
RX_B_0_N 7 8
1 2
CN108

U104 PNX5100

TX_1_AN A21 LOUT1_AN LOUT3_AN A14 TX_3_AN
TX_1_AP B21 LOUT1_AP LOUT3_AP B14 TX_3_AP
TX_1_BN C21 LOUT1_BN LOUT3_BN C14 TX_3_BN
TX_1_BP D21 LOUT1_BP LOUT3_BP D14 TX_3_BP
TX_1_CN B20 LOUT1_CN LOUT3_CN B13 TX_3_CN
TX_1_CP C20 LOUT1_CP LOUT3_CP C13 TX_3_CP
TX_1_DN A19 LOUT1_DN LOUT3_DN A12 TX_3_DN
TX_1_DP B19 LOUT1_DP LOUT3_DP B12 TX_3_DP
TX_1_EN C19 LOUT1_EN LOUT3_EN C12 TX_3_EN
TX_1_EP D19 LOUT1_EP LOUT3_EP D12 TX_3_EP
TX_1_CLKN E20 LOUT1_CLKN LOUT3_CLKN E12 TX_3_CLKN
TX_1_CLKP E21 LOUT1_CLKP LOUT3_CLKP E13 TX_3_CLKP
TX_2_AN A18 LOUT2_AN LOUT4_AN A11 TX_4_AN
TX_2_AP B18 LOUT2_AP LOUT4_AP B11 TX_4_AP
TX_2_BN C18 LOUT2_BN LOUT4_BN C11 TX_4_BN
TX_2_BP D18 LOUT2_BP LOUT4_BP D11 TX_4_BP
TX_2_CN B17 LOUT2_CN LOUT4_CN B10 TX_4_CN
TX_2_CP C17 LOUT2_CP LOUT4_CP C10 TX_4_CP
TX_2_DN A16 LOUT2_DN LOUT4_DN A9 TX_4_DN
TX_2_DP B16 LOUT2_DP LOUT4_DP B9 TX_4_DP
TX_2_EN C16 LOUT2_EN LOUT4_EN C9 TX_4_EN
TX_2_EP D16 LOUT2_EP LOUT4_EP D9 TX_4_EP
TX_2_CLKN E18 LOUT2_CLKN LOUT4_CLKN E10 TX_4_CLKN
TX_2_CLKP E19 LOUT2_CLKP LOUT4_CLKP E11 TX_4_CLKP
1V25_LVDSFRC E15 VDDA_1V2_LVDS_PLL IREF_LVDS1 E17 12k R677 3V3_LVDSD
3V3_LVDSA B15 VDDA_3V3_LVDS1 IREF_LVDS2 E14
D15 VDDA_3V3_LVDS2 RGB_CLK A22 12k R678
A15 VSSA_LVDS1 VDD_3V3_LVDSOUT_1 D10
C15 VSSA_LVDS2 VDD_3V3_LVDSOUT_2 D13 3V3_LVDSD
VDD_3V3_LVDSOUT_3 D17
VDD_3V3_LVDSOUT_4 D20

3V3_VCC F188 60R
C497 22u 16V C496 22u 16V
D10 D13 D17 D20 3V3_LVDSD
C518 100n 16V C512 100n 16V C513 100n 16V C519 100n 16V
3V3_VCC L127 4u7
C481 22u 16V C480 22u 16V
B15 D15 3V3_LVDSA
C515 100n 16V C485 100n 16V
3V3_VCC F196 60R
AB15 AB17 3V3_LVDSD_IN
C578 10u 10V C598 100n 16V C614 100n 16V
1V25_VCCFRC L128 4u7
C482 22u 16V C483 22u 16V
E15 1V25_LVDSFRC
C516 100n 16V C517 100n 16V

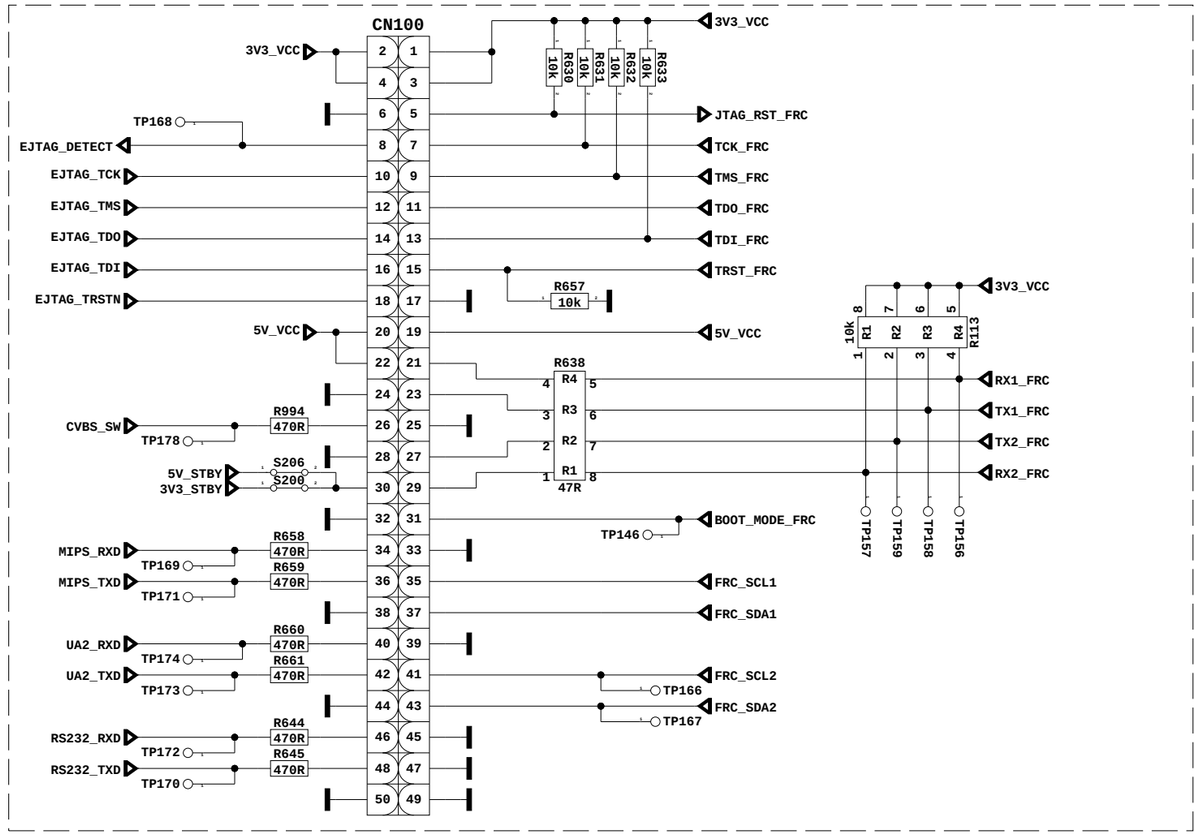
PANEL I2C BUFFER

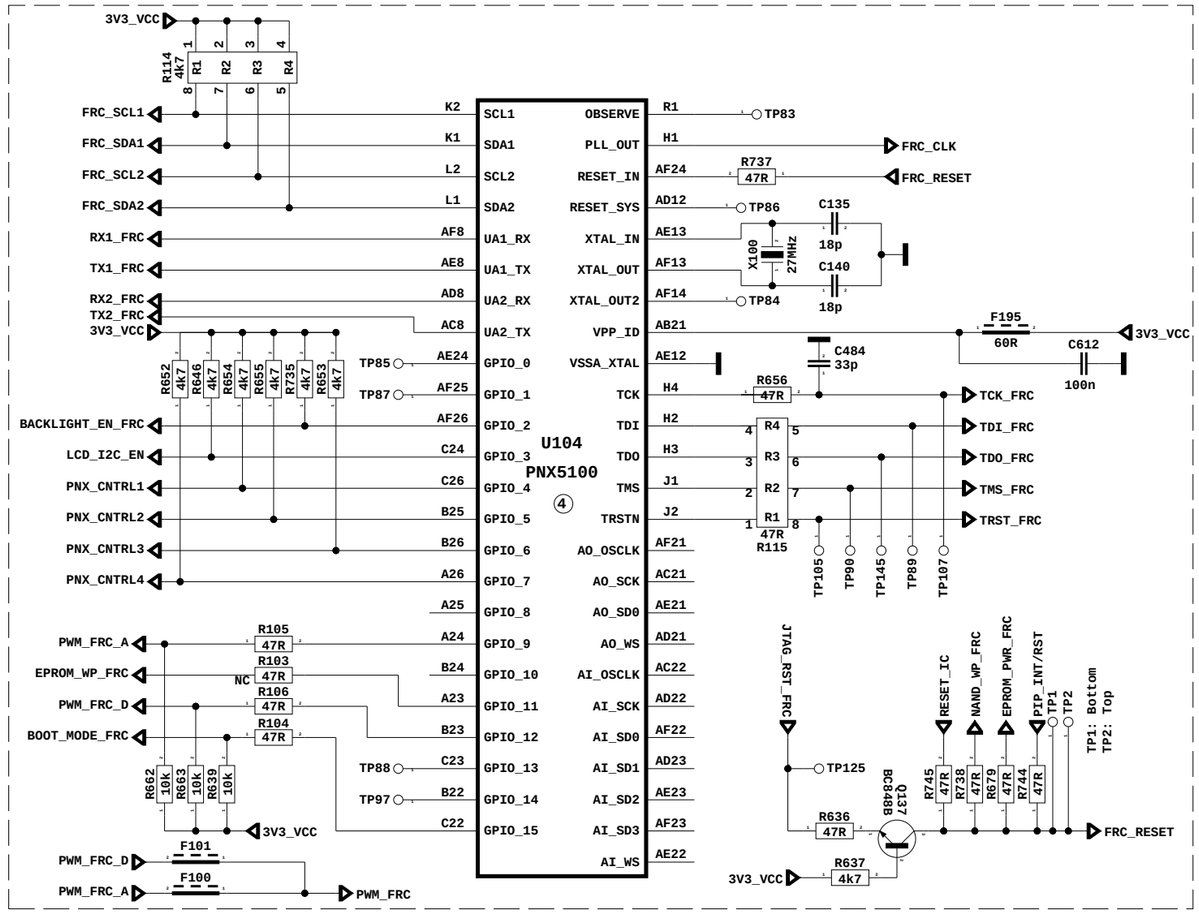
3V3_VCC 4k7 R634
FRC_SCL1 47R R680 NC
FRC_SCL2 47R R674 NC
SCL 47R R681
Q135 BSN20
SCL_PANEL
3V3_VCC R635 4k7
SDA 47R R682
FRC_SDA2 47R R675 NC
FRC_SDA1 47R R673 NC
Q136 BSN20
Q138 BC848B
4k7 R647
LCD_I2C_EN
3V3_VCC 4k7 R642
SDA_PANEL

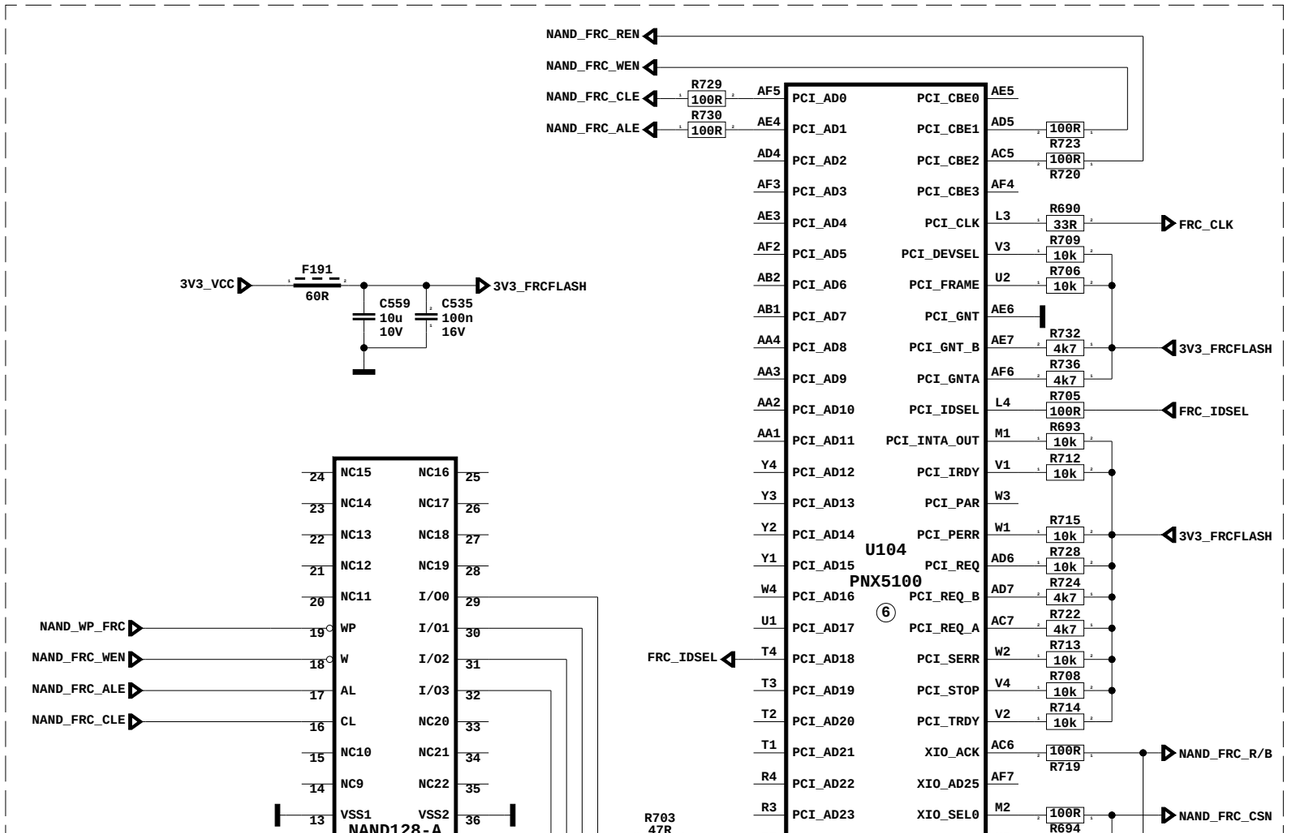
FFC LVDS CONNECTORS FOR WXGA PANELS

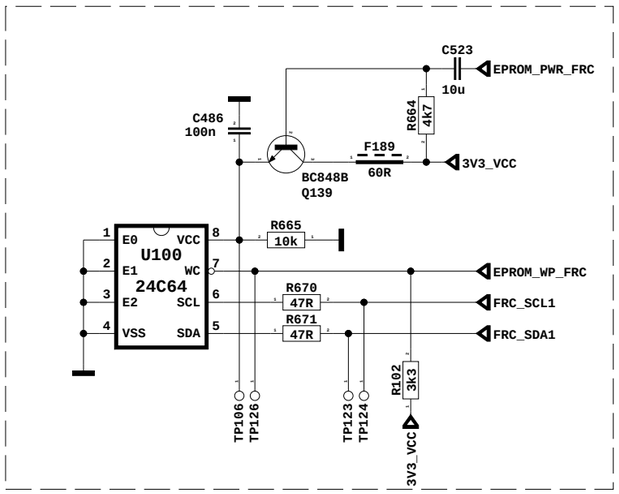
MEGA_DCR S234 R649 10k S205
OPTION2 3V3_VCC S204 R648 10k
3V3_VCC OPTION1 S245
PWM_MEGA_DCR
PANEL_VCC S218 S217 S216
RX_A_3_P RX_A_3_N RX_A_CLK_P RX_A_CLK_N RX_A_2_P RX_A_2_N RX_A_1_P RX_A_1_N RX_A_0_P RX_A_0_N
S233 S215 PANEL_VCC
R733 10k 3V3_VCC
R734 10k PANEL_VCC
F192 60R OPTION3
R749 10k 3V3_VCC
R759 10k PANEL_VCC
F204 60R
CN104
PANEL_VCC
CN105

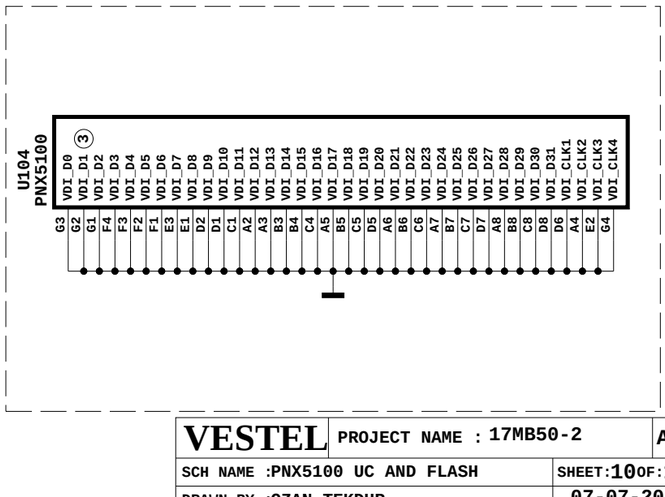
| | | |



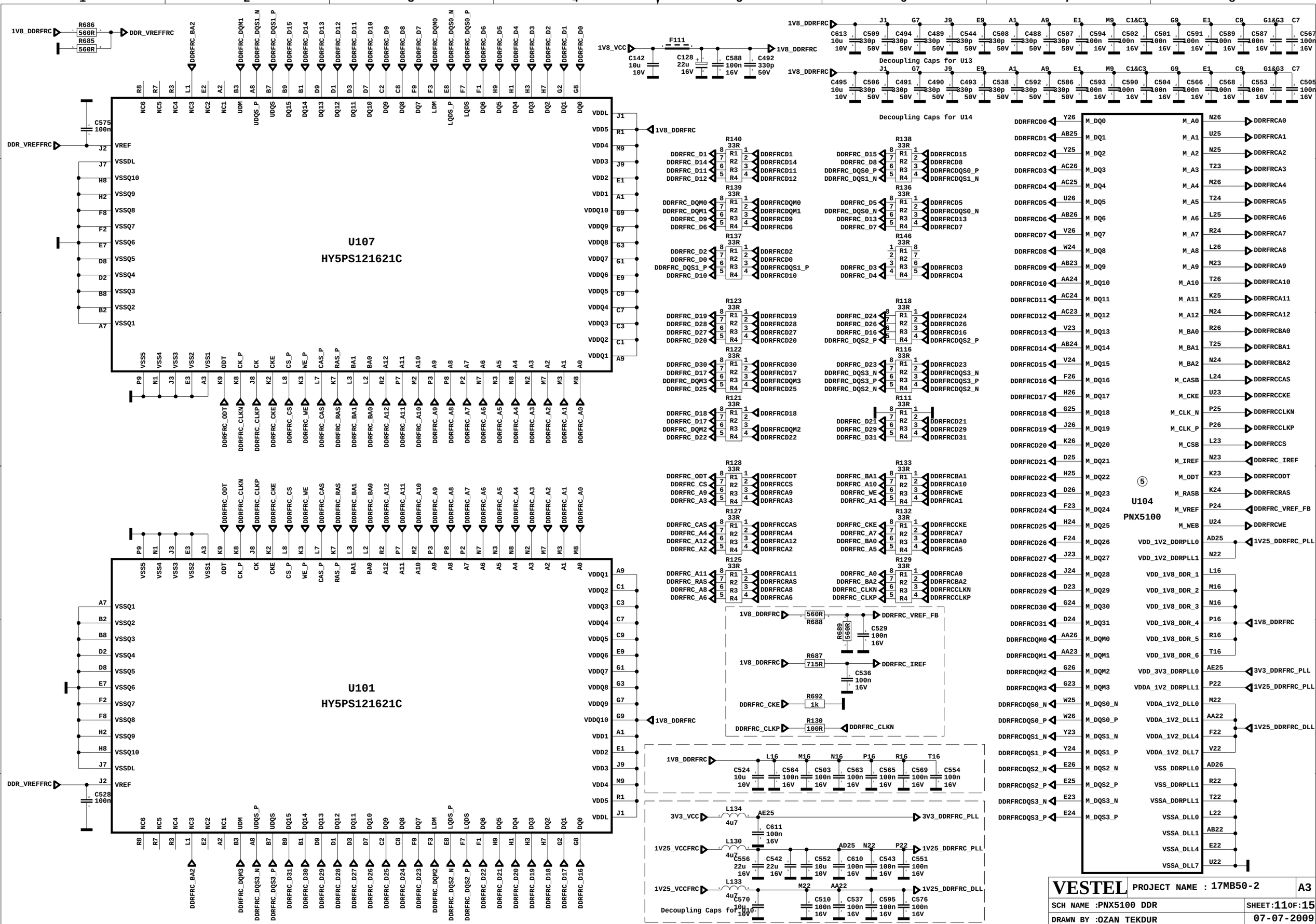


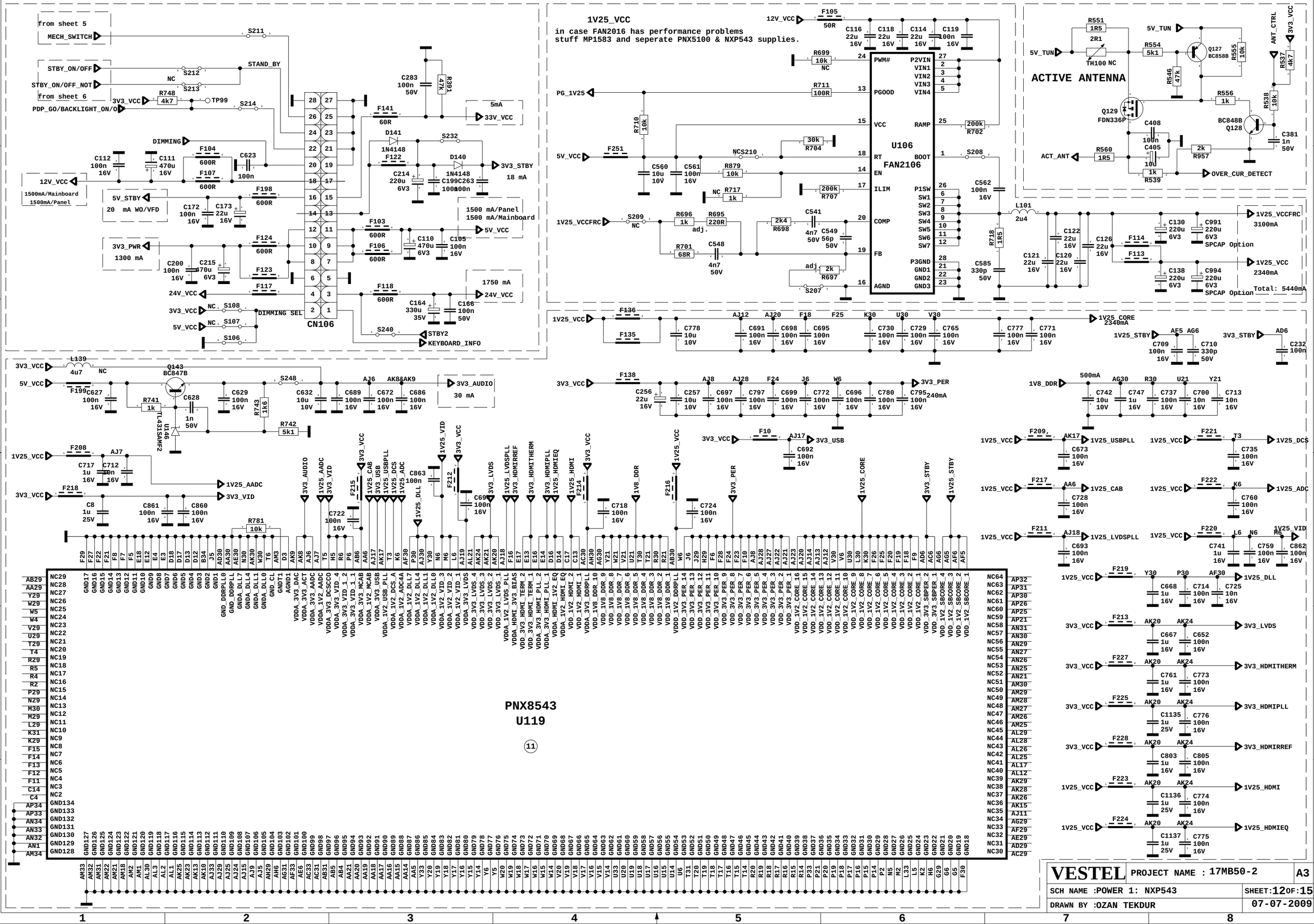






VESTEL	PROJECT NAME : 17MB50-2	A3
SCH NAME :PNX5100 UC AND FLASH	SHEET:10 OF:15	
DRAWN BY :OZAN TEKDUR	07-07-2009	


U107
HY5PS121621C
U101
HY5PS121621C
U104
PNX5100
Decoupling Caps for U13
Decoupling Caps for U14
Decoupling Caps for U10
1V8_DDRFRC
DDR_VREFFRC
1V8_VCC
3V3_VCC
1V25_VCCFRC
DDRFRC_VREF_FB
DDRFRC_IREF
DDRFRC_CLKN
DDRFRC_CLKP
DDRFRC_CKE
3V3_DDRFRC_PLL
1V25_DDRFRC_PLL
1V25_DDRFRC_DLL
VESTEL
PROJECT NAME : 17MB50-2
A3
SCH NAME :PNX5100 DDR
SHEET:11OF:15
DRAWN BY :OZAN TEKDUR
07-07-2009



VESTEL	PROJECT NAME : 17MB50-2		A3
SCH NAME :POWER 1: NXP543		SHEET:12 OF:15	
DRAWN BY :OZAN TEKDUR		07-07-2009	

PNX5100 Voltage In & Decoupling Caps

U104 PNX5100 (7)

1V25_COREFRC
1V25_UIP_PLL
3V3_PERFRC
C601 100n 16V
L137 4u7
C615 100n 16V

F194 50R
1V25_VCCFRC
F193 50R
C624 10u 10V
C625 10u 10V
C514 100n 16V
C557 100n 16V
C607 100n 16V
C555 100n 16V
C619 100n 16V
C608 100n 16V
C609 100n 16V
C521 100n 16V
C606 100n 16V
C622 100n 16V
C530 100n 16V
C531 100n 16V
C604 100n 16V
C599 100n 16V
C520 100n 16V
C605 100n 16V
1V25_COREFRC 3100mA

F190
3V3_VCC
C487 10u 10V
C499 10u 10V
C596 100n 16V
C546 100n 16V
C511 100n 16V
C522 100n 16V
C498 100n 16V
C545 100n 16V
C617 100n 16V
C618 100n 16V
C582 100n 16V
C572 100n 16V
C525 100n 16V
3V3_PERFRC 250mA

L129 4u7
1V25_VCCFRC
C527 10u 10V
C534 10u 10V
C532 100n 16V
C526 100n 16V
1V25_TRI_PLL1

L131 4u7
C547 10u 10V
C540 10u 10V
C533 100n 16V
C539 100n 16V
1V25_TRI_PLL2

L132 4u7
C573 10u 10V
C583 10u 10V
C571 100n 16V
C558 100n 16V
1V25_TRI_PLL3

F197
1V25_VCCFRC
C580 10u 10V
C616 100n 16V
C602 100n 16V
1V25_UIP_PLL

L138 4u7
C581 10u 10V
C603 100n 16V
1V25_XTAL

L136 4u7
3V3_VCC
C579 10u 10V
C600 100n 16V
3V3_SYS_PLL

F125 50R
3V3_PWR
C186 100n 16V
R233 4k7
Q105 FDC642P
TP147
R216 10k
3V3_VCC 1300mA
S114 NC
R234 4k7
C187 100n 16V
C995 220u 6V3 SPCAP Option
ENABLE_3V3
R250 100R
Q107 BC848B
C201 100n 16V
R971 4k7

U154 LM1117
12V_VCC
C1132 100u 16V
R1143 2k
C1133 10u 16V
C1138 22u
8V_VCC
R1144 10k
R1145 1k

Q178 BC847B
Q177 BC847B
3V3_VCC
F242
C899 100n 16V
S254
1V25_VID 30 mA
C898 100n 16V
R976 560R
R975 560R
R970 1k
R977 560R
C900 1n
U148 TL431SAMF2
R974 22k
Optional Clean 1V25_VID Supply

Can-Tuner Clean 5V Supply Option

5V_VCC
F128
C426 4u7 35V
C237 1u 16V
C996 220u 6V3 SPCAP Option
U128 LM317T
12V_VCC
C255 100u 16V
F132
TUNER_SUPPLY
C236 100u 16V
C458 100n 16V
R340 1k
R339 1k6
R338 560R
F133
TUNER_SUPPLY
F129
5V_VCC
C245 100u 16V
C264 1u 16V
C291 100n 16V
C993 220u 6V3 SPCAP Option
5V_TUN

SHORT CIRCUIT DETECTION

3V3_VCC
R965 5k6
R947 5k1
1V25_VCC
R920 100R
Q173 BC858B
R885 1k
D160 BAW56
R912 47k
R919 100R
Q161 BC848B
R911 47k
3V3_STBY
R403 10k NC
R402 10k
Q119 BC858B
3V3_VCC
R942 6k8
R874 10k
1V8_VCC
R917 100R
Q168 BC858B
R884 1k
R428 10k
R909 47k
R918 100R
Q164 BC848B
R910 47k
R420 10k
R423 10k
1N4148 D104
3V3_VCC
R1100 5k6
R1033 3k3
1V0_VCC
R1037 100R
Q183 BC858B
R1066 1k
R1057 47k
R1056 47k
R1036 100R
Q179 BC848B
Q122 BC848B
PROTECT_DETECT
PROTECTION
D203 BAW56
R1146 1k
8V_VCC
R1147 3k3
D103 BAW56
47k
R293
24V_VCC
10k
R300
R311 10k
R317 18k
12V_VCC
D104 BAW56
R280 1k
5V_VCC
R279 3k3
R294 3k3
R295 1k
TUNER_SUPPLY
3V3_VCC
D115 BAW56
R1011 4k7
R1017 22k
16V_VCC
R937 18k
AMP_PROTECT2
R873 10k
R872 10k
D161 BAW56
R936 18k
AMP_PROTECT1

1V8_VCC

12V_VCC
F129 60R
P6_1V25
STBY_ON/OFF_NOT
R791 10k
C702 100n 16V
S222
R771 1k NC
C640 22n 16V
U113 MP1583
BS SS IN EN SW COMP GND FB
C701 100n 16V
C703 10n 16V
C704 10n 16V
R803 7k5
C143 22u
C152 22u
C157 100n
R788 10k
S223
R787 5k1
C992 SPCAP Option 220u
D101 SS33
L103 22u
C137 22u 16V
C136 22u 16V
C139 100n 16V
TP150
1V8_VCC 1970mA

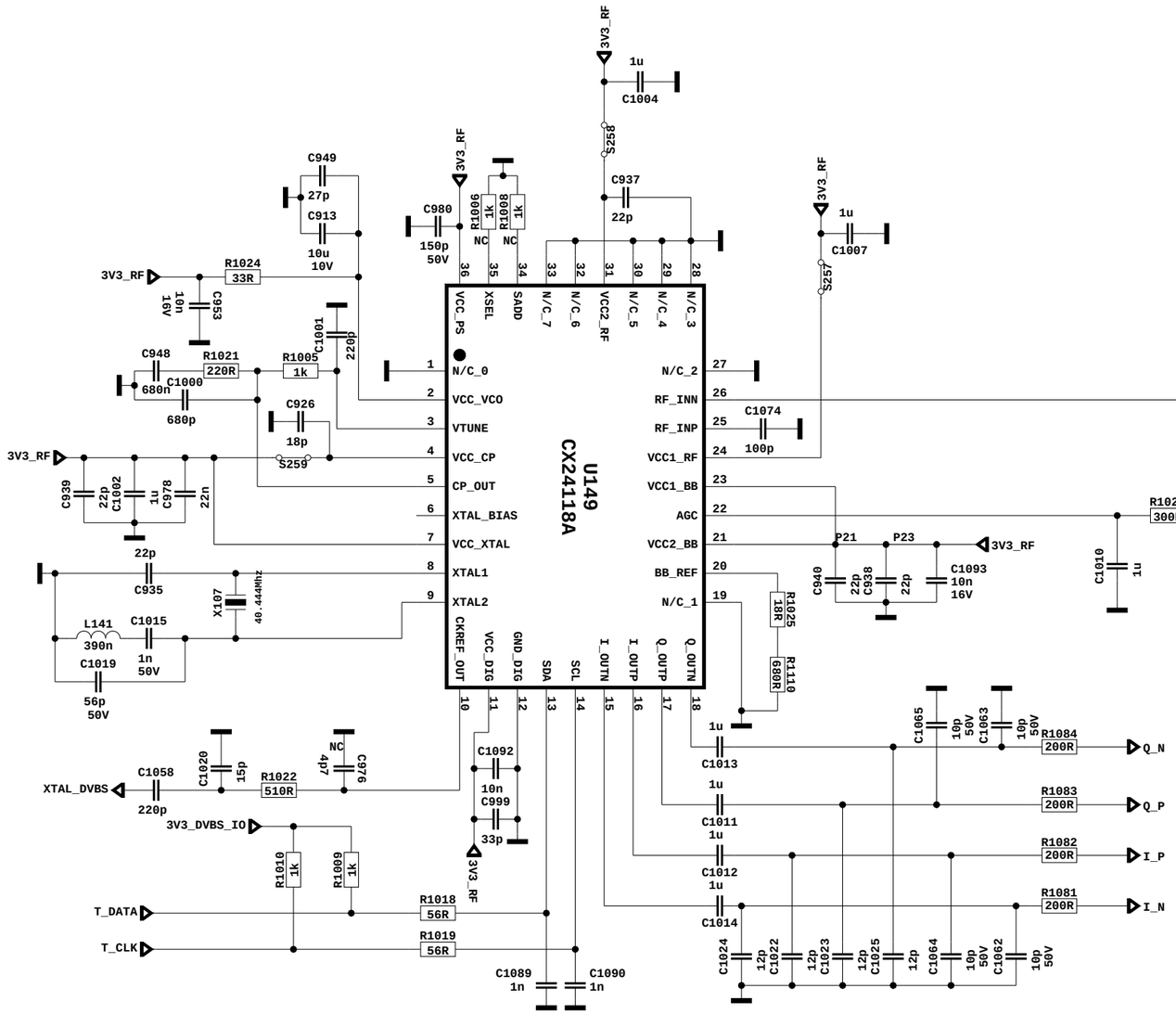
OPTIONAL 1V25 Supply (WO\PNX5100)

12V_VCC
F110 60R
C647 100n
R770 47k
STBY_ON/OFF_NOT
R764 47k NC
C642 22n 16V
U110 MP1583
BS SS IN EN SW COMP GND FB
C641 100n 16V
C661 10n 16V
C662 10n 16V
R778 7k5
C131 22u
C125 22u
C663 100n
R776 22k
R783 4k7
R777 1k
TP151
D100 SS33
L102 22u
C129 22u 16V
C133 22u 16V
C670 100n 16V
1V25_VCC 2340mA

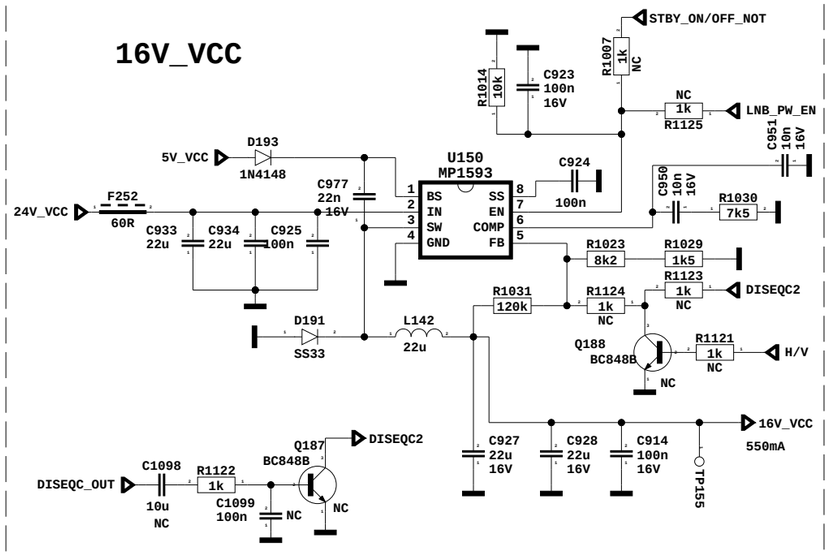
1V25_STBY

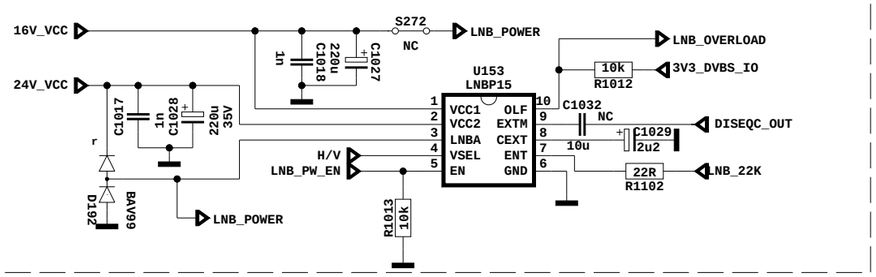
3V3_STBY
L140 4u7
C716 1u 16V
C715 100n 16V
U147 XC6215
VIN VOUT CE VSS
10k R799
F205
1V25_STBY 10mA
C684 1u 16V
C711 100n 16V

VESTEL	PROJECT NAME : 17MB50-2	A3
SCH NAME :POWER 2: PNX5100	SHEET:13 OF:15	
DRAWN BY :OZAN TEKDUR	07-07-2009	

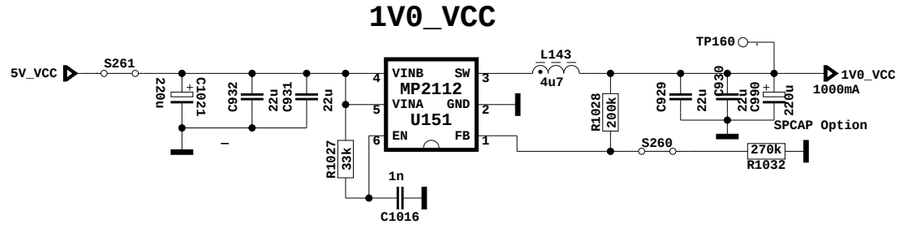


16V_VCC

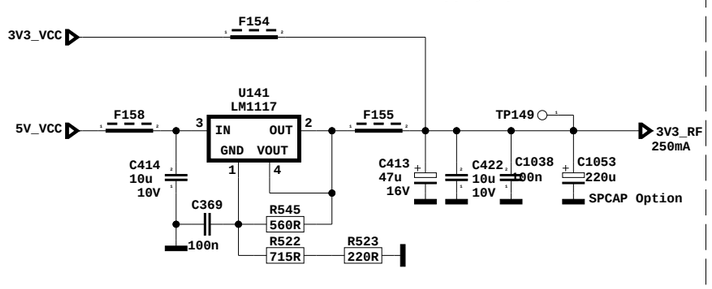




1V0_VCC



DVB-S2 Tuner Clean 3V3 Supply Option



VESTEL	PROJECT NAME : 17MB50-2	A3
	SCH NAME :DVB-S2 TUNER & POWER	SHEET:14 OF:15
	DRAWN BY :RECEP KIZILKAYA	07-07-2009

CX24120
U152

Power supply

1V0_VCC — F243 — P34 P28 P22 P16 P10 P4 P41 P48 P104 P97 P140 P133 P127 P120 P112 — 1V0_DVBS
C1069 10u 16V; C956 C962 C965 C964 C967 C907 C957 C909 C966 C908 C970 C971 C968 C969 10n; 10u

3V3_VCC — F254 — P18 P32 P43 114 P138 P125 P6 P102 — 3V3_DVBS_IO
C1067 10u 16V; C952 C959 C958 C961 C963 C973 C960 C974 10n

3V3_RF — F263 — NC
3V3_VCC — F258 — 3V3_DVBS_VDD
C1088 220u 6V3 SPCAP Option
C1068 10u 16V; C975 C972 10n

1V0_DVBS 3V3_DVBS_IO 1V0_DVBS DVBS_TS9 DVBS_TS10 1V0_DVBS 3V3_DVBS_IO DVBS_TS11 DVBS_TS12 1V0_DVBS DVBS_TS13 DVBS_TS14 DVBS_TS15 3V3_DVBS_IO 1V0_DVBS DVBS_TS16 DVBS_TS17

GPIO4 (pin 37)

LNB_OVERLOAD — R1041 100R
GPIO4 — 37 — GPIO3 36 — GPIO2 35 — 1V0_VDD_5 34 — GND_5 33 — 3V3_IO_VDD_2 32 — IO_GND_2 31 — GPIO1 30 — GPIO0 29 — 1V0_VDD_4 28 — GND_4 27 — RS_DFAIL 26 — RS_DVAL 25 — RS_START 24 — NC_1 23 — 1V0_VDD_3 22 — GND_3 21 — NC_0 20 — RS_CLK 19 — 3V3_IO_VDD_1 18 — IO_GND_1 17 — 1V0_VDD_2 16 — GND_2 15 — RS_DATA0 14 — RS_DATA1 13 — RS_DATA2 12 — RS_DATA3 11 — 1V0_VDD_1 10 — GND_1 9 — RS_DATA4 8 — RS_DATA5 7 — 3V3_IO_VDD_0 6 — IO_GND_0 5 — 1V0_VDD_0 4 — GND_0 3 — RS_DATA6 2 — RS_DATA7 1 — RS_DATA7

GPIO5 (pin 38)

H/V — R1040 100R
GPIO5 — 38

NC_2 (pin 39)

R1106 4k7; R1004 1k
3V3_DVBS_IO
NC_2 — 39

GND_6 (pin 40)

GND_6 — 40

1V0_VDD_6 (pin 41)

1V0_DVBS
1V0_VDD_6 — 41

IO_GND_3 (pin 42)

IO_GND_3 — 42

3V3_IO_VDD_3 (pin 43)

3V3_DVBS_IO
R1104 4k7
3V3_IO_VDD_3 — 43

NC_3 (pin 44)

NC_3 — 44

NC_4 (pin 45)

NC_4 — 45

LNB_DC (pin 46)

3V3_DVBS_IO
R1103 100R
LNB_22K
LNB_DC — 46

GND_7 (pin 47)

GND_7 — 47

1V0_VDD_7 (pin 48)

1V0_DVBS
1V0_VDD_7 — 48

LNB_22K_TX (pin 49)

3V3_DVBS_IO
R1045 4k7
DISEQC_OUT
LNB_22K_TX — 49

AGND_0 (pin 50)

AGND_0 — 50

NC_5 (pin 51)

NC_5 — 51

NC_6 (pin 52)

NC_6 — 52

3V3_APWR_0 (pin 53)

3V3_DVBS_VDD — F247 60R — C1006 1u; C941 470n; C919 100n
3V3_APWR_0 — 53

NC_7 (pin 54)

NC_7 — 54

NC_8 (pin 55)

NC_8 — 55

NC_9 (pin 56)

NC_9 — 56

NC_10 (pin 57)

3V3_DVBS_VDD — F245 60R — C942 470n; C917 100n
NC_10 — 57

3V3_APWR_1 (pin 58)

3V3_APWR_1 — 58

NC_11 (pin 59)

NC_11 — 59

NC_12 (pin 60)

NC_12 — 60

AGND_1 (pin 61)

AGND_1 — 61

LNB_22K_RX (pin 62)

LNB_22K_RX — 62

NC_13 (pin 63)

NC_13 — 63

BG_EXT (pin 64)

R1076 470R; R1101 27k; C1048 10n
BG_EXT — 64

3V3_BG_VAA (pin 65)

C1047 100n
3V3_DVBS_VDD — F260 60R
3V3_BG_VAA — 65

BG_GND (pin 66)

BG_GND — 66

3V3_XTAL_VAA (pin 67)

3V3_DVBS_VDD — F259 60R — C1046 100n; C1073 100p
3V3_XTAL_VAA — 67

XTAL_IN (pin 68)

XTAL_DVBS
XTAL_IN — 68

XTAL_OUT (pin 69)

XTAL_OUT — 69

XTAL_GND (pin 70)

XTAL_GND — 70

PLL_GND (pin 71)

PLL_GND — 71

3V3_PLL_VAA (pin 72)

3V3_DVBS_VDD — F249 60R — C1005 1u; C943 470n; C921 100n
3V3_PLL_VAA — 72

AGND_2 (pin 73)

AGND_2 — 73

NC_14 (pin 74)

NC_14 — 74

NC_15 (pin 75)

NC_15 — 75

3V3_APWR_2 (pin 76)

3V3_DVBS_VDD — F248 60R — C1009 1u; C944 470n; C920 100n
3V3_APWR_2 — 76

NC_16 (pin 77)

NC_16 — 77

NC_17 (pin 78)

NC_17 — 78

NC_18 (pin 79)

NC_18 — 79

NC_19 (pin 80)

NC_19 — 80

3V3_APWR_3 (pin 81)

3V3_DVBS_VDD — F244 60R — C945 470n; C916 100n
3V3_APWR_3 — 81

NC_20 (pin 82)

NC_20 — 82

NC_21 (pin 83)

NC_21 — 83

AGND_3 (pin 84)

AGND_3 — 84

I_P (pin 86)

I_P — 86 — Q_N

I_N (pin 87)

I_N — 87 — Q_P

3V3_ADC_I_VAA (pin 88)

3V3_DVBS_VDD — F246 60R — C1008 1u; C946 470n; C918 100n
3V3_ADC_I_VAA — 88

3V3_ADC_I_VDD (pin 89)

3V3_ADC_I_VDD — 89

ADC_I_DGND (pin 90)

ADC_I_DGND — 90

ADC_Q_DGND (pin 91)

ADC_Q_DGND — 91

3V3_ADC_Q_VDD (pin 92)

3V3_DVBS_VDD — F250 60R — C947 470n; C922 100n
3V3_ADC_Q_VDD — 92

3V3_ADC_Q_VAA (pin 93)

3V3_ADC_Q_VAA — 93

Q_N (pin 94)

Q_N — 94 — I_P

Q_P (pin 95)

Q_P — 95 — I_N

ADC_Q_AGND (pin 96)

ADC_Q_AGND — 96

1V0_VDD_8 (pin 97)

1V0_VDD_8 — 97 — 1V0_DVBS

GND_8 (pin 98)

GND_8 — 98 — R1063 1k — 3V3_DVBS_IO

NC_22 (pin 99)

NC_22 — 99

NC_23 (pin 100)

NC_23 — 100

NC_24 (pin 101)

NC_24 — 101

3V3_IO_VDD_4 (pin 102)

3V3_IO_VDD_4 — 102 — 3V3_DVBS_IO

IO_GND_4 (pin 103)

IO_GND_4 — 103

1V0_VDD_9 (pin 104)

1V0_VDD_9 — 104 — 1V0_DVBS

GND_9 (pin 105)

GND_9 — 105

TUNER_CLK (pin 106)

TUNER_CLK — 106 — T_CLK

TUNER_DATA (pin 107)

TUNER_DATA — 107 — T_DATA

AGCV (pin 108)

AGCV — 108 — R1085 300R — AGC_S
C1013 100n; C1009 1u

NC_25 (pin 109)

NC_25 — 109

NC_26 (pin 110)

NC_26 — 110

GPIO6 (pin 111)

R1039 100R — LNB_PW_EN
R1046 4k7 — 3V3_DVBS_IO
GPIO6 — 111

1V0_VDD_10 (pin 112)

1V0_VDD_10 — 112 — 1V0_DVBS

GND_10 (pin 113)

GND_10 — 113

3V3_IO_VDD_5 (pin 114)

3V3_IO_VDD_5 — 114 — 3V3_DVBS_IO

IO_GND_5 (pin 115)

IO_GND_5 — 115

GPIO7 (pin 116)

GPIO7 — 116

RESET (pin 117)

R1038 100R — RESET_IC
RESET — 117

INTERRUPT (pin 118)

R1053 4k7 — 3V3_DVBS_IO
R1044 100R — DVBS_INT
INTERRUPT — 118

SER_ADD5 (pin 119)

R1063 10k — 1V0_DVBS
SER_ADD5 — 119

1V0_VDD_11 (pin 120)

1V0_VDD_11 — 120

GND_11 (pin 121)

GND_11 — 121

SER_ADD7 (pin 122)

R1064 10k — 3V3_DVBS_IO
SER_ADD7 — 122

SER_DATA (pin 123)

R1043 100R — SDA
SER_DATA — 123

SER_CLK (pin 124)

R1042 100R — SCL
SER_CLK — 124

3V3_IO_VDD_6 (pin 125)

3V3_IO_VDD_6 — 125 — 3V3_DVBS_IO

IO_GND_6 (pin 126)

IO_GND_6 — 126

1V0_VDD_12 (pin 127)

1V0_VDD_12 — 127 — 1V0_DVBS

GND_12 (pin 128)

GND_12 — 128

TRS (pin 129)

4k7 R1048 — TRS — 129

TMS (pin 130)

TMS — 130

TDO (pin 131)

R1065 10k — 3V3_DVBS_IO
TDO — 131

TDI (pin 132)

TDI — 132

1V0_VDD_13 (pin 133)

1V0_VDD_13 — 133 — 1V0_DVBS

GND_13 (pin 134)

3V3_DVBS_IO
GND_13 — 134

TCK (pin 135)

4k7 R1047 — TCK — 135

TEST3 (pin 136)

4k7 R1050 — TEST3 — 136

TEST2 (pin 137)

4k7 R1049 — TEST2 — 137

3V3_IO_VDD_7 (pin 138)

3V3_DVBS_IO
3V3_IO_VDD_7 — 138

IO_GND_7 (pin 139)

IO_GND_7 — 139

1V0_VDD_14 (pin 140)

1V0_DVBS
1V0_VDD_14 — 140

GND_14 (pin 141)

GND_14 — 141

TEST1 (pin 142)

4k7 R1052 — TEST1 — 142

TEST0 (pin 143)

4k7 R1051 — TEST0 — 143

NC_27 (pin 144)

NC_27 — 144

R1015 56R

DVBS_TS0 — 8 R1 1 — TS0
DVBS_TS3 — 7 R2 2 — TS3
DVBS_TS7 — 6 R3 3 — TS7
DVBS_TS2 — 5 R4 4 — TS2

R1016 56R

DVBS_TS6 — 8 R1 1 — TS6
DVBS_TS5 — 7 R2 2 — TS5
DVBS_TS4 — 6 R3 3 — TS4
DVBS_TS1 — 5 R4 4 — TS1

R1058 56R

DVBS_TS9 — 8 R1 1 — TS9
DVBS_TS8 — 7 R2 2 — TS8
DVBS_TS10 — 6 R3 3 — TS10
4 R4 5

TS[0:10]

