TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA76L431FT,TA76L431S

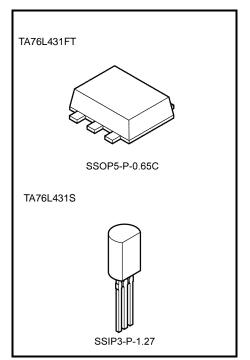
2.49-V Adjustable High-Precision Shunt Regulators

These devices are adjustable high-precision shunt regulators whose output voltage (V_{KA}) can be set arbitrarily using two external resistors.

The devices have a precise internal reference voltage of 2.49 V, enabling them to operate at low voltage. In addition, they can be used as zener diodes to perform temperature compensation.

Features

- Precision reference voltage : $V_{REF} = 2.49V \pm 1.0\%$ (Ta = 25°C)
- Adjustable output voltage $: V_{REF} \le V_{OUT} \le 19 \text{ V}$
- Minimum cathode current for regulation
 :I_{kmin} = 0.5 mA (max.)
- Operating temperature: $Ta = -40 \sim 85^{\circ}C$
- Packages: UFV (TA76L431FT), TO-92MOD (TA76L431S)
- The TA76L431FT is housed in an ultra-thin UFV package. (thickness: 0.7 mm typ.)

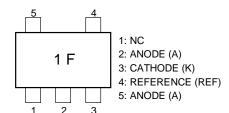


Weight

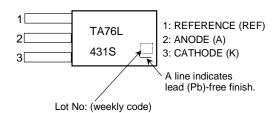
SSOP5-P-0.65C: 0.007 g (typ.) SSIP3-P-1.27: 0.36 g (typ.)

Pin Assignment/Marking





TA76L431S

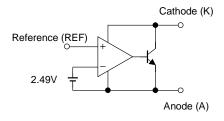


How to Order

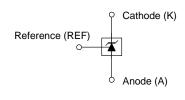
Product No.	Package Type	Packing Type and Capacity	Minimum Order
TA76L431FT (TE85L,F	UFV (surface-mount type)	Embossed tape: 3000/tape	1 tape
TA76L431S(Q)	TO-92MOD	Loose in bag: 200/bag	1 bag
TA76L431S (TPE6,Q)	(lead type)	Radial tape: 2000/tape	1 tape

Note: The lead pitch for the TA76L431S(Q) and TA76L431S(TPE6,Q) may vary.

Functional Block Diagram



Circuit Symbol

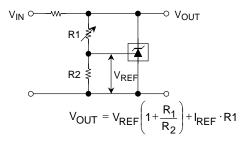


Typical Application Circuits

(1) 2.49 V Reference $(V_{KA} = V_{REF})$

V_{IN} V_{OUT} = 2.49 V

(2) Shunt regulator $(V_{KA} > V_{REF})$



Precautions during Use

1. TA76L431FT, TA76L431S

These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.

2. TA76L431FT, TA76L431S

The oscillation frequency of these devices is determined by the value of the capacitor connected between the anode and the cathode.

When establishing maximum operating condition parameters, please derate the maximum rating values specified in these datasheets so as to allow an operational safety margin.

Use of a laminated ceramic capacitor is recommended

3. Precautions when handling anode pin of TA76L431FT

Pin 2 and pin 5 should normally be shorted together. If only pin 5 is used, pin 2 should either be left open or always kept at a lower potential than pin 5. Do not leave pin 5 open and use pin 2 only.

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Cathode voltage		V_{KA}	20	V	
Cathode current		Ι _Κ	50	mA	
Cathode-anode reverse current		-I _K	50	mA	
Reference voltage		V _{REF}	7	V	
Reference current		I _{REF}	50	μΑ	
Reference-anode reverse current		-I _{REF}	10	mA	
Power dissipation	TA76L431FT	P _D	0.45 (Note 1)	W	
	TA76L431S		0.8		
Thermal resistance	TA76L431FT	р.	277 (Note 1)	°C/W	
	TA76L431S	R _{th}	156		
Operating temperature		T _{opr}	-40~85	°C	
Junction temperature		Tj	150	°C	
Storage temperature		T _{stg}	-55~150	°C	

Note 1: Glass epoxy substrate mounting: $30 \text{ mm} \times 30 \text{ mm} \times 0.8 \text{ mmt}$ (Cu pad area 35 mm^2)

Recommended Operating Conditions

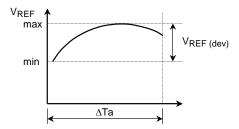
Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	V_{KA}	V_{REF}	_	19	V
Cathode current	Ι _Κ	0.5		40	mA
Operating temperature	T _{opr}	-40	_	85	°C

Electrical Characteristics (Unless otherwise specified, Ta = 25°C, $I_K = 10$ mA)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	V_{REF}	$V_{KA} = V_{REF}$	2.465	2.49	2.515	V
Deviation of reference input voltage over temperature	V _{REF (dev)}	$0^{\circ}\text{C} \le \text{Ta} \le 70^{\circ}\text{C}, V_{\text{KA}} = V_{\text{REF}}$	_	5	15	mV
Ratio of change in reference input voltage to the change in cathode voltage	ΔV _{REF} /ΔV	$V_{REF} \le V_{KA} \le 10 \text{ V}$	_	0.8	2.4	mV/V
		$10V \le V_{KA} \le 19 V$	_	0.8	2.0	
Reference Input current	I _{REF}	$V_{KA} = V_{REF}$	_	0.6	3	μА
Deviation of reference input current over temperature	I _{REF (dev)}	$\begin{array}{l} 0^{\circ}C \leqq Ta \leqq 70^{\circ}C, \ V_{KA} = V_{REF}, \\ R_{1} = 10 \ k\Omega, \ R_{2} = \infty \end{array}$	_	0.3	1.2	μΑ
Minimum cathode current for regulation	I _{Kmin}	V _{KA} = V _{REF}	_	0.2	0.5	mA
Off-State cathode current	I _{Koff}	$V_{KA} = 19 \text{ V}, V_{REF} = 0 \text{ V}$	_	_	1.0	μΑ
Dynamic impedance	Z _{KA}	$V_{KA} = V_{REF}, f \le 1 \text{ kHz},$ 0.5 mA $\le I_K \le 40 \text{ mA}$	_	0.2	0.5	Ω

The deviation parameters $V_{REF\,(dev)}$ and $I_{REF\,(dev)}$ are defined as the maximum variation of the V_{REF} and I_{REF} over the rated temperature range(Ta=0 to 70°C).

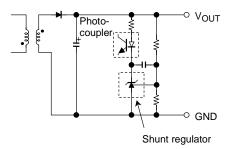
The average temperature coefficient of the $\ensuremath{V_{\rm REF}}$ is defined as:



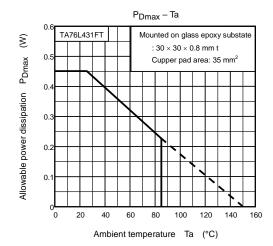
$$\left| \alpha V_{REF} \right| = \frac{\left(\frac{V_{REF \, (dev)} \times 10^6}{V_{REF} \, @25^{\circ}C} \right)}{\Delta Ta} \, (ppm/^{\circ}C)$$

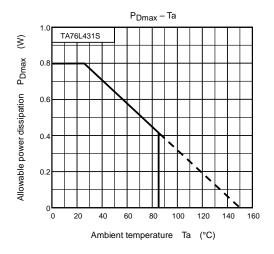
Application Circuit Example

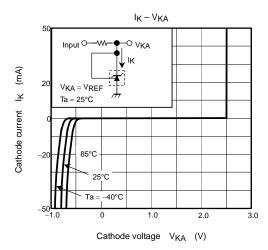
Error amplification circuit for switching power supply

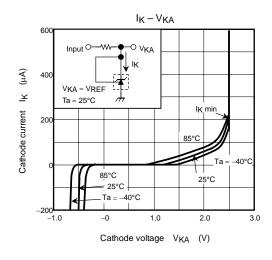


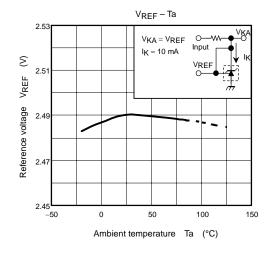
This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

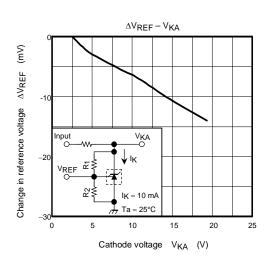


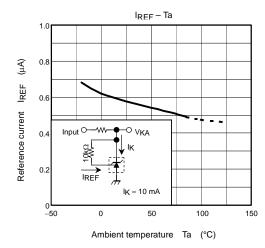


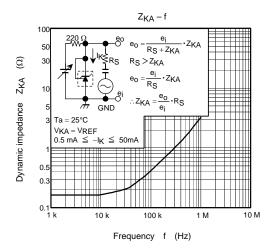


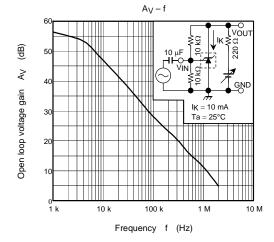


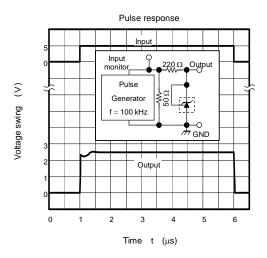


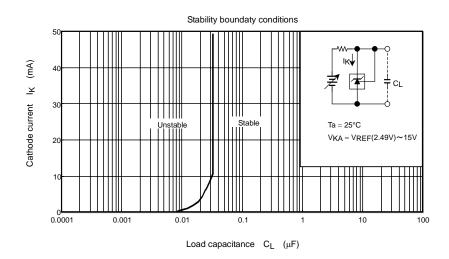








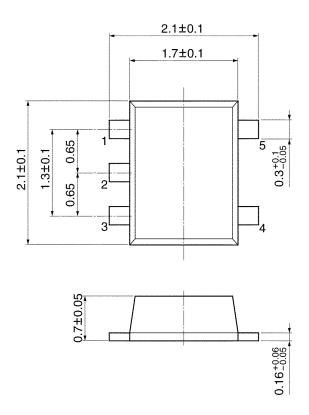




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Package Dimensions

SSOP5-P-0.65C Unit: mm

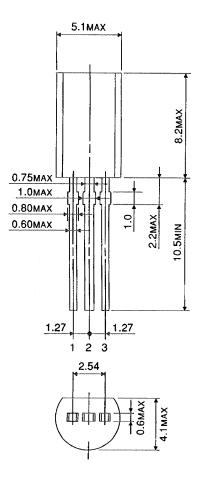


TA76L431FT (UFV)

Weight: 0.007 g (typ.)

Package Dimensions

SSIP3-P-1.27



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TA76L431S (TO-92MOD)

Weight: 0.36 g (typ.)

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