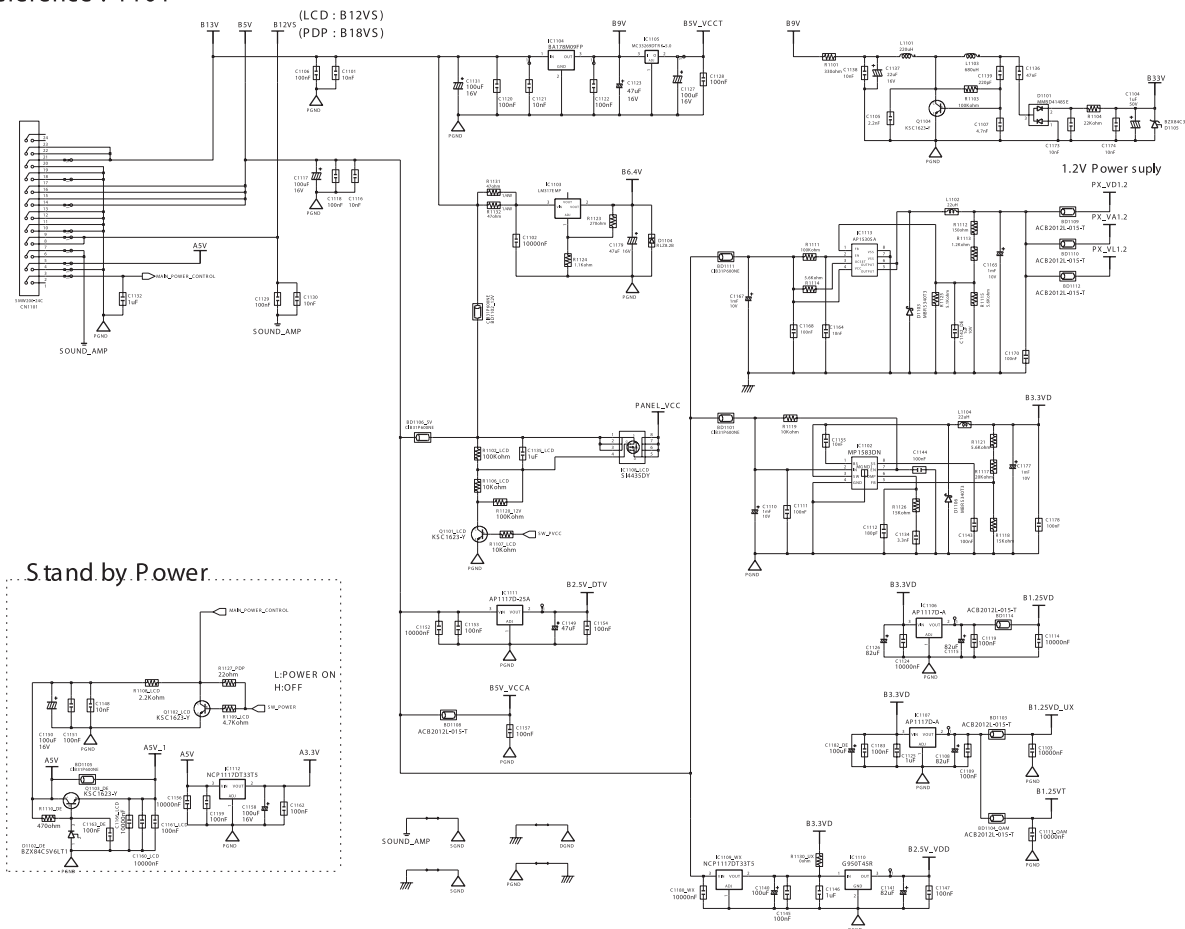


9 Schematic Diagrams

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9-1 Power Schematic Diagram

Reference : 1101~

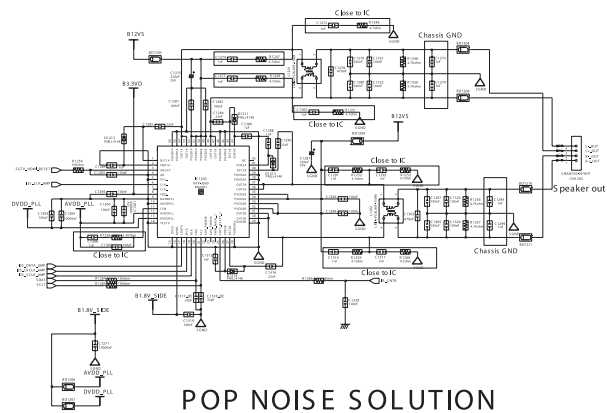


-This Document can not be used without Samsung' s authorization.

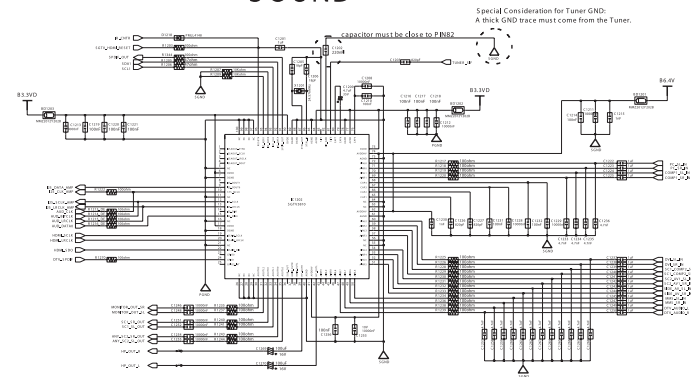
9-2 Sound Processing Schematic Diagram

Reference : 1201~

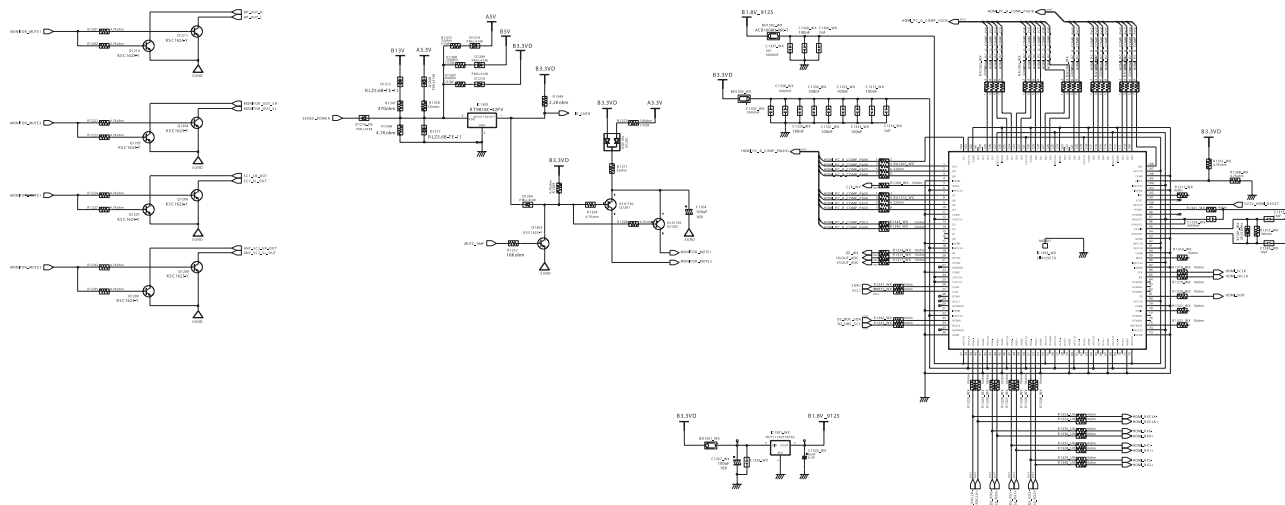
SOUND AMP



SOUND



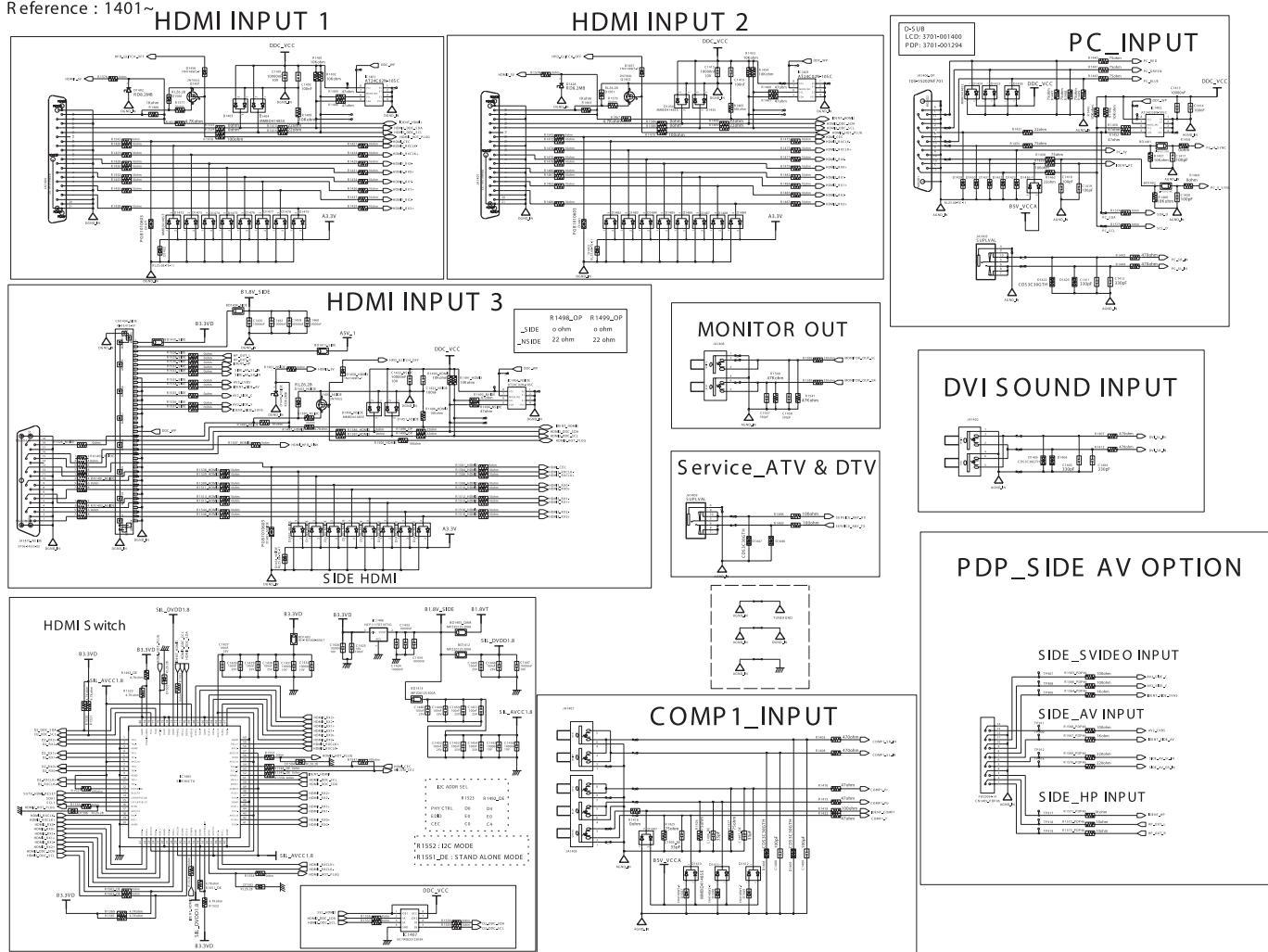
HDMI RECEIVER



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9-3 Input & Output Jack I Schematic Diagram

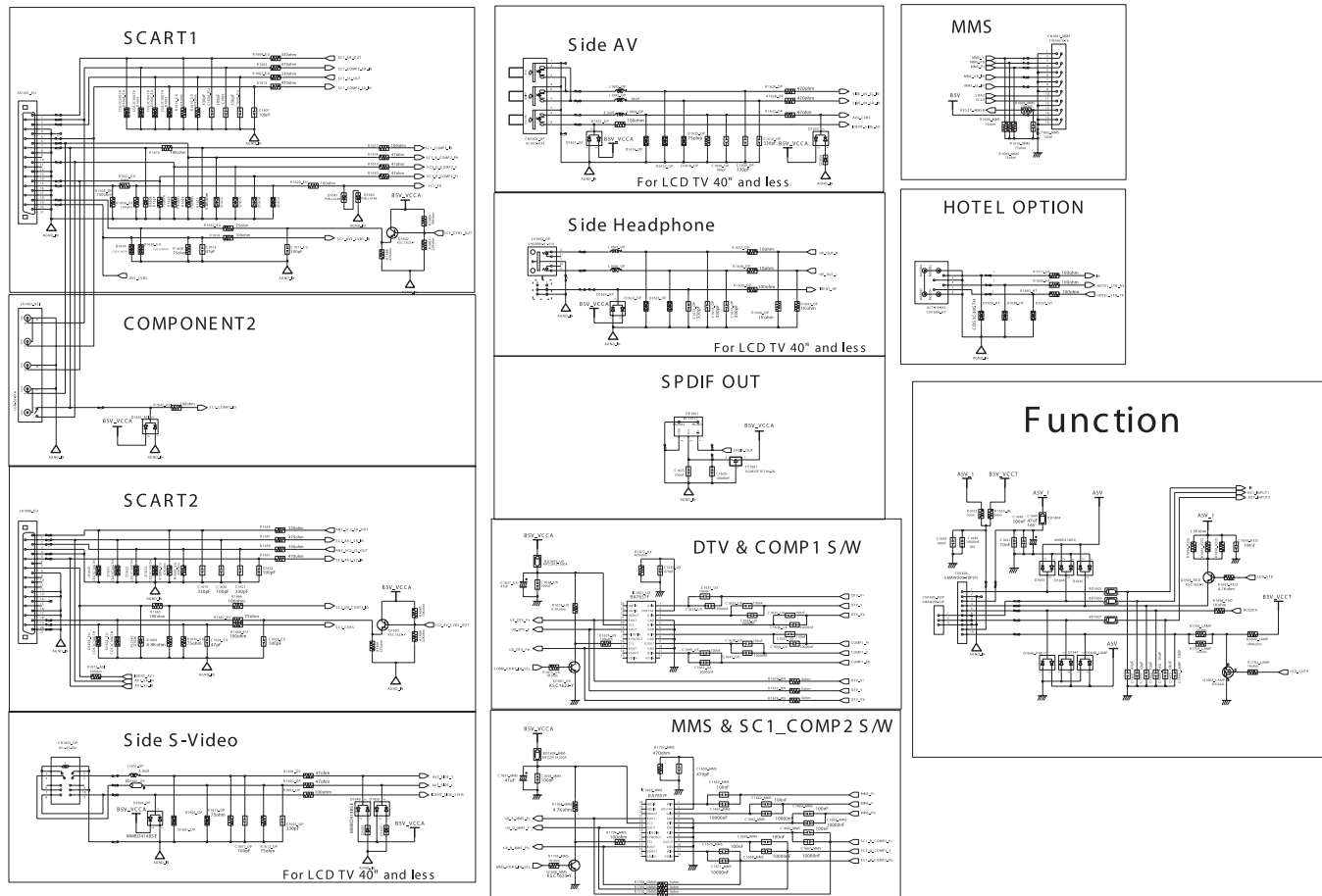
Reference : 1401~



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9-4 Input & Output Jack II Schematic Diagram

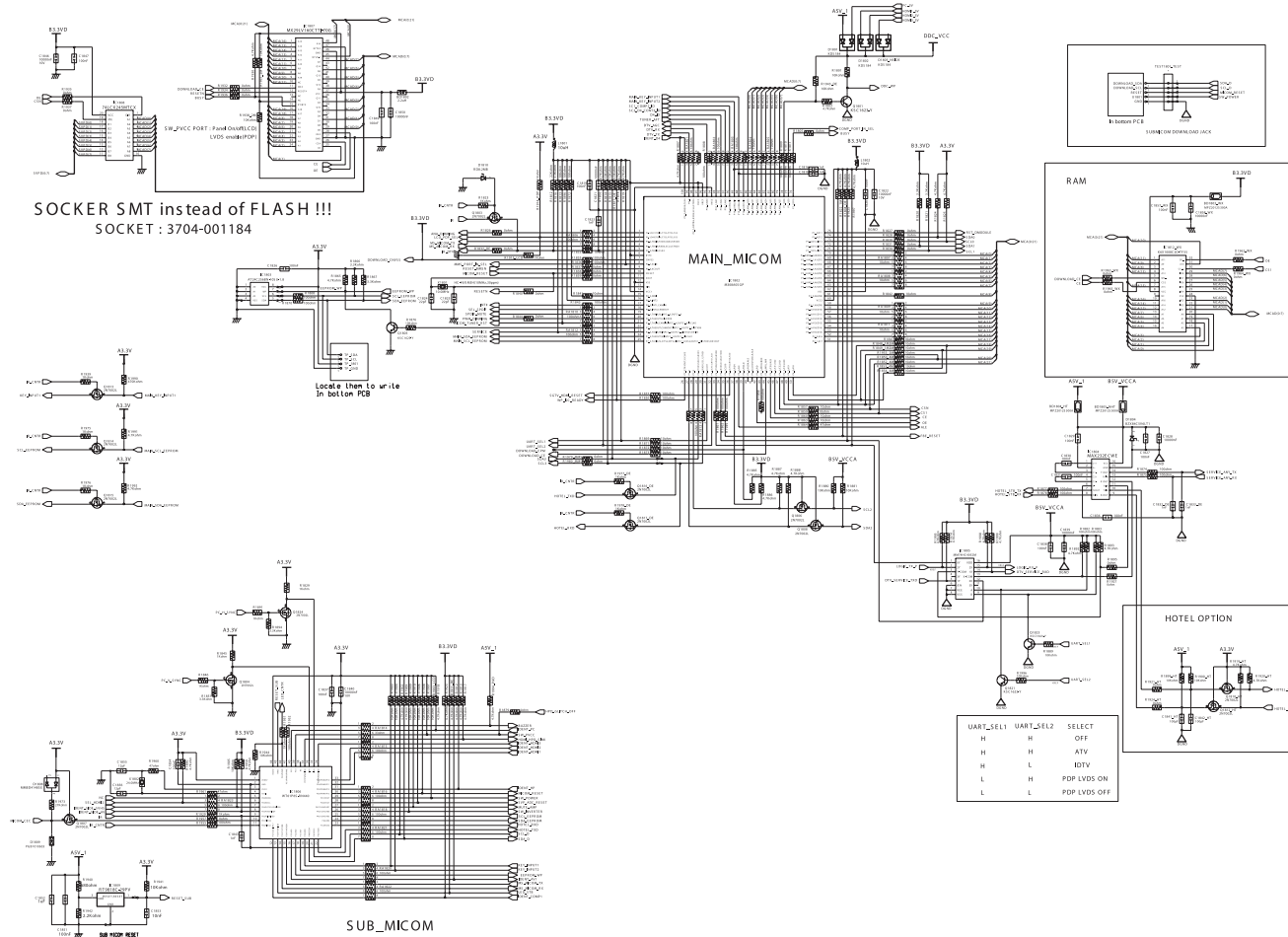
Reference : 1601~



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9-5 MICOM Schematic Diagram

Reference : 1801~

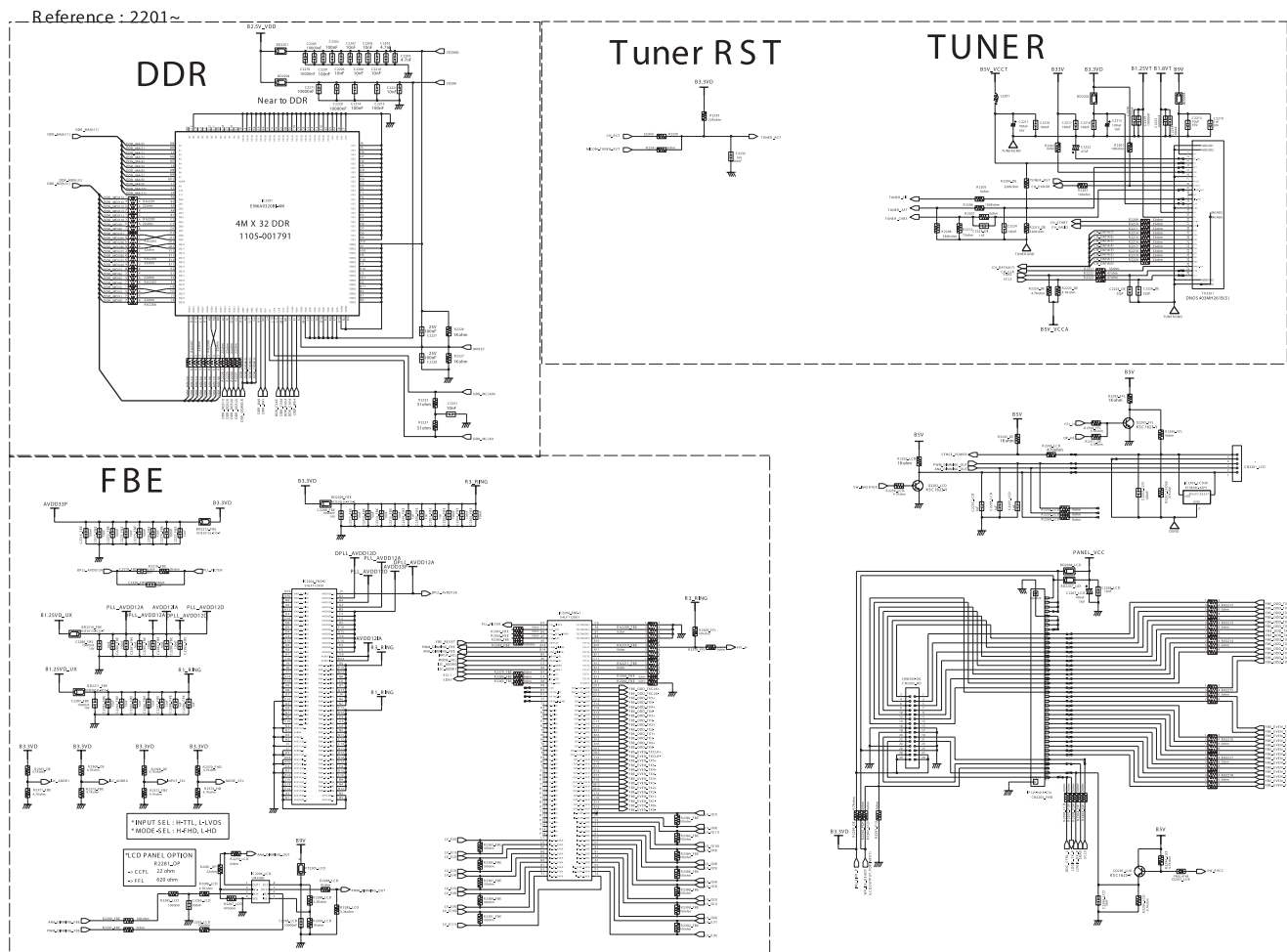


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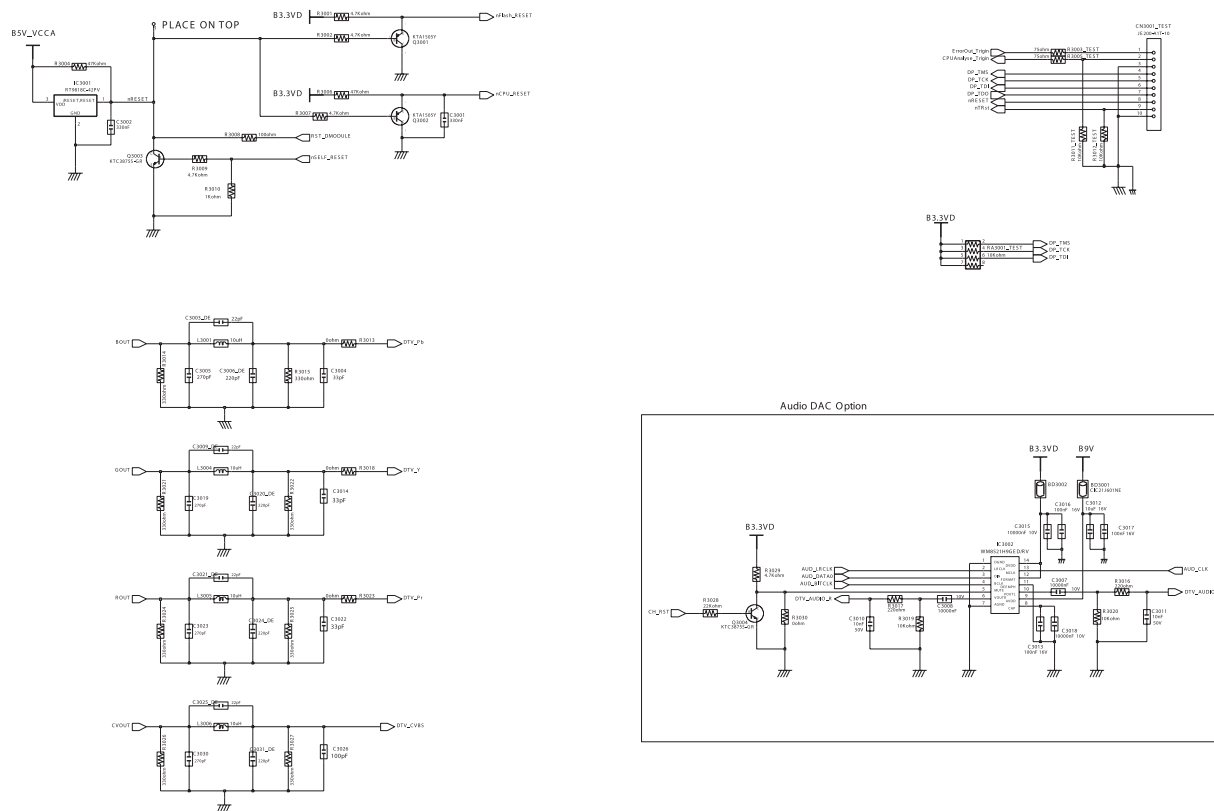
9-7 DDR & Tuner Schematic Diagram



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9-8 DTV Reset & AV out Schematic Diagram

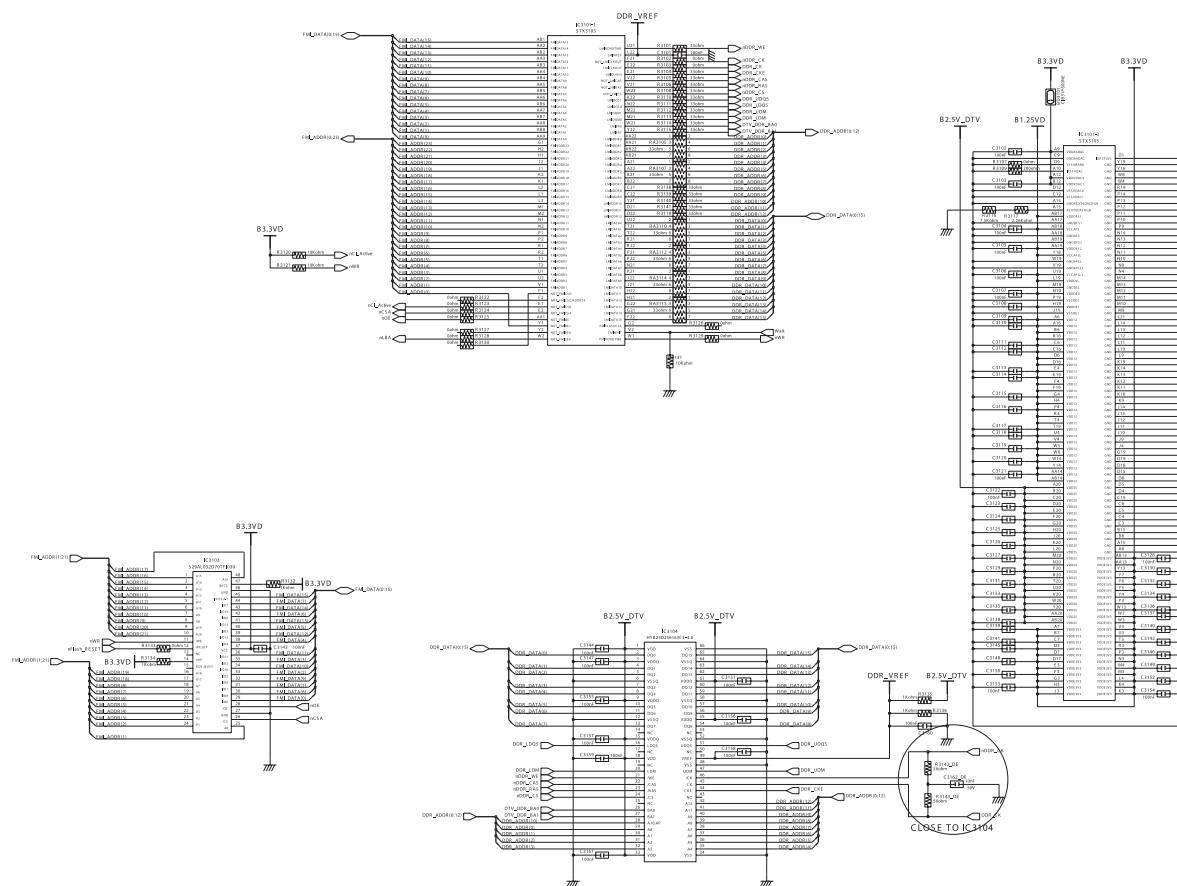
Reference : 3001~



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9-9 DTV Memory & STi5105 Schematic Diagram

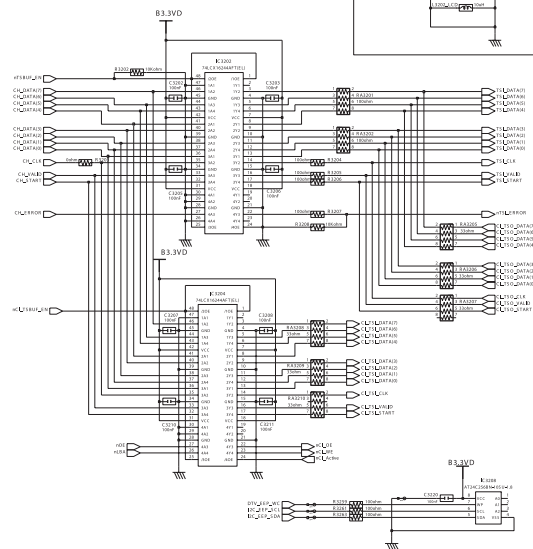
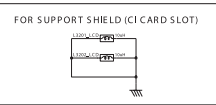
Reference : 3101~



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9-10 PCMCIA & STi5105 Schematic Diagram

Reference : 3201~



S/S 27MHz XTAL OPTION

