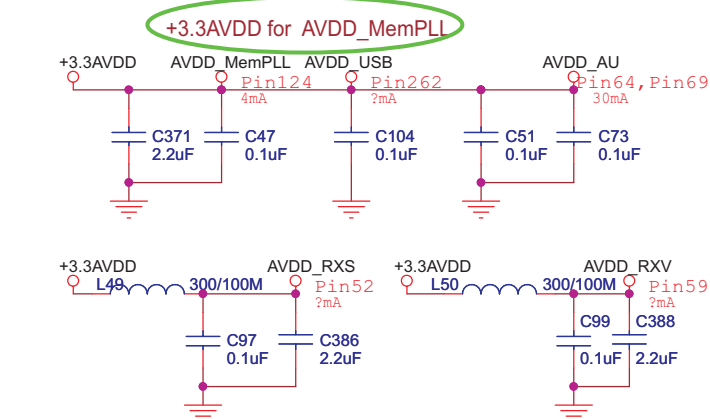
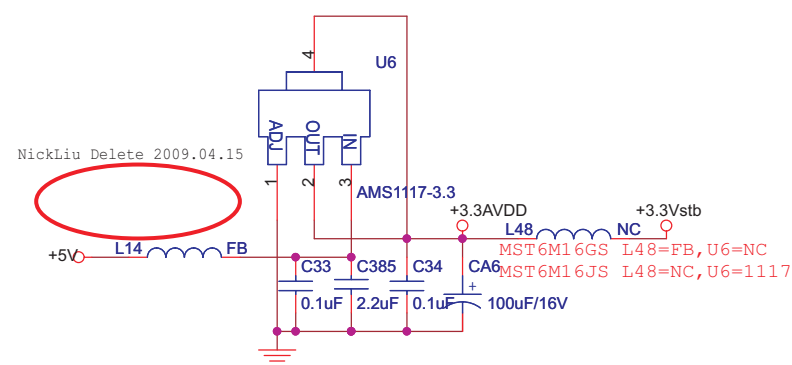
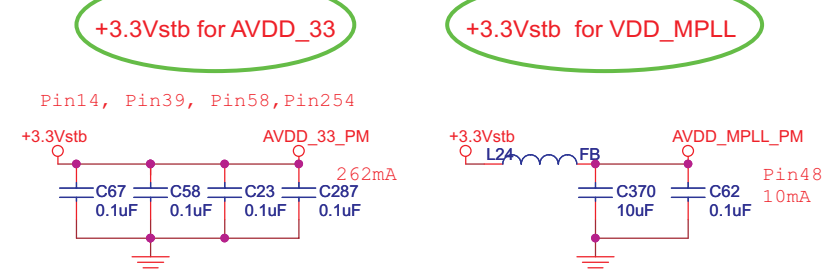
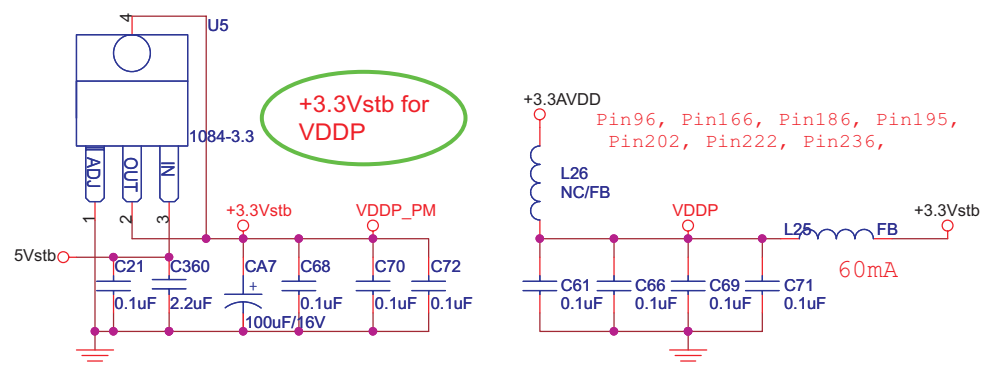
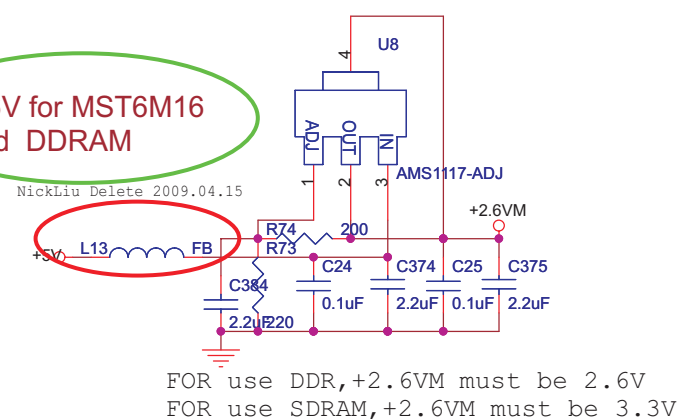
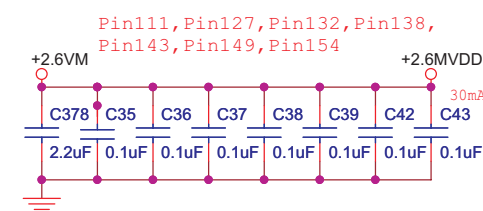




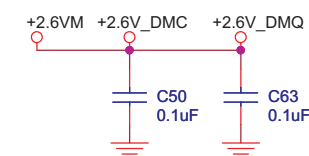
2.6V for MST6M16 and DDRAM



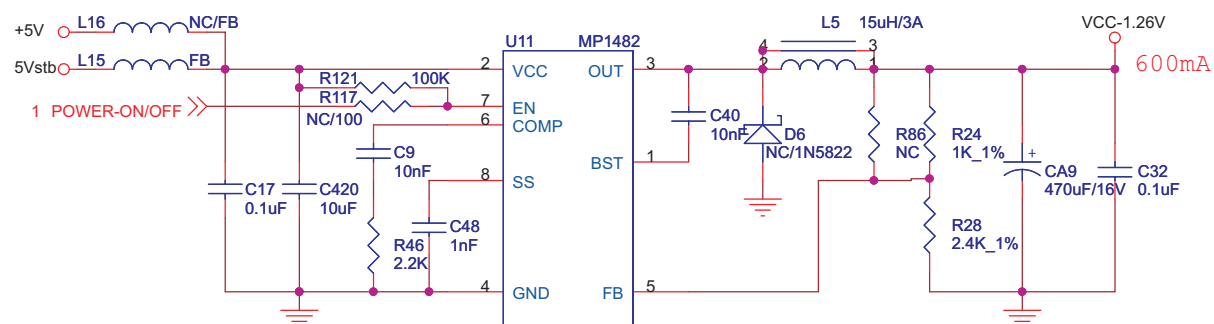
+2.6V for MST6M16



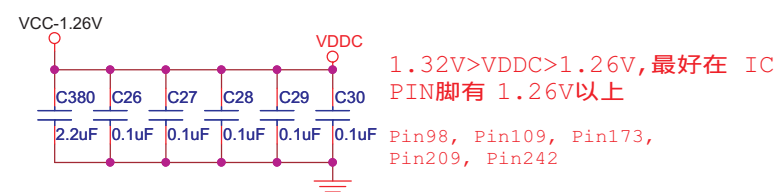
2.6V for DDRAM



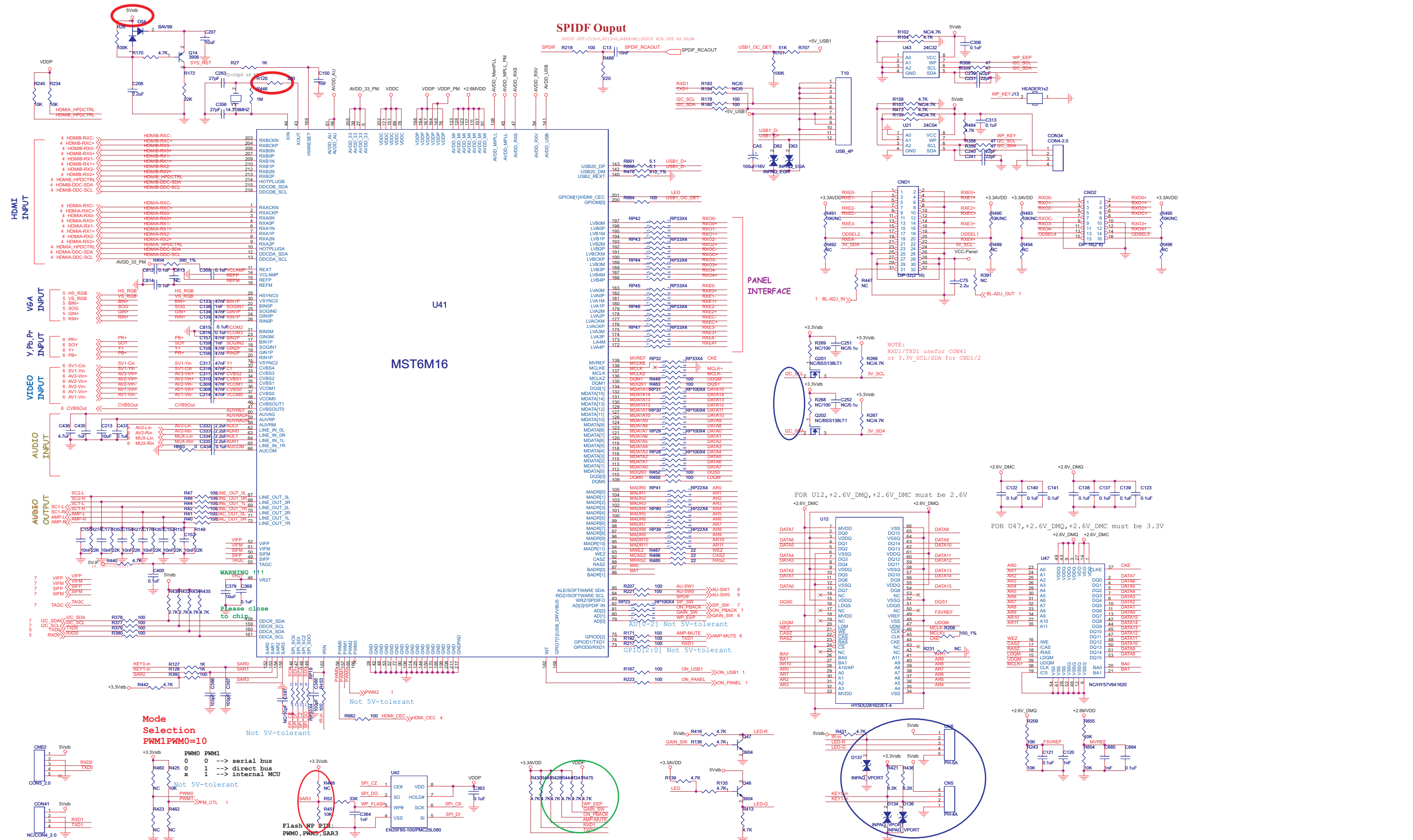
Vcc 1.26V for MST6M16 Core power



$$0.923 \cdot (1 + R_{up}/R_{down}) = 1.26V \text{---} R_{up} = 1.0K, R_{down} = 2.7K$$

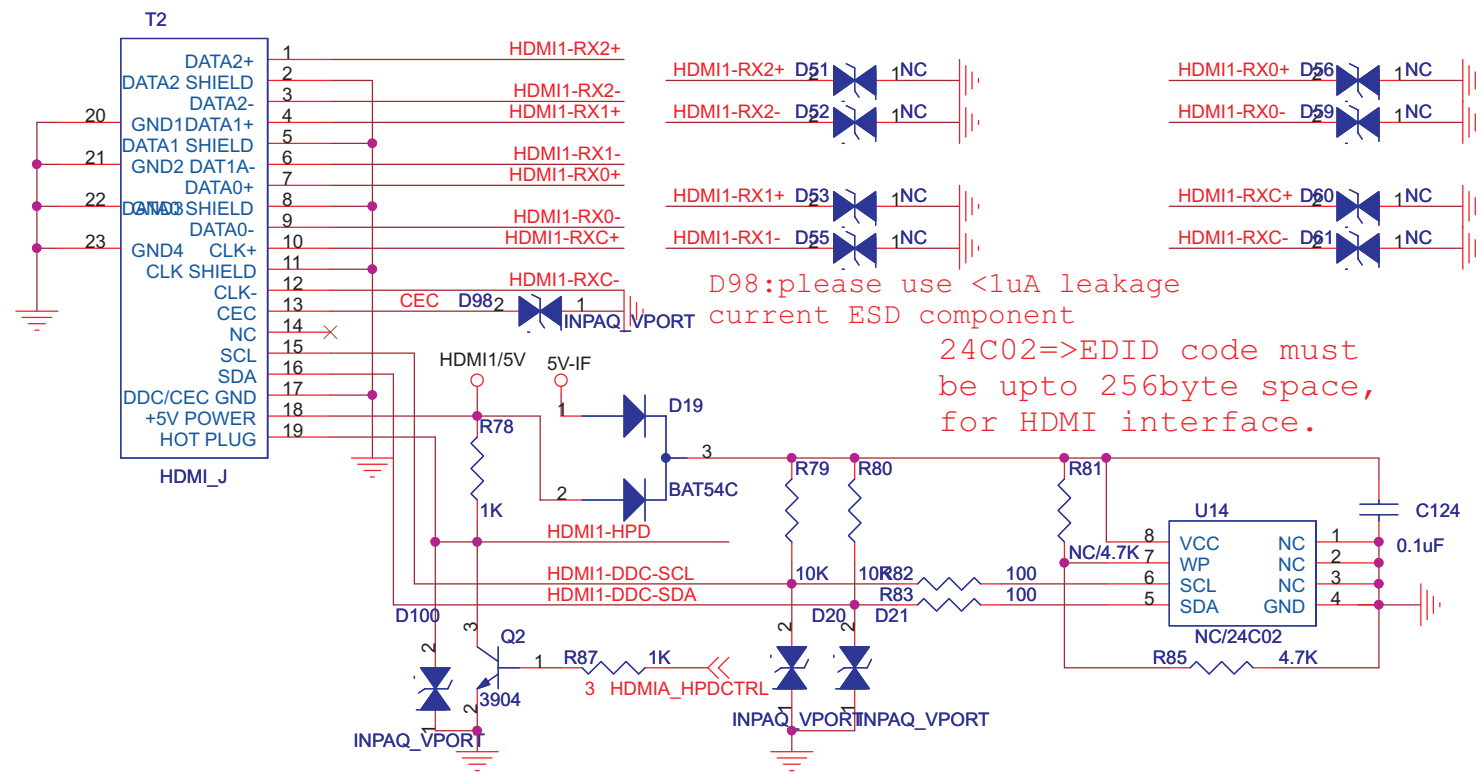


01	01/09/09		Liberação inicial	
Revisão	Data	Documento	Descrição	
Formato A3		Elaborado por: Éber Silva	Revisado por: Eduardo Macedo	Aprovado por: Thomas Wang
Escala S/E	Dimensão mm	Tol.não especificada	Página 2/9	Código matriz
PCI Principal HBTv-3203HD, HBTv-4203FD				Código sistema



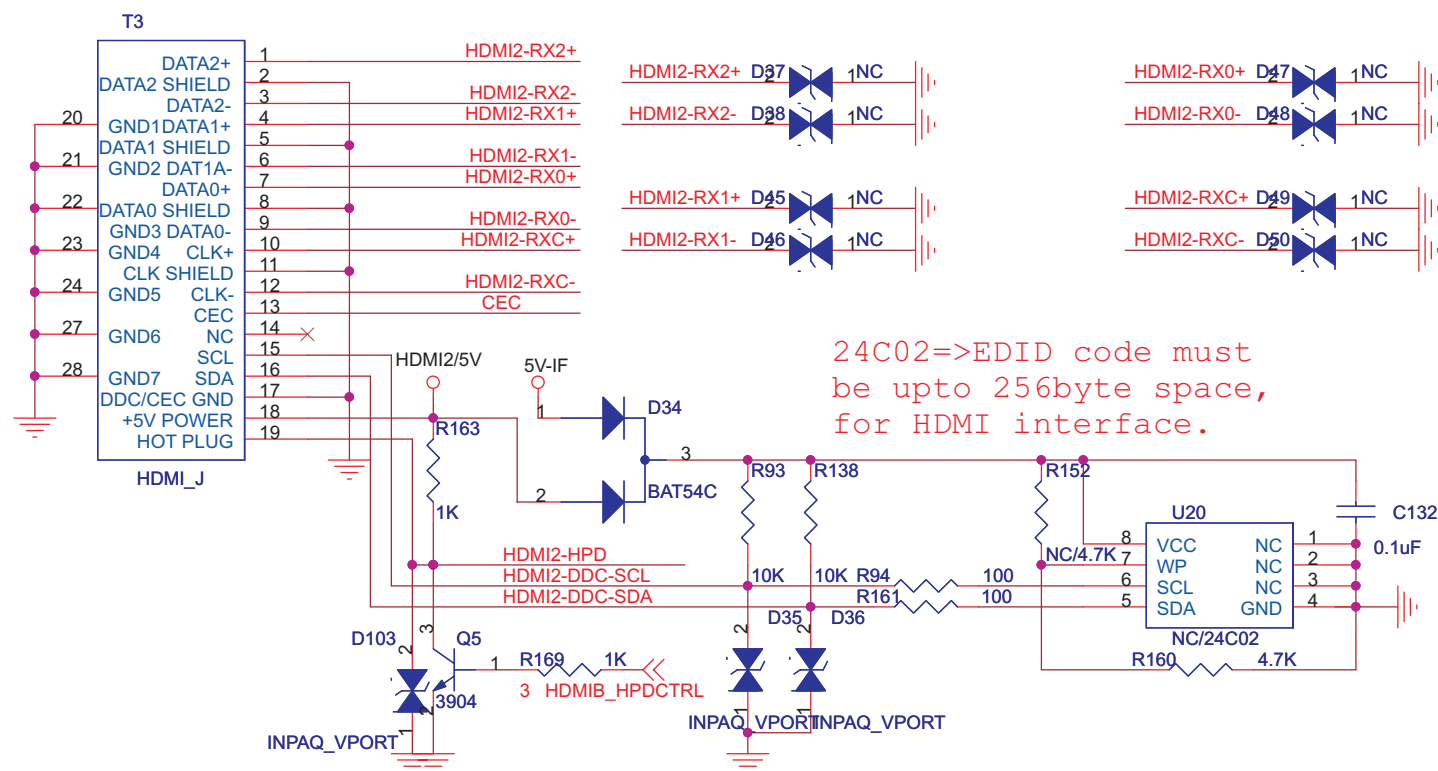
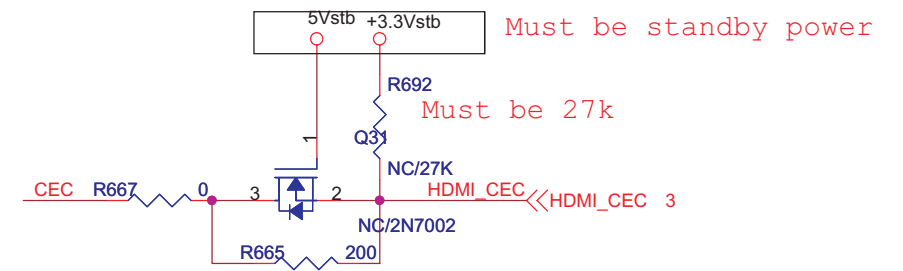
01	01/09/09		Liberação inicial		
Revisão	Data	Documento	Descrição		
Formato A3		Elaborado por: Éber Silva	Revisado por: Eduardo Macedo	Aprovado por: Thomas Wang	Modificado por:
Escala S/E	Dimensão mm	Tol.não especificada	Página 3/9	Código matriz	
PCI Principal HBTv-3203HD, HBTv-4203FD				Código sistema	





HDMI1-RX2+	R88	10	HDMIA-RX2+	HDMIA-RX2+	3
HDMI1-RX2-	R89	10	HDMIA-RX2-	HDMIA-RX2-	3
HDMI1-RX1+	R90	10	HDMIA-RX1+	HDMIA-RX1+	3
HDMI1-RX1-	R91	10	HDMIA-RX1-	HDMIA-RX1-	3
HDMI1-RX0+	R92	10	HDMIA-RX0+	HDMIA-RX0+	3
HDMI1-RX0-	R137	10	HDMIA-RX0-	HDMIA-RX0-	3
HDMI1-RXC+	R155	10	HDMIA-RXC+	HDMIA-RXC+	3
HDMI1-RXC-	R156	10	HDMIA-RXC-	HDMIA-RXC-	3
HDMI1-DDC-SCL	R84	100	HDMIA-DDC-SCL	HDMIA-DDC-SCL	3
HDMI1-DDC-SDA	R157	100	HDMIA-DDC-SDA	HDMIA-DDC-SDA	3

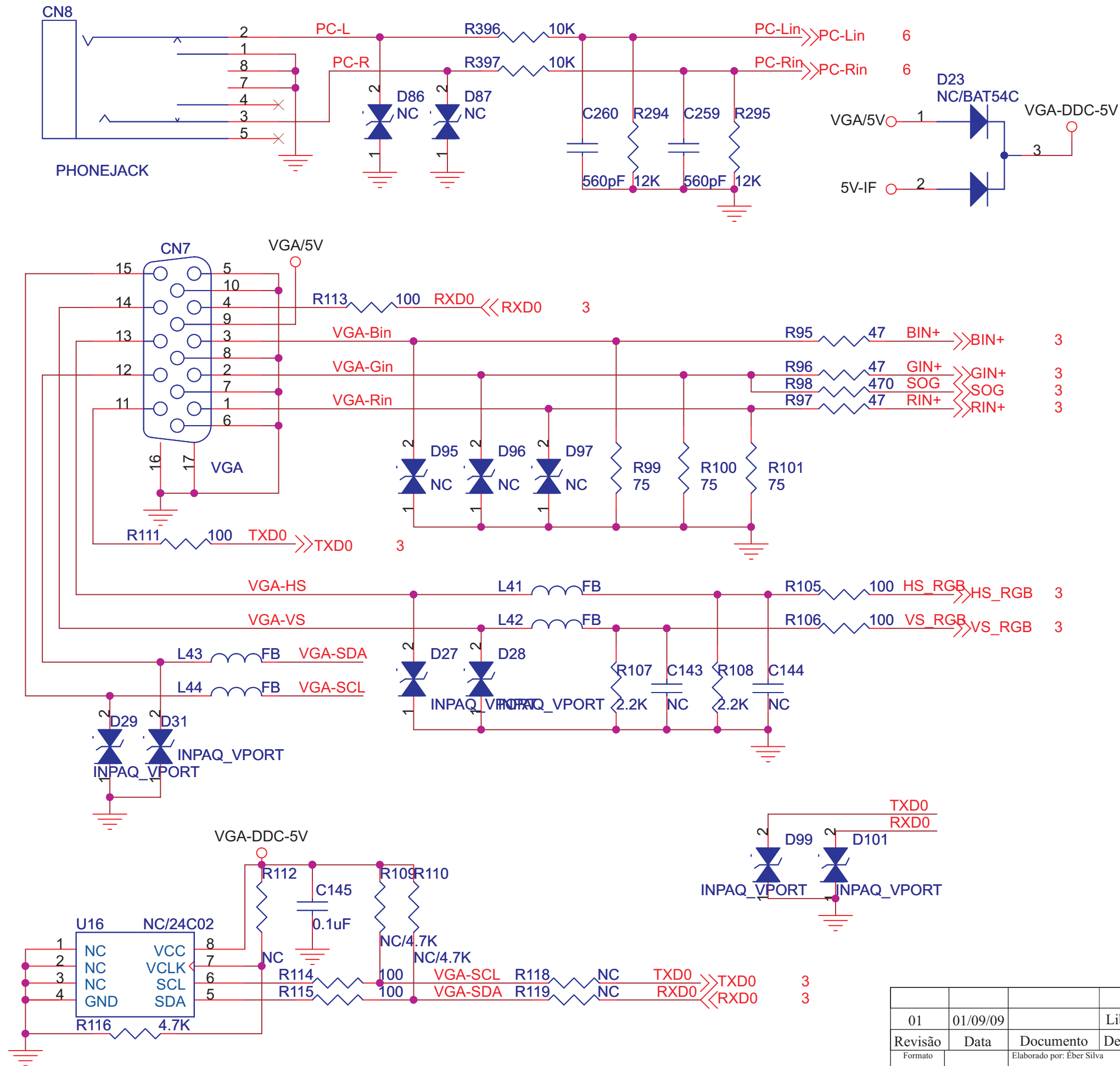
For CEC Leakage Protect



HDMI2-RX2+	R182	10	HDMIB-RX2+	HDMIB-RX2+	3
HDMI2-RX2-	R184	10	HDMIB-RX2-	HDMIB-RX2-	3
HDMI2-RX1+	R185	10	HDMIB-RX1+	HDMIB-RX1+	3
HDMI2-RX1-	R190	10	HDMIB-RX1-	HDMIB-RX1-	3
HDMI2-RX0+	R206	10	HDMIB-RX0+	HDMIB-RX0+	3
HDMI2-RX0-	R173	10	HDMIB-RX0-	HDMIB-RX0-	3
HDMI2-RXC+	R175	10	HDMIB-RXC+	HDMIB-RXC+	3
HDMI2-RXC-	R176	10	HDMIB-RXC-	HDMIB-RXC-	3
HDMI2-DDC-SCL	R177	100	HDMIB-DDC-SCL	HDMIB-DDC-SCL	3
HDMI2-DDC-SDA	R181	100	HDMIB-DDC-SDA	HDMIB-DDC-SDA	3

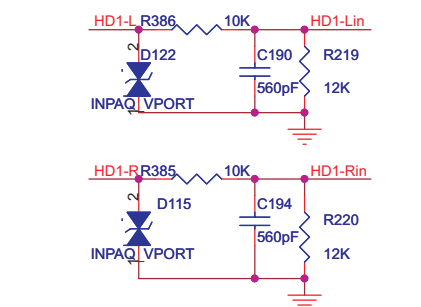
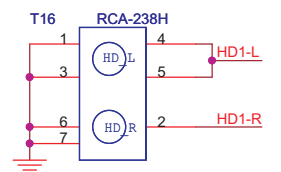
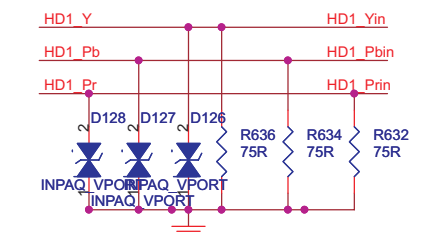
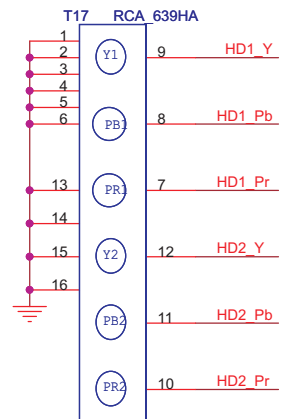
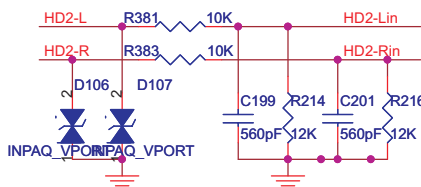
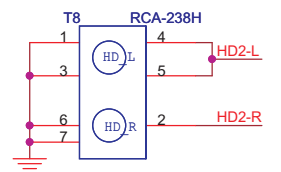
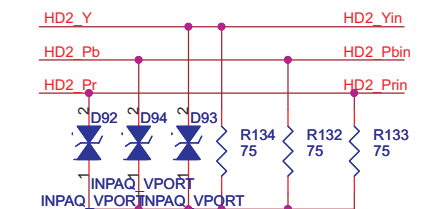
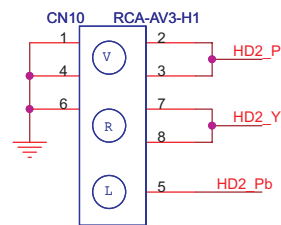
01	01/09/09		Liberação inicial	
Revisão	Data	Documento	Descrição	
Formato A3		Elaborado por: Éber Silva	Revisado por: Eduardo Macedo	Aprovado por: Thomas Wang
Escala S/E	Dimensão mm	Tol.não especificada	Página 4/9	Código matriz
PCI Principal HBTV-3203HD, HBTV-4203FD				Código sistema



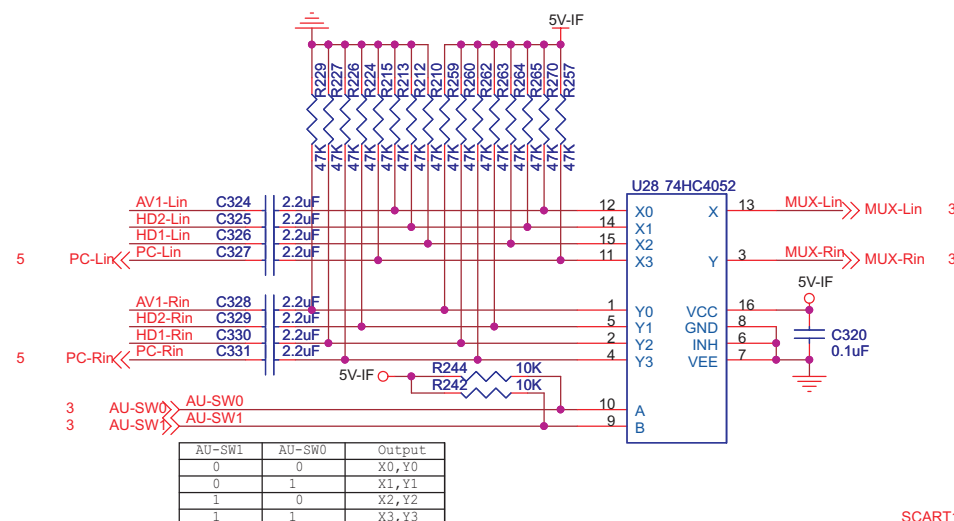
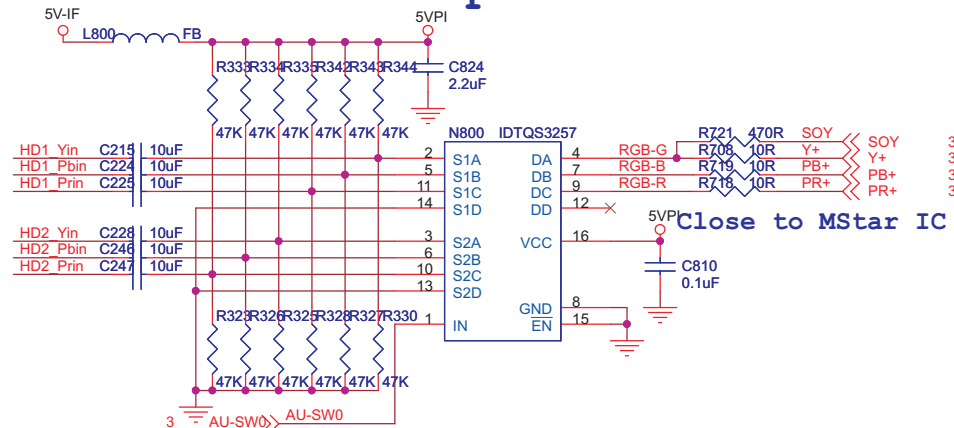


01	01/09/09		Liberação inicial	
Revisão	Data	Documento	Descrição	
Formato A3		Elaborado por: Éber Silva	Revisado por: Eduardo Macedo	Aprovado por: Thomas Wang
Escala S/E	Dimensão mm	Tol.não especificada	Página 5/9	Código matriz
PCI Principal HBTV-3203HD, HBTV-4203FD				Código sistema



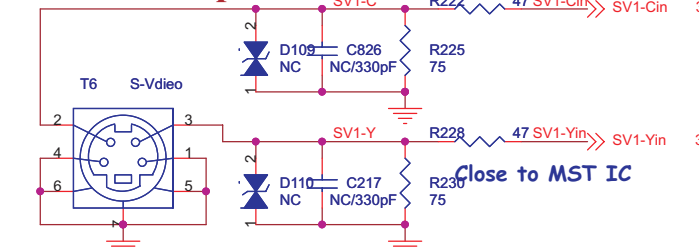


YPbPr Video Input Switch

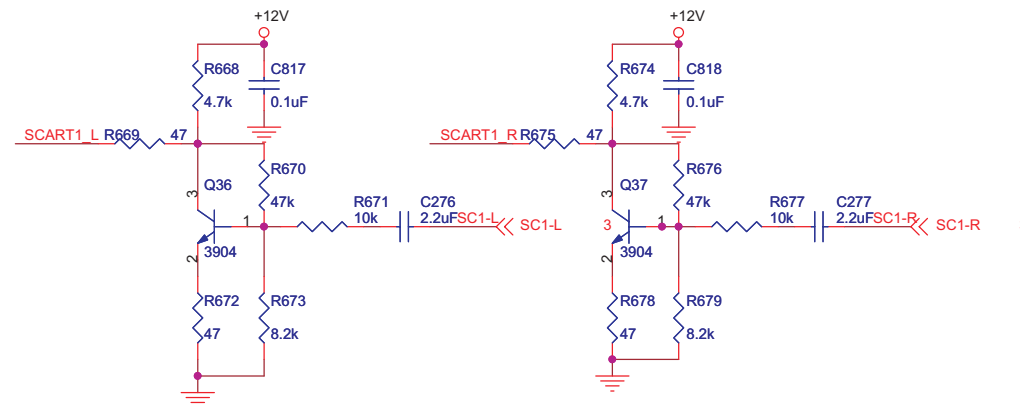
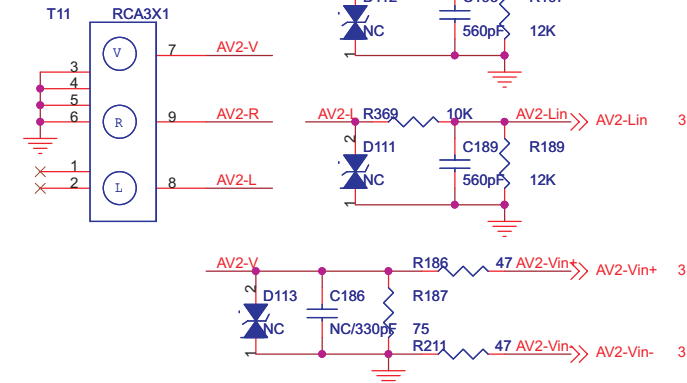


AU-SW1	AU-SW0	Output
0	0	X0, Y0
0	1	X1, Y1
1	0	X2, Y2
1	1	X3, Y3

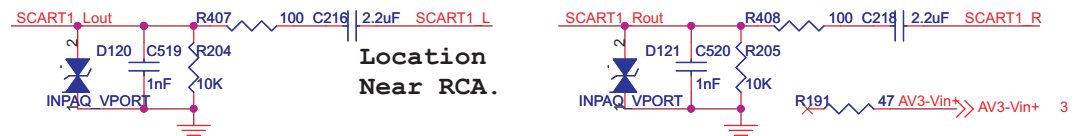
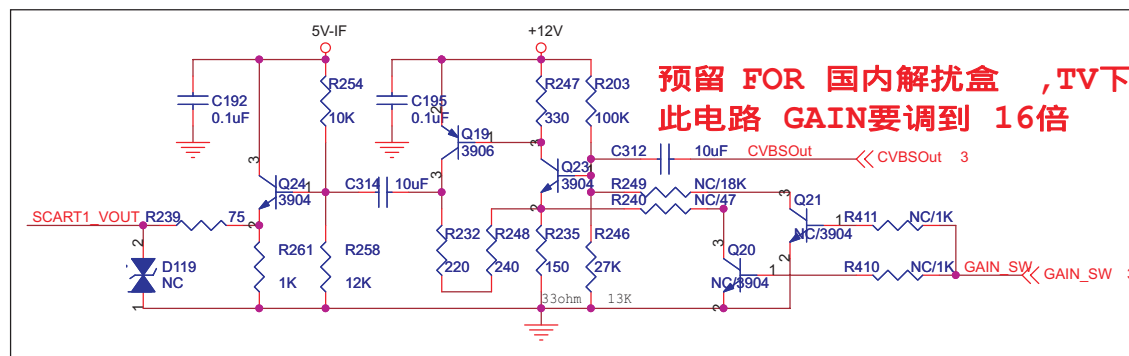
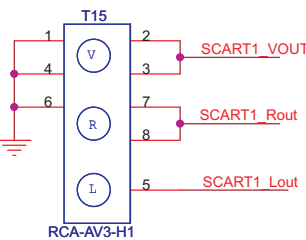
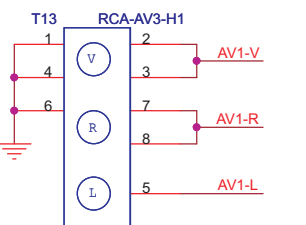
S-Video1 Input



SUB AV Input SIDE VIDEO

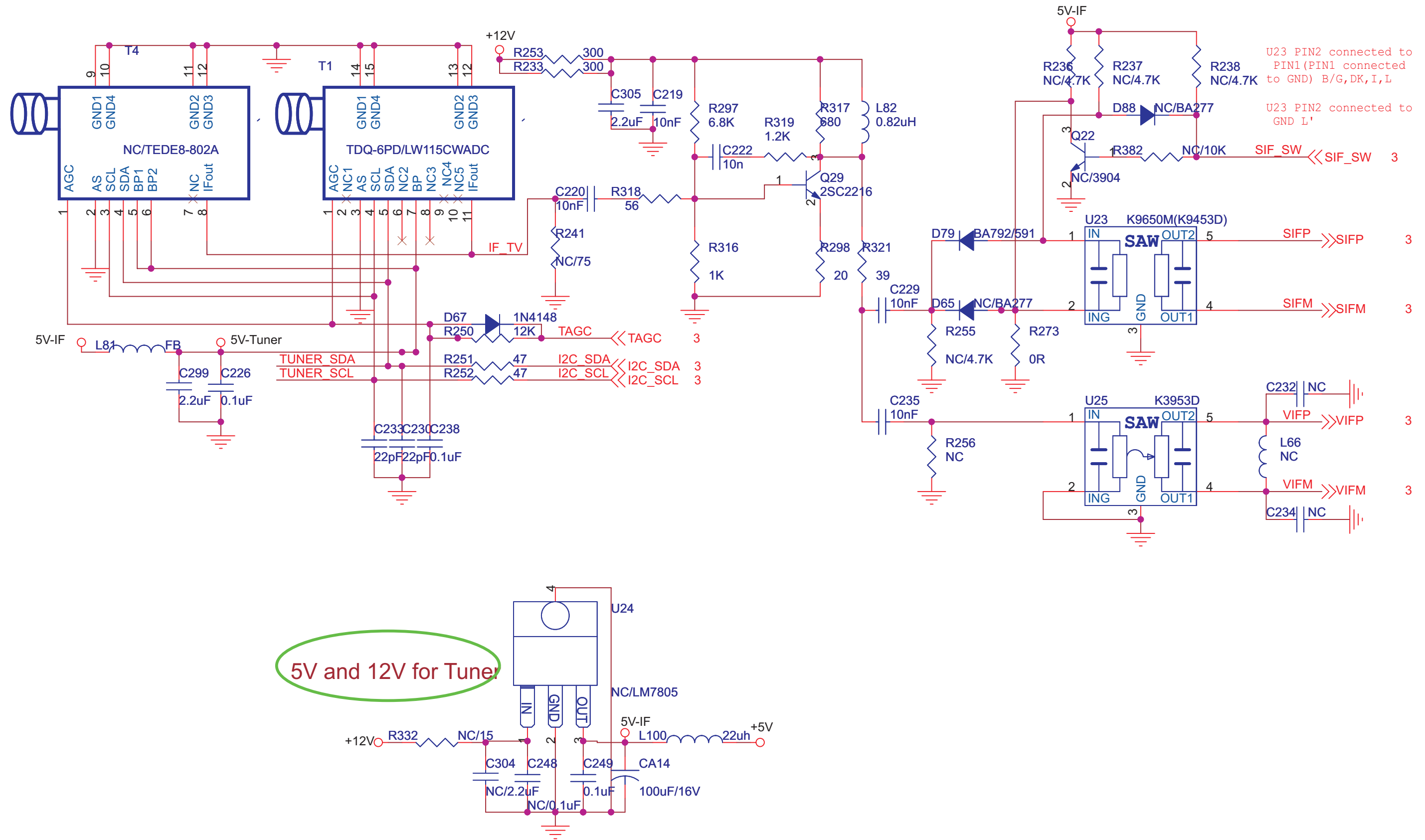


AV1 Input



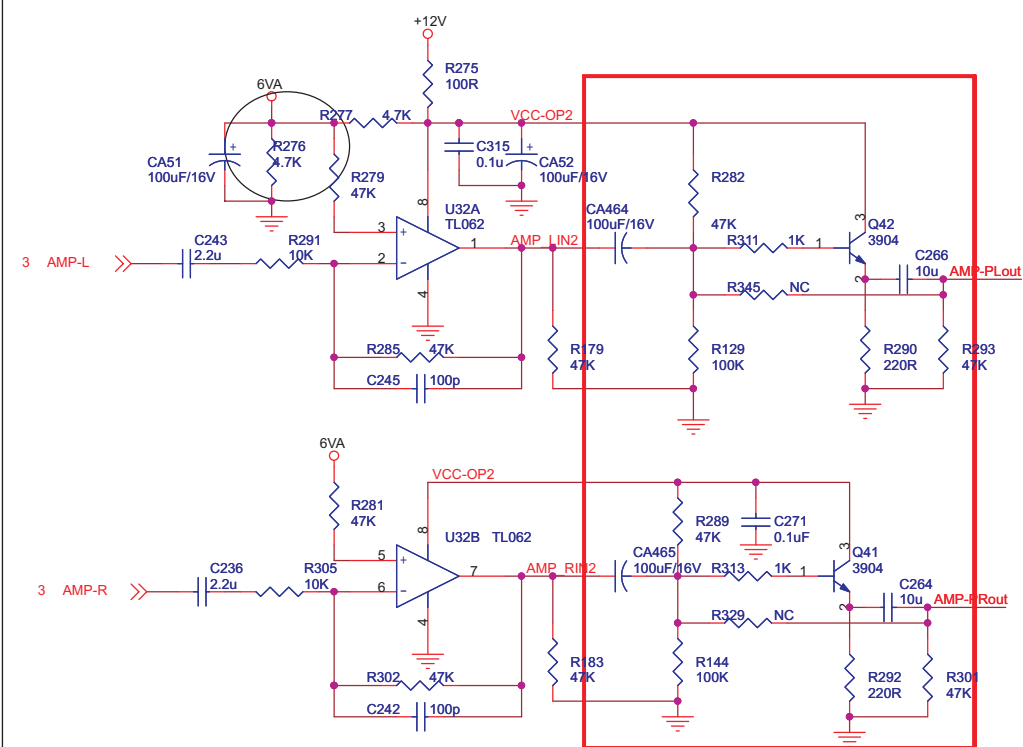
01	01/09/09	Liberação inicial	
Revisão	Data	Documento	Descrição
Formato A3		Elaborado por: Éber Silva	Revisado por: Eduardo Macedo
Escala S/E	Dimensão mm	Tol.não especificada	Página 6/9
PCI Principal HDTV-3203HD, HDTV-4203FD			



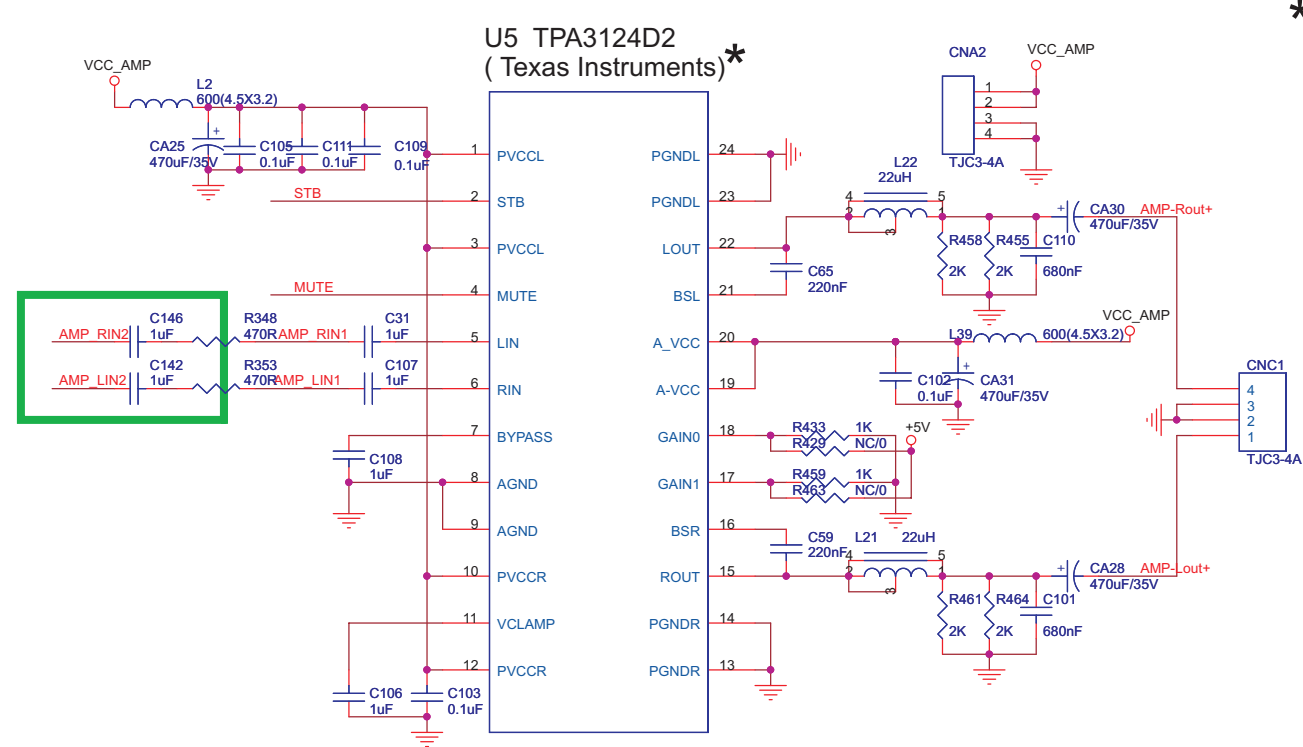


01	01/09/09		Liberação inicial	
Revisão	Data	Documento	Descrição	
Formato A3		Elaborado por: Éber Silva	Revisado por: Eduardo Macedo	Aprovado por: Thomas Wang
Escala S/E	Dimensão mm	Tol.não especificada	Página 7/9	Código matriz
PCI Principal HBTV-3203HD, HBTV-4203FD				Código sistema

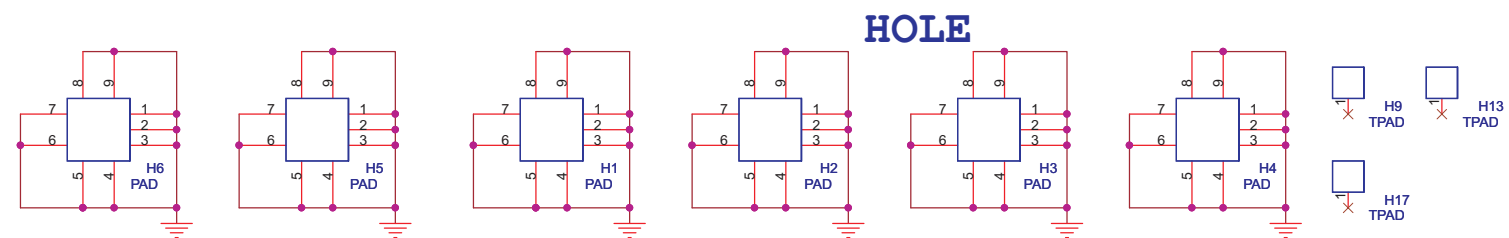
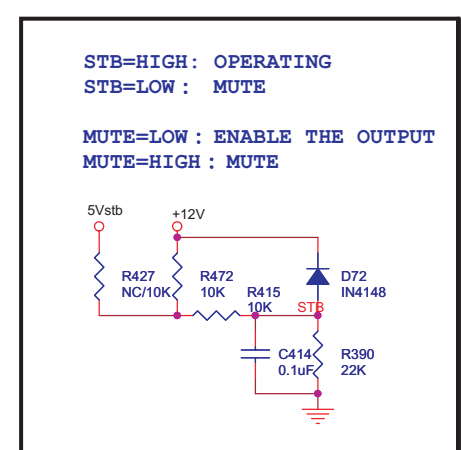
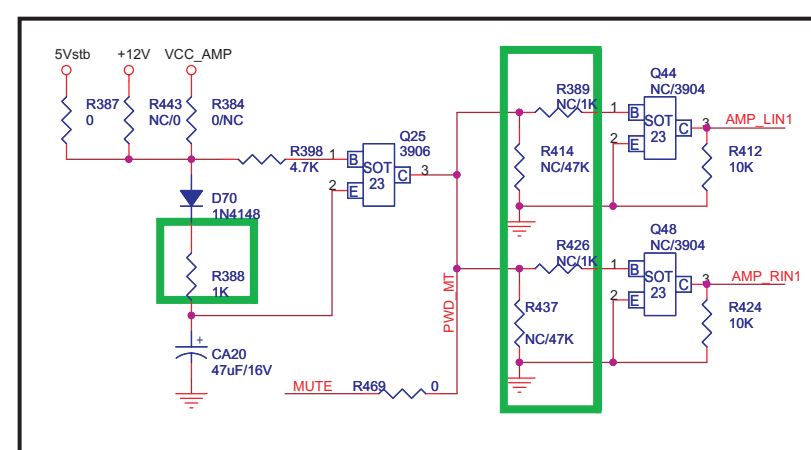
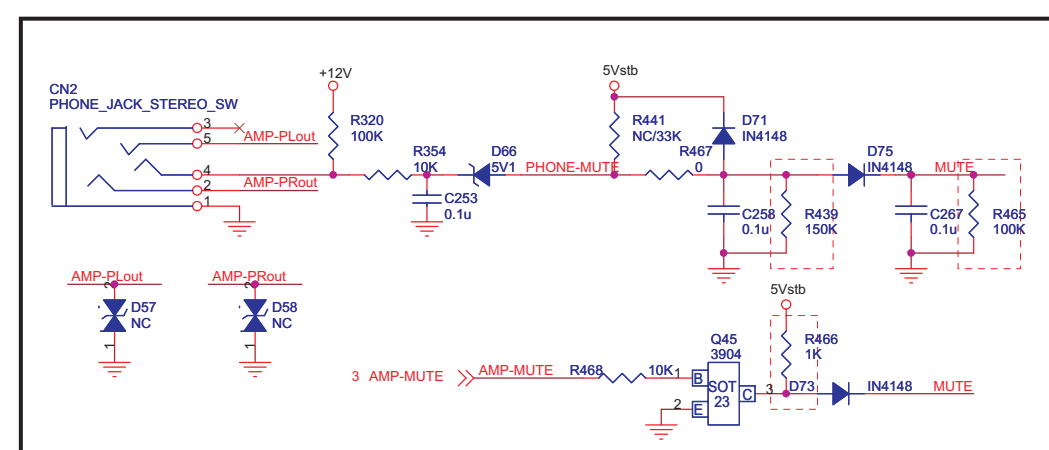




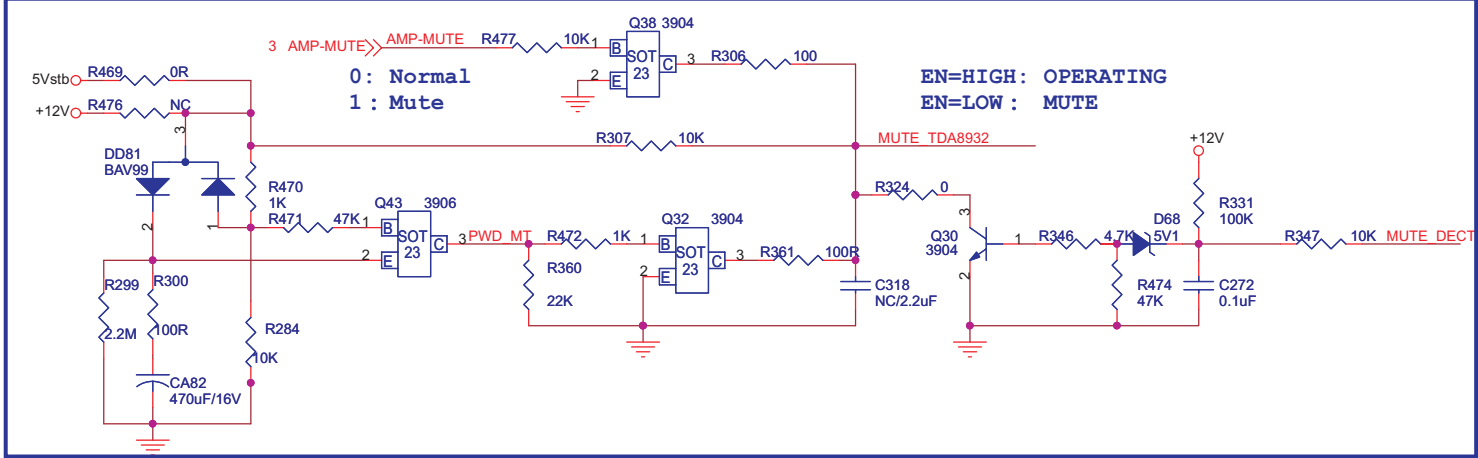
Near MST.IC
Ground in the middle of the L/R



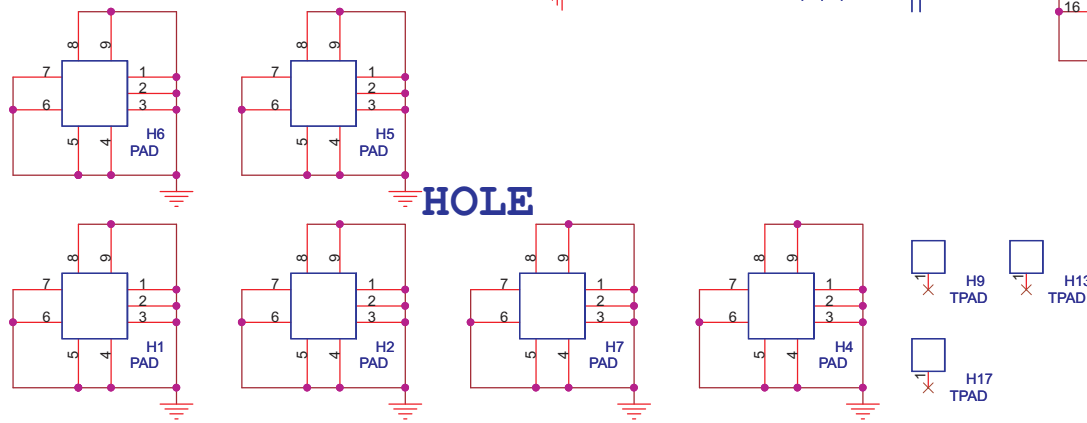
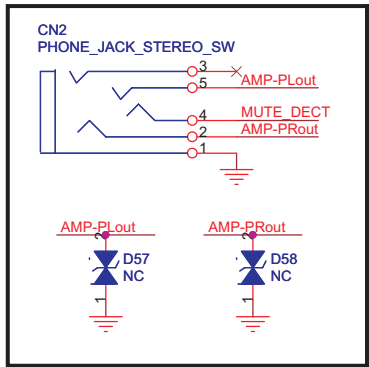
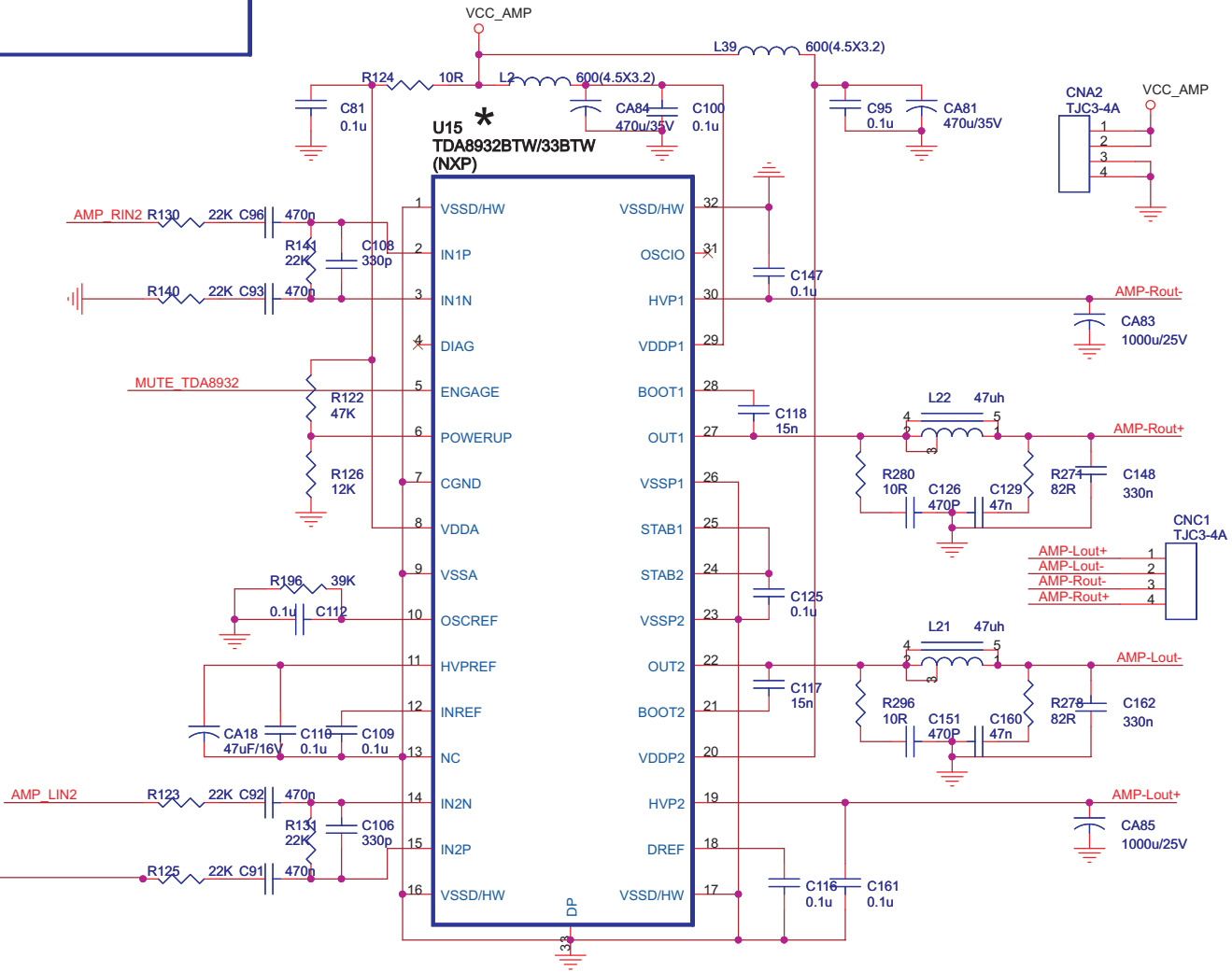
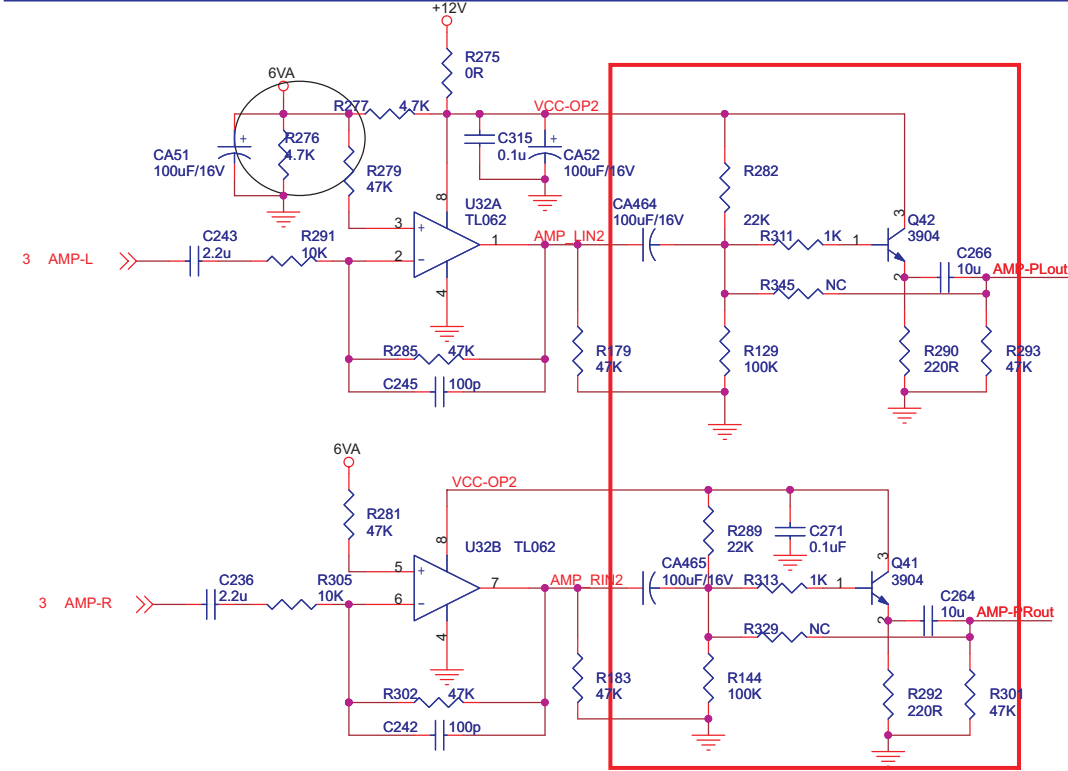
* Para placas utilizando amplificador da Texas Instruments, acessar o menu de serviço (modo fábrica), selecionar a opção: OTHER OPTION -->AMP --> OLD



MUTE Circuit



* Para placas utilizando amplificador NXP, acessar o menu de serviço (modo fábrica),selecionar a opção: OTHER OPTION -->AMP --> NEW



Amplificador TDA8932BTW (NXP)

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Escala S/E	Dimensão mm	Tol.não especificada	Página 9/9	Código matriz
PCI Principal HBTV-3203HD, HBTV-4203FD				Código sistema

