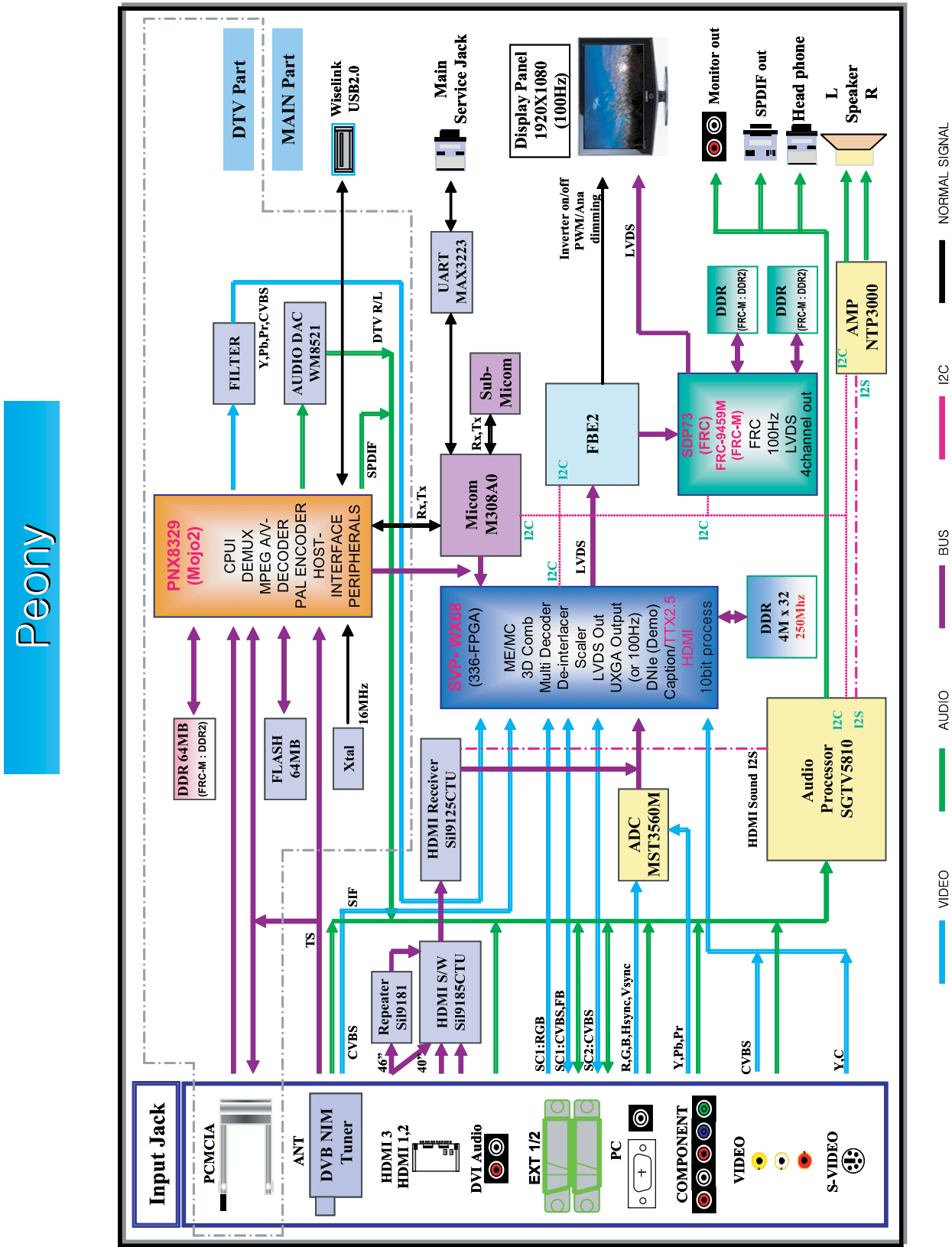
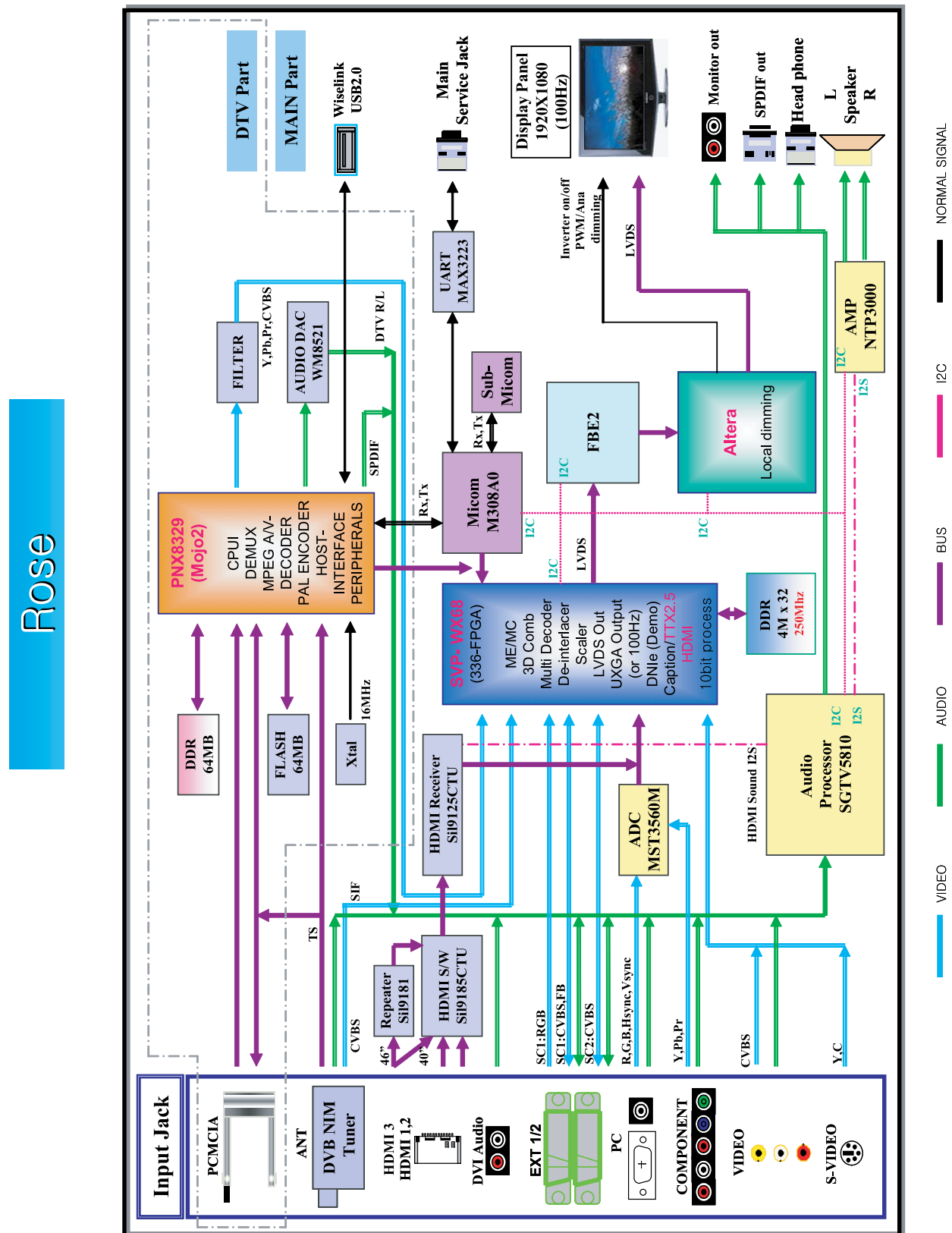


7. Schematic Diagram

7-1. Peony Block Diagram



7-2. Rose Block Diagram



The Block processing Video Signal

1. CVBS signal like RF and AV is inputted to Scaler-IC(SVP-WX68) directly.
2. Component signal and PC signal are inputted to ADC-IC(MST3560) first, and then ADC-IC convert them to 30bit signal of YPbPr.
Scaler-IC receives this signal. The reason why ADC-IC is used is the bandwidth limitation when SVP-WX68 processes 1080P_signal .
3. HDMI signal is inputted to switch-IC, SIL9185 to select just 1-HDMI input in 3-HDMI input.
And then Selected HDMI signal is inputted to HDMI Receiver-IC(SIL9125) due to the same reason to ADC-IC.
After SIL9125 IC, 30bit signal of YPbPr is inputted to Scaler-IC.
4. Scaler-IC converts video signal to 10bit of LVDS signal, standard format to run a LCD panel.
5. FBE2-IC receives this LVDS signals and processing it to enhance picture quality.
And then final 10bit LVDS signal is outputted.

The Block processing Sound Signal

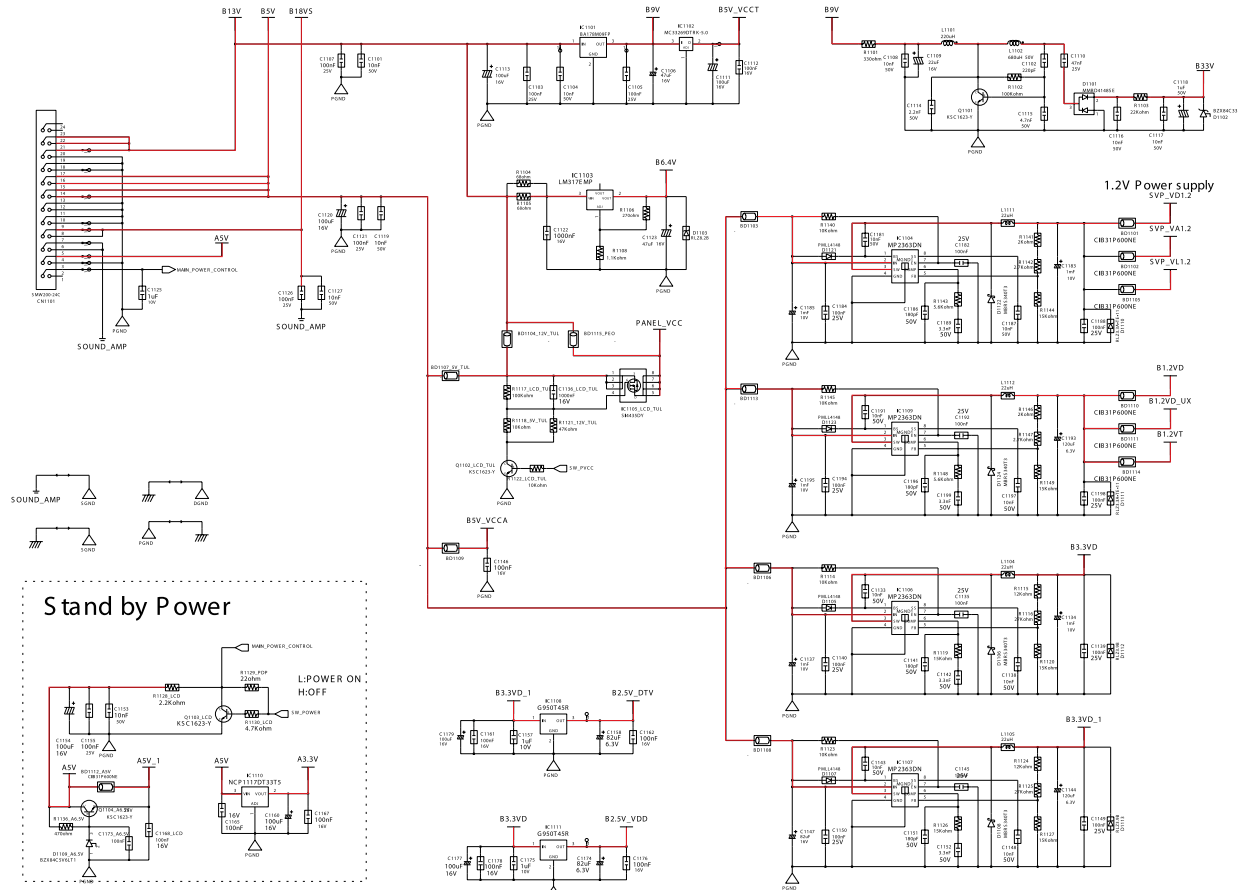
1. Sound signals of RF(SIF), HDMI, Component, DVI, PC and AV input are inputted to Sound-IC(SGTV5810).
DTV_AUDIO signal is also inputted for MP3 connected to wiselink.
SGTV5810-IC processes these signal and outputs I2C signal to digital AMP-IC(NTP3000).
And signals for headphone and Monitor-out are ~~ouputted~~ outputted.
2. NTP-3000 IC outputs an analog sound signal to run Speakers.

WiseLink Block

1. Sound signals of RF(SIF), HDMI, Component, DVI, PC and AV input are inputted to Sound-IC(SGTV5810).
DTV_AUDIO signal is also inputted for MP3 connected to wiselink.
SGTV5810-IC processes these signal and outputs I2C signal to digital AMP-IC(NTP3000).
And signals for headphone and Monitor-out are outputted.
2. NTP-3000 IC outputs an analog sound signal to run Speakers.

FRC Block

- 1.FRC Board receives LVDS signals from FBE2-IC.
2. FRC Board processes a LVDS signal to 100MHz(or 120MHz) of LVDS signal with 10bit EVEN and 10bit ODD LVDS format.
3. The function of FRC IC is to process MJC and reduce MOTION BLUR with 100MHz(or 120MHz) format.

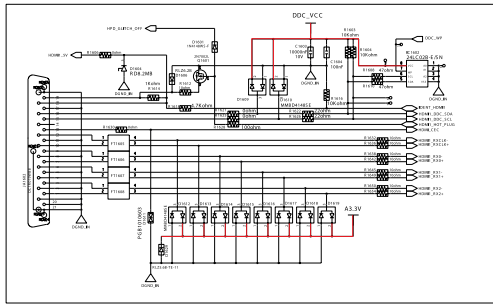




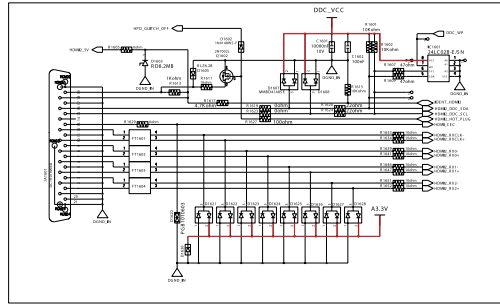
7-3-4. JACK INPUT

Reference : 1601~

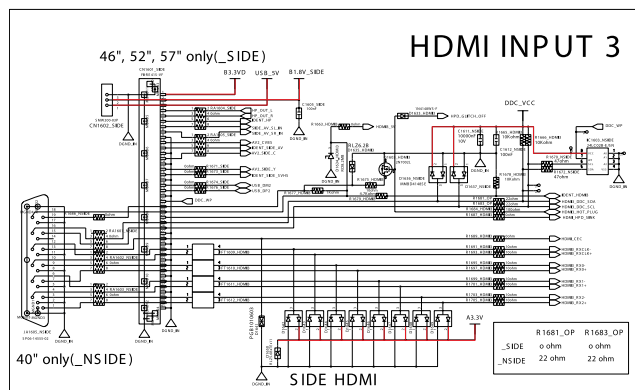
HDMI INPUT 1



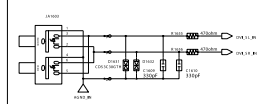
HDMI INPUT 2



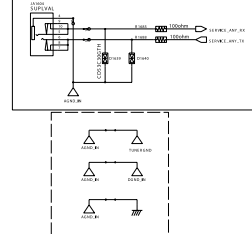
HDMI INPUT 3



DVI SOUND INPUT



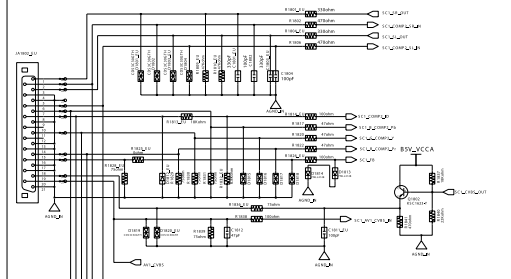
Service_ATV & DTV



7-3-5. JACK_IN/OUT 2

Reference : 1801~

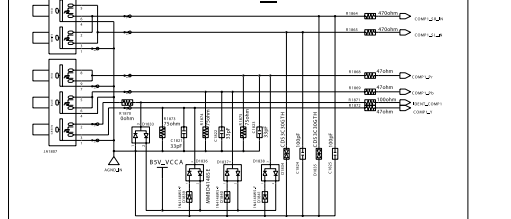
SCART1 Europe model only



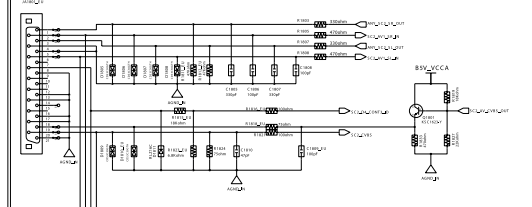
COMPONENT2 Asia model only



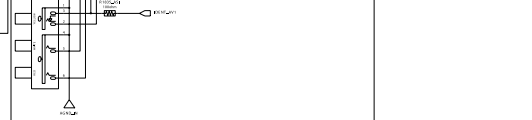
COMP1_INPUT



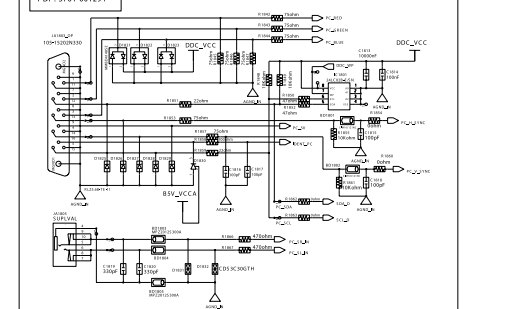
SCART2



AV1 Asia model only

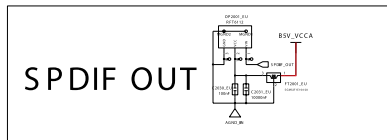
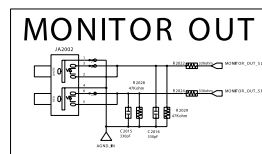
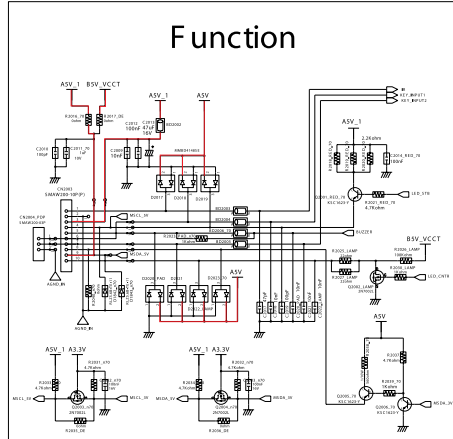
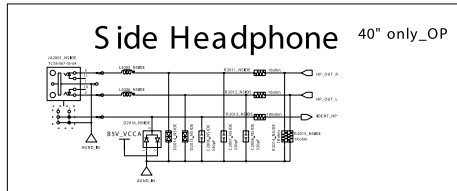
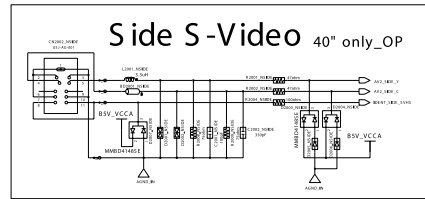
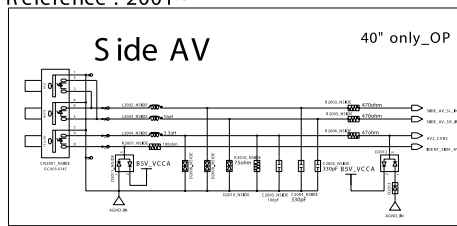


PC_INPUT



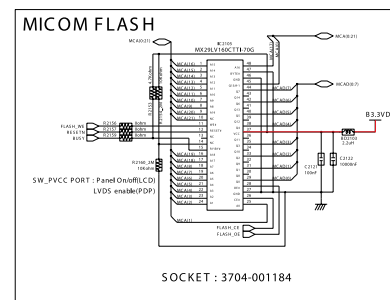
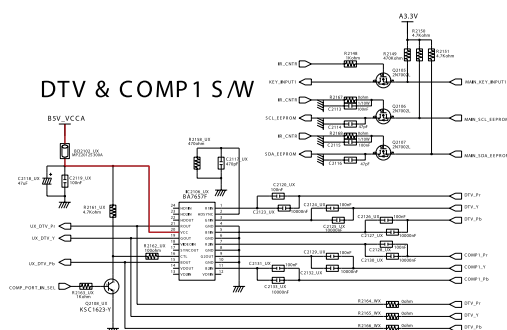
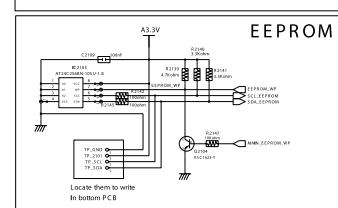
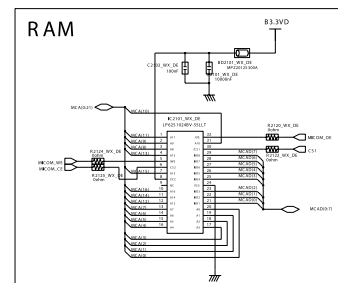
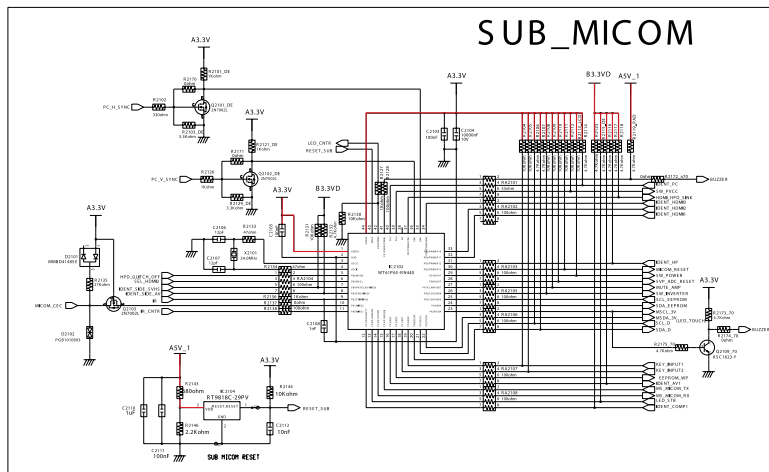
7-3-6. JACK IN & OUT 3

Reference : 2001~

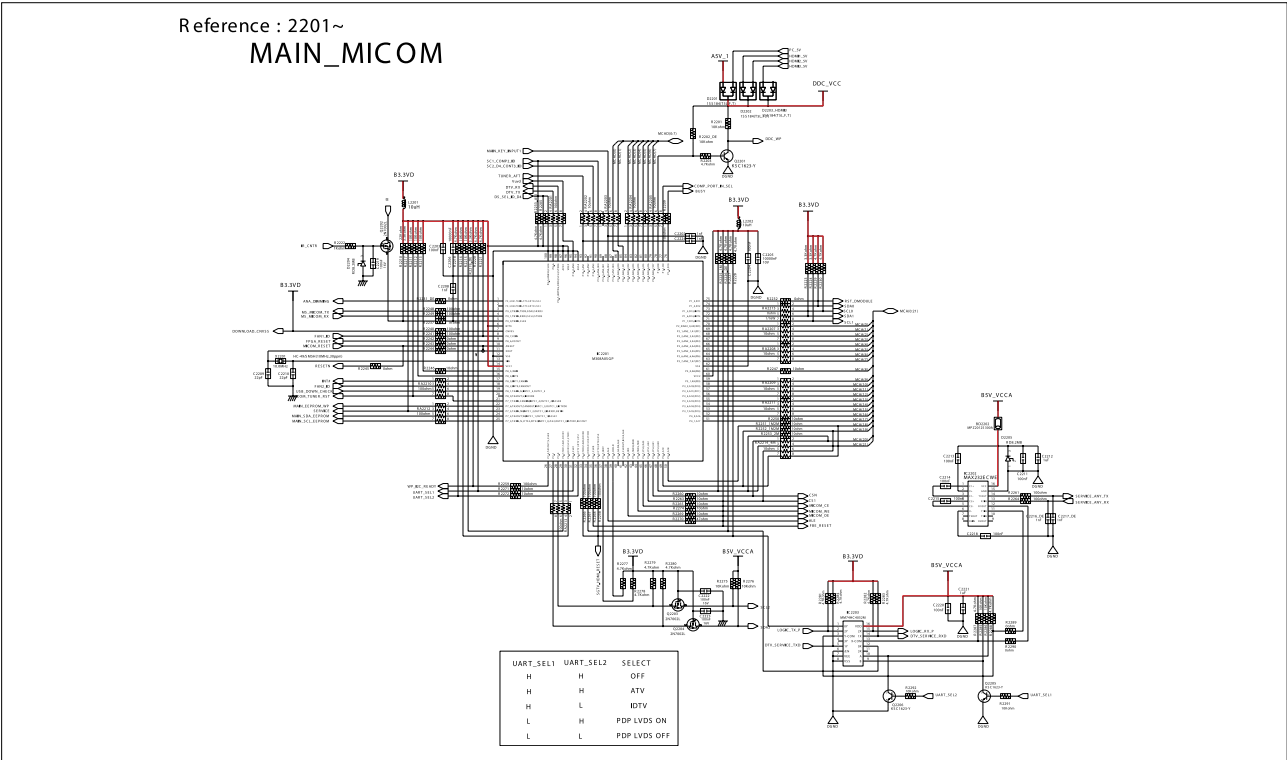


7-3-7. SUB-MICOM

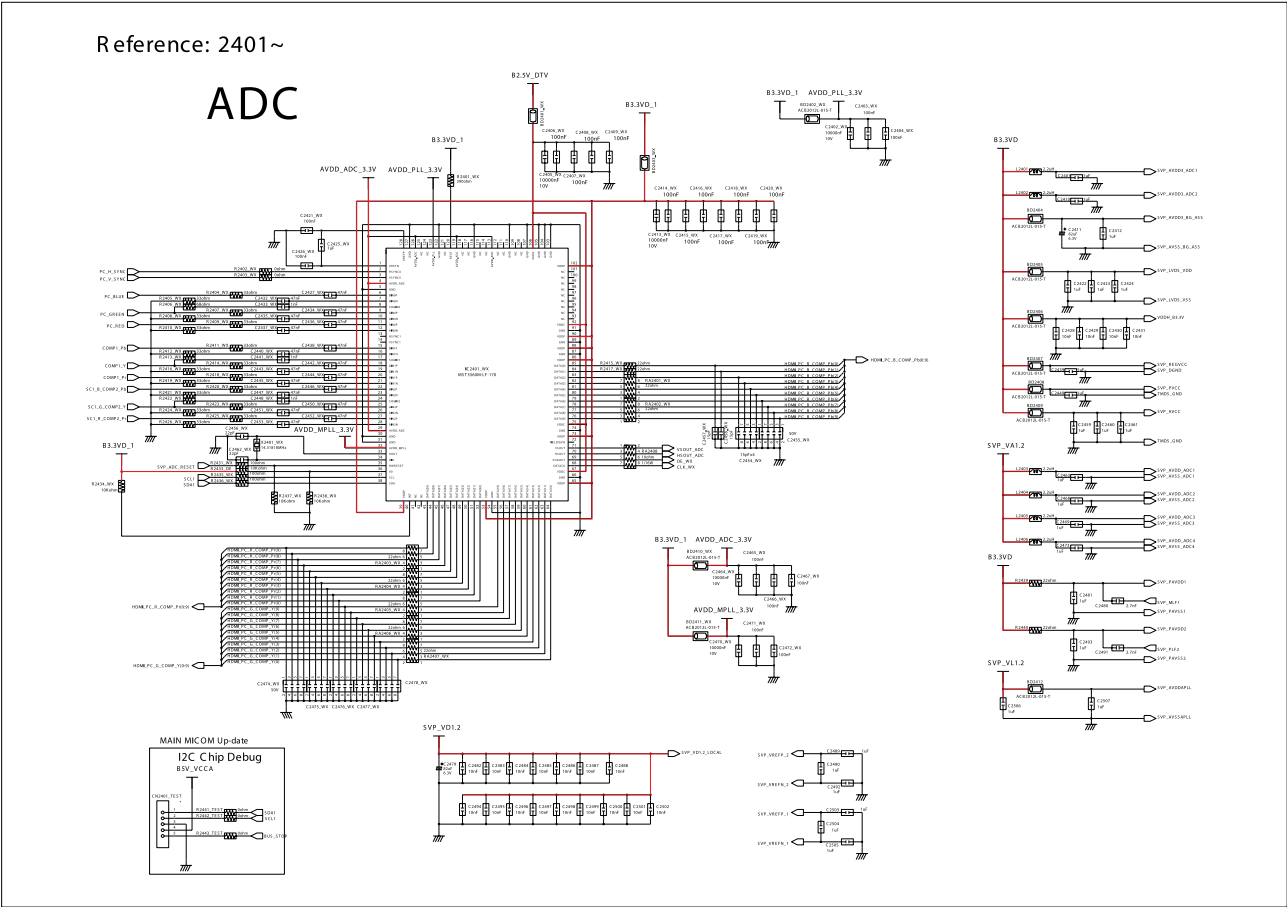
Reference : 2101~



7-3-8. MICOM

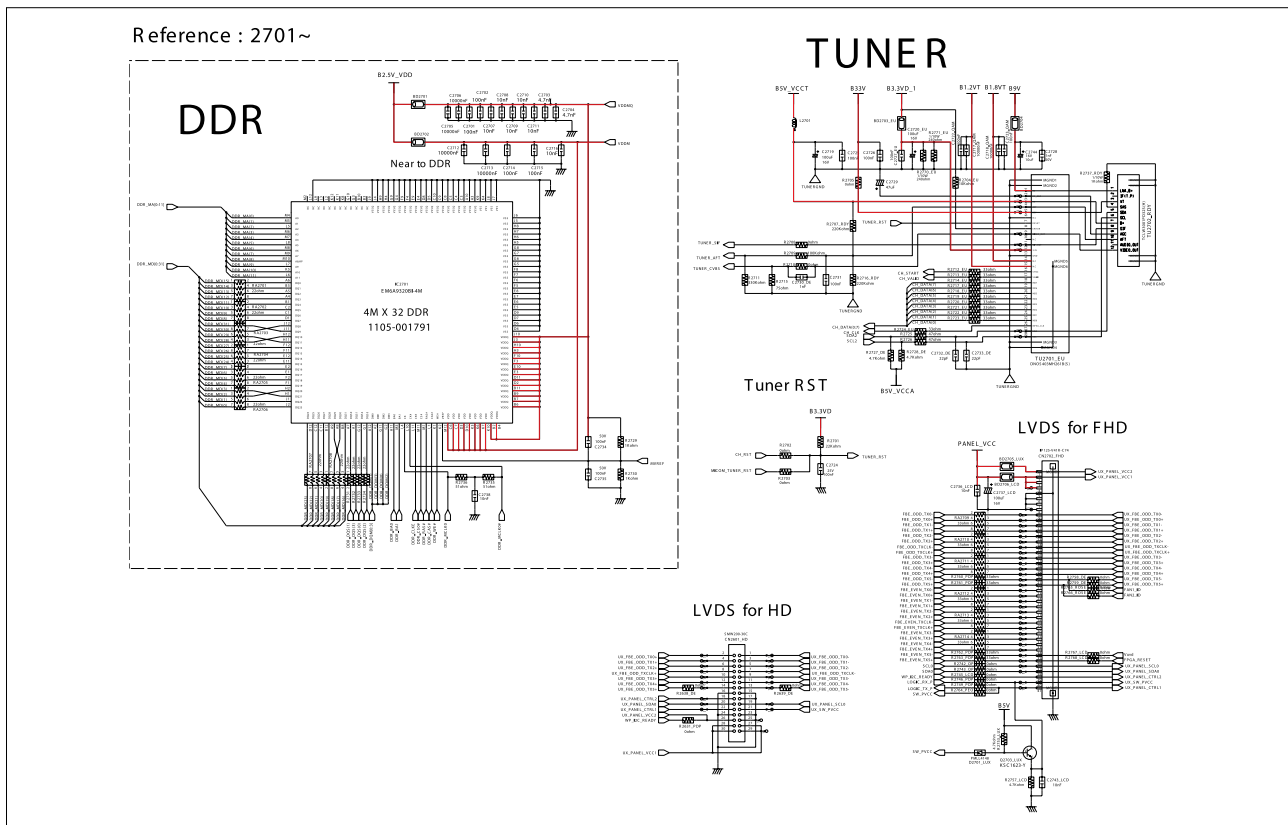


7-3-9. ADC & SVP-WX POWER

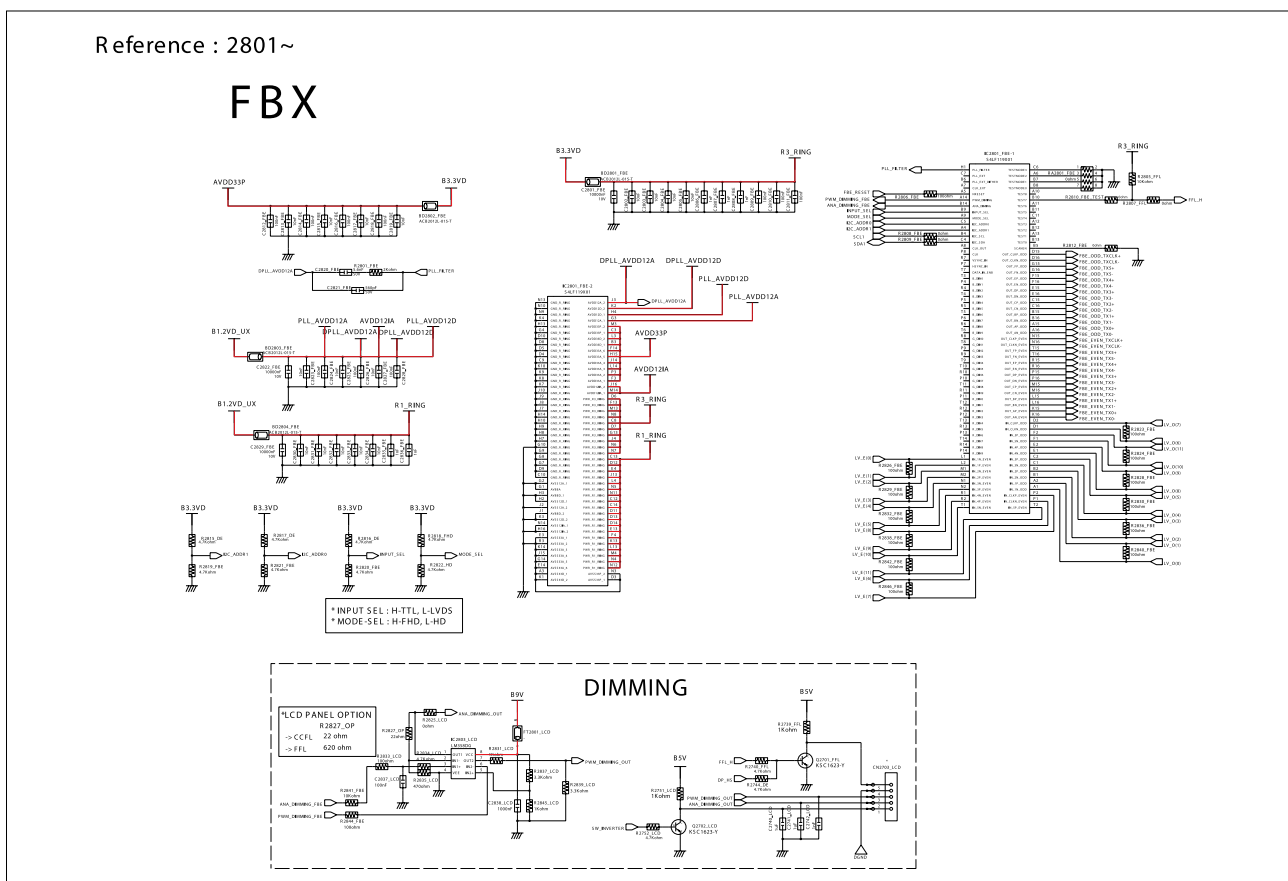




7-3-11. DDR, TUNER, LVDS OUT



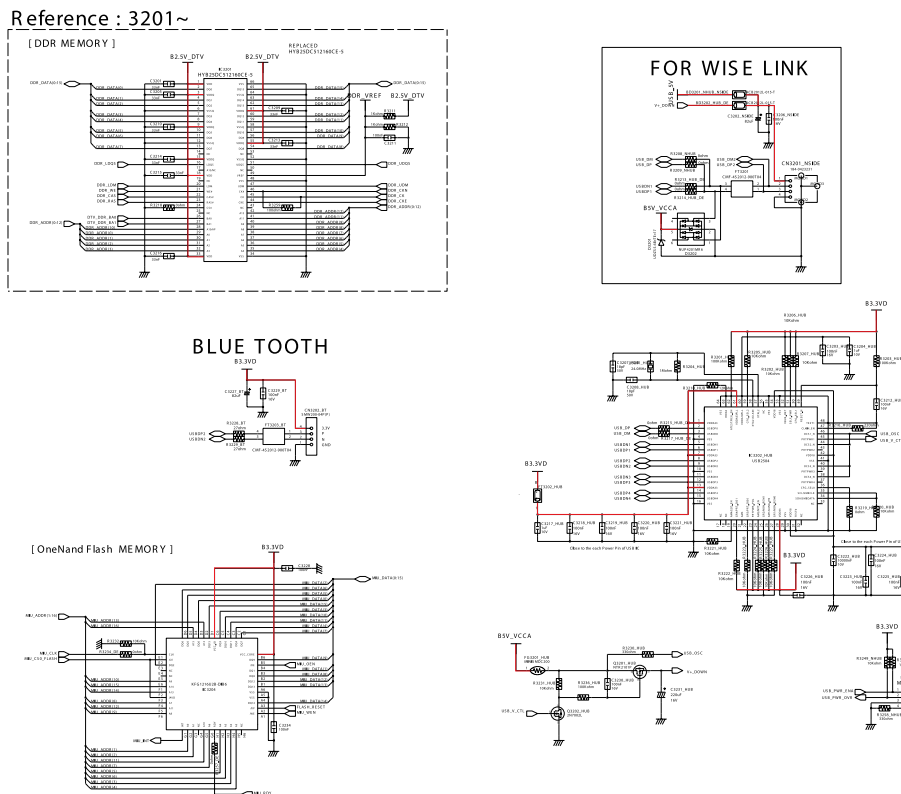
7-3-12. FBX



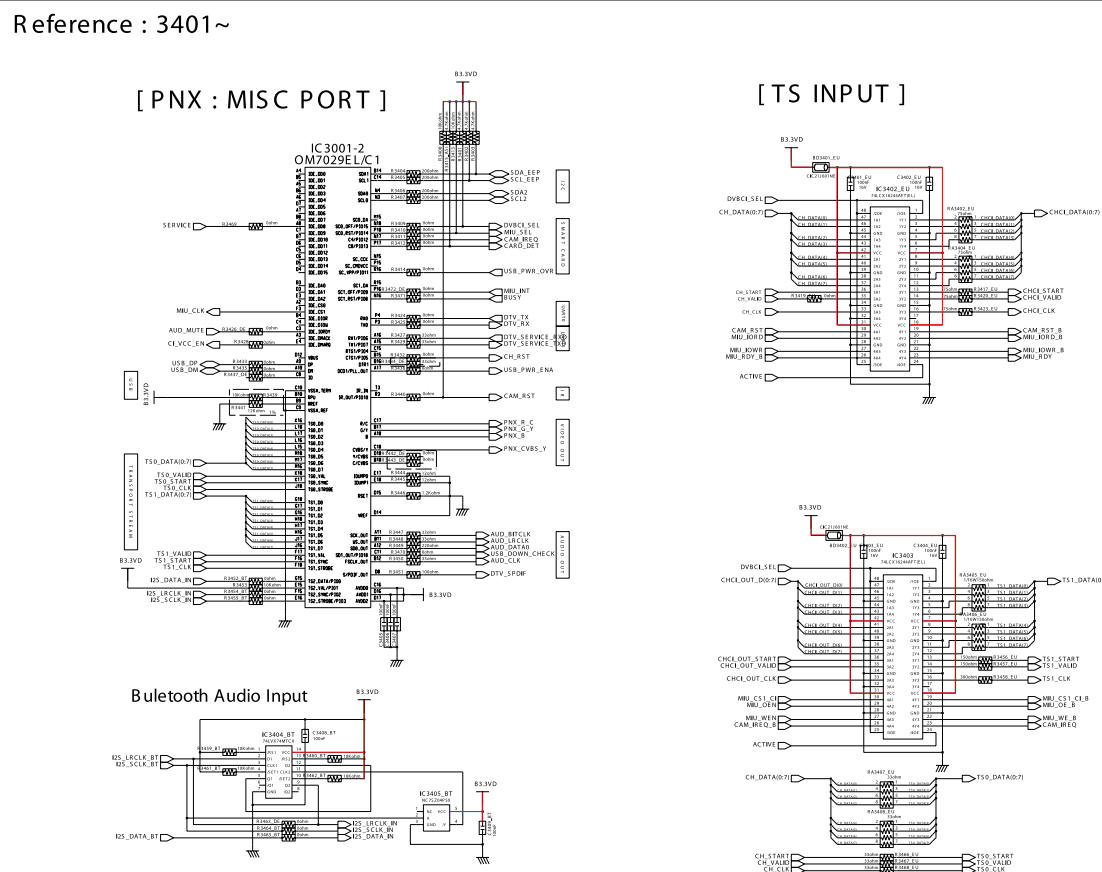
[PNX MEMORY]



7-3-14. DTV DDR, FLASH-MEMORY, USB



7-3-15. M0J02 TS/I0/SMC/PVR/ETC



[P C M C I A]

