

1. General description

Planar passivated four quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

2. Features and benefits

- High blocking voltage capability
- Least sensitive gate for highest noise immunity
- · Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants

3. Applications

- General purpose motor control
- General purpose switching

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DRM}	repetitive peak off- state voltage		-	-	800	V
I _{TSM}	non-repetitive peak on- state current	full sine wave; T _{j(init)} = 25 °C; t _p = 20 ms; <u>Fig. 4; Fig. 5</u>	-	-	95	A
Tj	junction temperature		-	-	125	°C
I _{T(RMS)}	RMS on-state current	full sine wave; T _{mb} ≤ 99 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	-	12	A
Static chara	acteristics	1	1			
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 7</u>	-	5	50	mA
		$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T2+ G-};$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	-	8	50	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 7</u>	-	10	50	mA





BT138-800G

4Q Triac

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	-	22	100	mA
Dynamic cha	racteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	200	250	-	V/µs

5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2-71
2	T2	main terminal 2	$2 \rightarrow 0 \leq 1$	sym051
3	G	gate		
mb	T2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
BT138-800G	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

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7. Limiting values

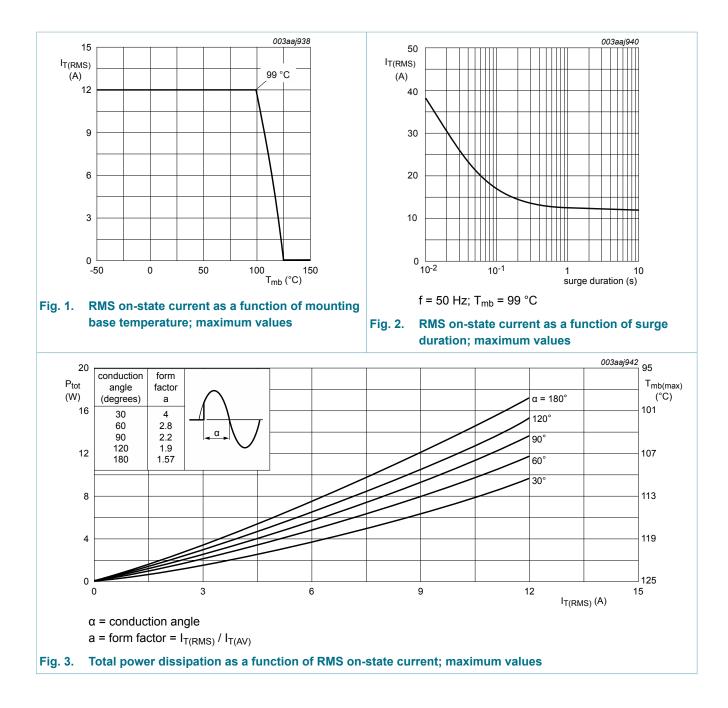
Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 99$ °C; Fig. 1; Fig. 2; Fig. 3	-	12	A
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; \frac{\text{Fig. 4}}{25}; \text{ Fig. 5}$	-	95	A
		full sine wave; $T_{j(init)}$ = 25 °C; t_p = 16.7 ms	-	105	A
l ² t	I ² t for fusing	t_p = 10 ms; sine-wave pulse	-	45	A ² s
dl _T /dt	rate of rise of on-state current	I_T = 20 A; I_G = 0.2 A; dI_G/dt = 0.2 A/µs; T2+ G+	-	50	A/µs
		I_T = 20 A; I_G = 0.2 A; dI_G/dt = 0.2 A/µs; T2+ G-	-	50	A/µs
		I_T = 20 A; I_G = 0.2 A; dI_G/dt = 0.2 A/µs; T2- G-	-	50	A/µs
		I_T = 20 A; I_G = 0.2 A; dI_G/dt = 0.2 A/µs; T2- G+	-	10	A/µs
I _{GM}	peak gate current		-	2	А
P _{GM}	peak gate power		-	5	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

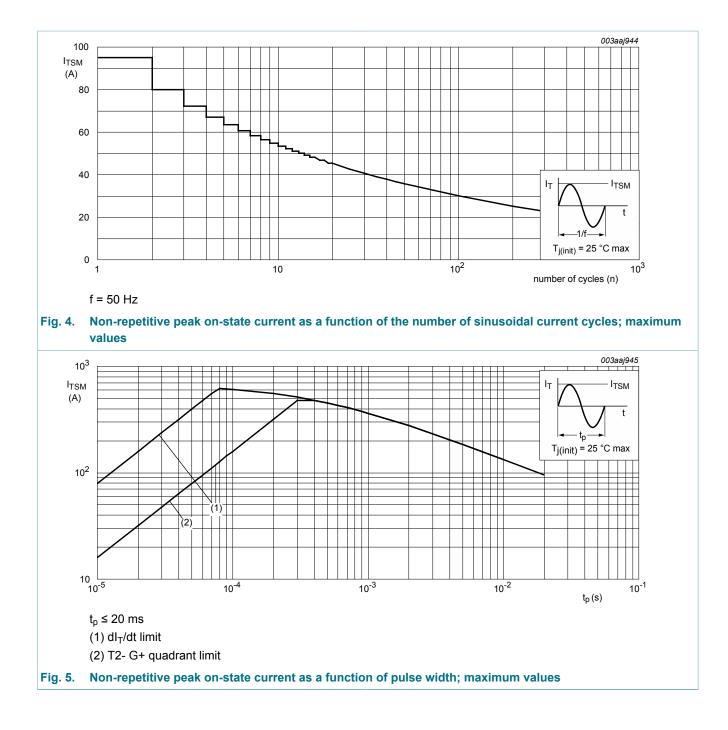
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4Q Triac



BT138-800G

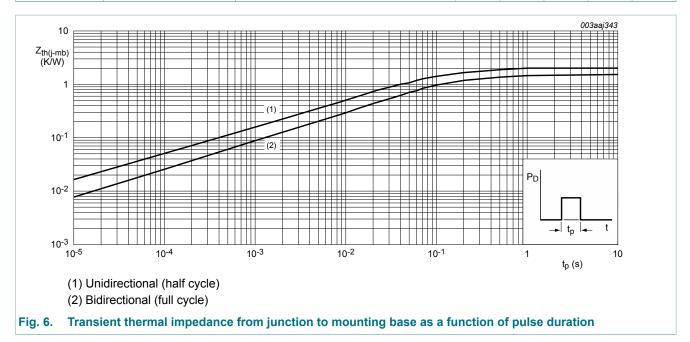
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8. Thermal characteristics

Table 5. The	ermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance	full cycle; <u>Fig. 6</u>	-	-	1.5	K/W
	from junction to mounting base	half cycle; Fig. 6	-	-	2	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W



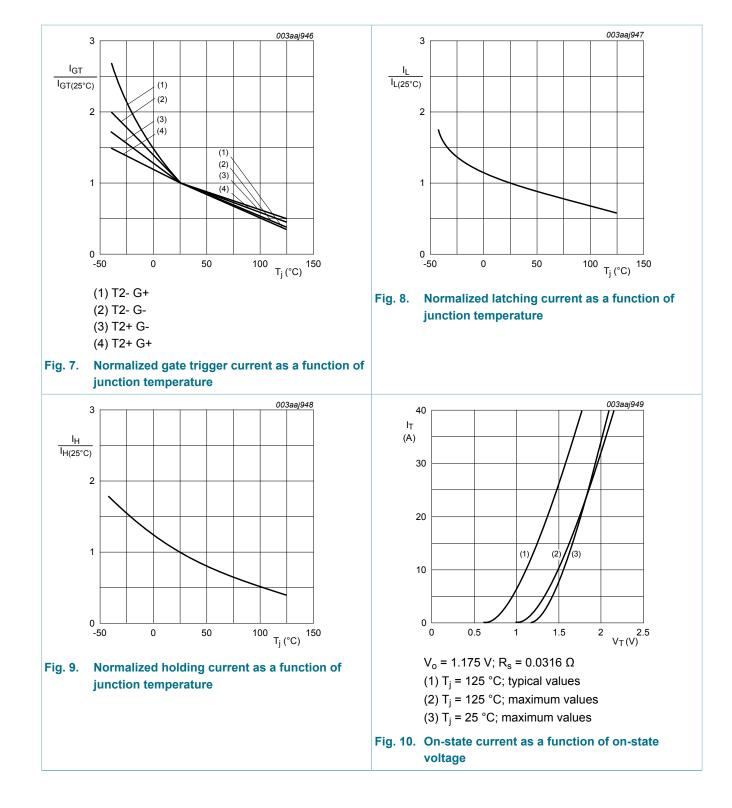
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9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	· · · · ·				
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2+ G+};$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	-	5	50	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 7</u>	-	8	50	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 7</u>	-	10	50	mA
		V _D = 12 V; I _T = 0.1 A; T2- G+; T _j = 25 °C; <u>Fig. 7</u>	-	22	100	mA
lL	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 8</u>	-	7	60	mA
		$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2+ G-};$ T _j = 25 °C; Fig. 8	-	20	90	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 8</u>	-	8	60	mA
		V _D = 12 V; I _G = 0.1 A; T2- G+; T _j = 25 °C; <u>Fig. 8</u>	-	10	90	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	6	60	mA
V _T	on-state voltage	I _T = 15 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; Fig. 11	-	0.7	1	V
		V _D = 400 V; I _T = 0.1 A; T _j = 125 °C; Fig. 11	0.25	0.4	-	V
I _D	off-state current	V _D = 800 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic ch	naracteristics	·	I			
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	200	250	-	V/µs
t _{gt}	gate-controlled turn-on time	I_{TM} = 16 A; V _D = 800 V; I _G = 0.1 A; dI _G / dt = 5 A/µs	-	2	-	μs

BT138-800G

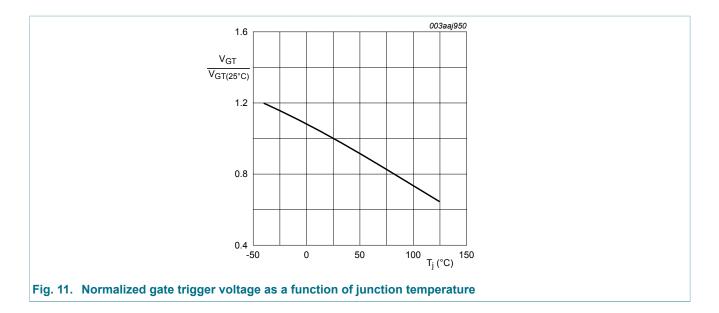
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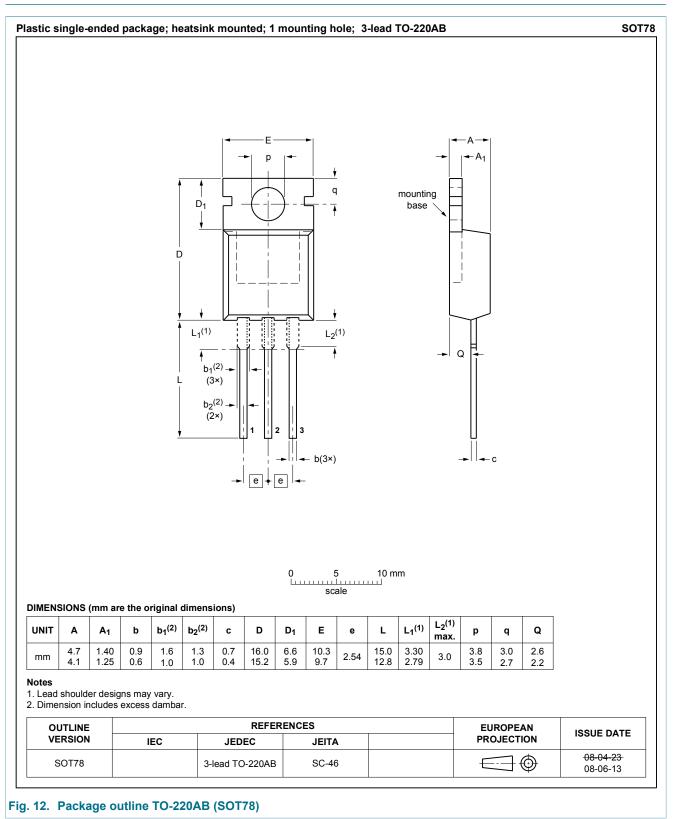
BT138-800G

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10. Package outline



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11. Legal information

11.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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12. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Limiting values	3
8	Thermal characteristics	6
9	Characteristics	7
10	Package outline	10
	Fackage outline	
11	Legal information	
11 11.1	-	11
••	Legal information	11 11
11.1	Legal information Data sheet status	11 11 11

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