

### General Description

The KP1500 series are monolithic integrated circuits that provide all the active functions for a step-down DC/DC converter, capable of driving a 3A load without additional transistor component. Requiring a minimum number of external component, the board space can be saved easily. The external shutdown function can be controlled by TTL logic level and then come into standby mode. The internal compensation makes feedback control have good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. The KP1500 series operates at a switching frequency of thus allowing sized 150Khz smaller components than what would be needed with lower frequency switching regulators. Other features include a guaranteed +4% tolerance on output voltage under specified input voltage and output load conditions, and +15% on the oscillator frequency. The output version included fixed 3.3V, 5V, 12V, and an adjustable type. The packages are available in a standard 5-lead TO-220(T) package and a 5-lead TO-263(S).

### Features

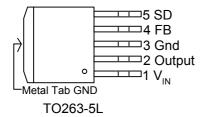
- 3.3V, 5V, 12V and adjustable output versions
- Adjustable version output voltage range, 1.23V to 28V +4% max over line and load condiction
- Available in TO220-5L and TO263-5L packages
- Voltage mode non-synchronous PWM control
- Thermal-shutdown and current-limit protection
- ON/OFF shutdown control input
- Input voltage range up to 32V
- Output load current: 3A
- 150 kHz fixed frequency internal oscillator
- Low power standby mode
- Built-in switching transistor on chip

## ■ Applications

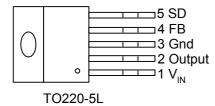
- Simple High-efficiency step-down(buck) regulator
- Efficient preregulator for linear regulators
- On-card switching regulators
- Positive to negative converter
- Battery Charger

# ■ Pin Assignments

(Top View)



(Top View)

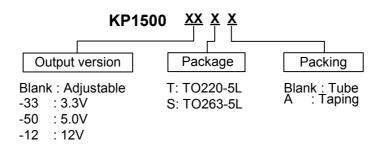


### Pin Descriptions

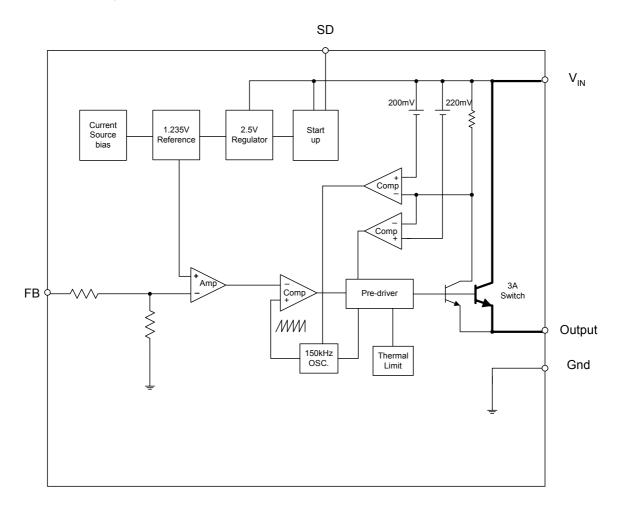
Name	Description			
V <sub>IN</sub>	Operating voltage input			
Output	Switching output			
Gnd	Ground			
FB	Output voltage feedback control			
SD	ON/OFF Shutdown			



# ■ Ordering Information



# **■** Block Diagram





**Absolute Maximum Ratings** 

Symbol	Parameter	Rating	Unit
V <sub>CC</sub>	Supply Voltage	+34	V
$V_{SD}$	ON/OFF Pin input voltage	-0.3 to +25	V
$V_{FB}$	Feedback Pin voltage	-0.3 to +25	V
V <sub>OUT</sub>	Output voltage to Ground	-1	V
P <sub>D</sub>	Power dissipation	Internally limited	W
T <sub>ST</sub>	Storage temperature	-65 to +150	°C
T <sub>OP</sub>	Operating temperature	-40 to +125	°C
V <sub>OP</sub>	Operating voltage	+4.5 to +25	V

# ■ Electrical Characteristics (All Output Voltage Versions) Unless otherwise specified, V<sub>IN</sub>=12V for 3.3V, 5V, adjustable version and V<sub>IN</sub>=24V for the 12V version. I<sub>LOAD</sub> = 0.3A

Symbol	Parameter		ditions	Min.	Тур.	Max.	Unit
l <sub>B</sub>	I <sub>B</sub> Feedback bias current		V <sub>FB</sub> =1.3V		40	60	nA
		(Adjustable version only)			10	100	ША
F	F <sub>OSC</sub> oscillator frequency				150	173	Khz
OSC						173	MIZ
	and worth an analysis and	I <sub>OUT</sub> =3A	it		1.3	1.4	V
$V_{SAT}$	saturation voltage	no outside circuit V <sub>FB</sub> =0V force driver on				1.5	
DC	Max. Duty Cycle(ON)	V <sub>FB</sub> =0V force driver on			100		%
DC	Min. Duty cycle(OFF)	V <sub>FB</sub> =12V force driver off			0		%
1	accommon to line it	peak current		0.0	4.5	5.5	А
I <sub>CL</sub>	current limit	no outside ci V <sub>FB</sub> =0 force o		3.6		6.5	
IL	Output= 0V Output leakage	no outside ci V <sub>FB</sub> =12 force				-200	uA
_	Output=-1V current	V <sub>IN</sub> =24V			-5		mA
IQ	Quiescent Current	V <sub>FB</sub> =12 force	driver off		5	10	mA
	, Standby Quiescent		ON/OFF pin=5V		150	250	
$I_{STBY}$	Current	V <sub>IN</sub> =24V			150	300	uA
$V_{IL}$	ON/OFF sin logic input	Low (regulate	or ON)	-		0.6	.6 - V
$V_{\text{IH}}$	ON/OFF pin logic input threshold voltage	High (regulator OFF)		2.0	1.3	-	
I <sub>H</sub>	ON/OFF pin logic input current	V <sub>LOGIC</sub> =2.5V	(OFF)		15	25	uA
ΙL	ON/OFF pin input current	V <sub>LOGIC</sub> =0.5V (ON)			0.02	5	
Өлс	Thermal Desistance	TO220-5L	Junction to		2.5		°C/W
	Thermal Resistance	TO263-5L	case		3.5		C/VV
	Thermal Resistance	TO220-5L	Junction to		28		°C/W
⊖ <sub>JA</sub>	With copper area of approximately 3 in <sup>2</sup>	TO263-5L			23		



# ■ Electrical Characteristics (Continued)

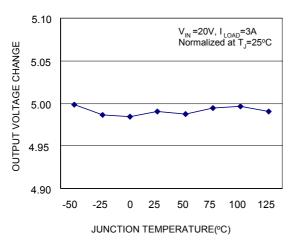
	Symbol	Parameter	Conditions	Тур.	Limit	Unit
KP1500-ADJ	$V_{FB}$	Output Feedback	$\begin{array}{c} 5V \leq V_{\text{IN}} \leq 32V \\ 0.2A \leq I_{\text{LOAD}} \leq 3A \\ V_{\text{OUT}} \text{ programmed for} \\ 3V \end{array}$	1.23	1.193/ <b>1.18</b> 1.267/ <b>1.28</b>	V V <sub>MIN</sub> V <sub>MAX</sub>
	η	Efficiency	V <sub>IN</sub> = 12V, I <sub>LOAD</sub> =3A	73		%
KP1500-3.3V	V <sub>OUT</sub>	Output voltage	$5.5V \le V_{IN} \le 32V$ $0.2A \le I_{LOAD} \le 3A$	3.3	3.168/ <b>3.135</b> 3.432/ <b>3.465</b>	V V <sub>MIN</sub> V <sub>MAX</sub>
	η	Efficiency	$V_{IN}$ = 12V, $I_{LOAD}$ =3A	73		%
KP1500-5V	V <sub>OUT</sub>	Output voltage	$8V \le V_{IN} \le 32V$ $0.2A \le I_{LOAD} \le 3A$	5	4.8/ <b>4.75</b> 5.2/ <b>5.25</b>	V V <sub>MIN</sub> V <sub>MAX</sub>
	η	Efficiency	$V_{IN}$ = 12V, $I_{LOAD}$ =3A	80		%
KP1500-12V	V <sub>out</sub>	Output voltage	$15V \le V_{\text{IN}} \le 32V$ $0.2A \le I_{\text{LOAD}} \le 3A$	12	11.52/ <b>11.4</b> 12.48/ <b>12.6</b>	V V <sub>MIN</sub> V <sub>MAX</sub>
	η	Efficiency	V <sub>IN</sub> = 15V, I <sub>LOAD</sub> = 3A	90		%

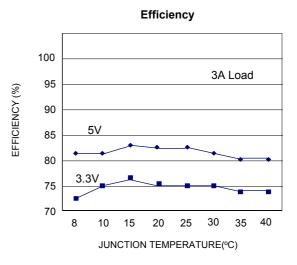
Specifications with **boldface type** are for full operating temperature range, the other type are for  $T_J=25^{\circ}C$ .



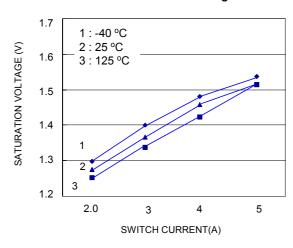
# **■** Typical Performance Characteristics

Typical Performance Characteristics Normalized Output Voltage

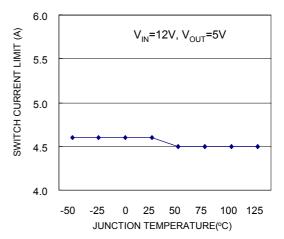




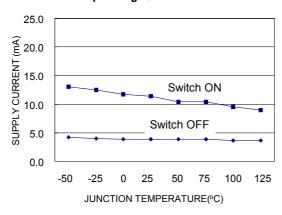
#### **Switch Saturation Voltage**



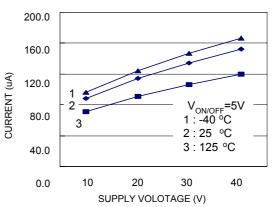
#### **Switch Current Limit**



#### **Operating Quiescent Current**



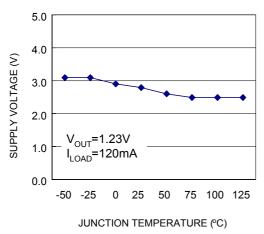
#### Shutdown Quiescent Current



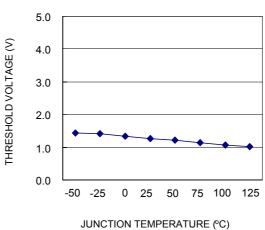


# **Typical Performance Characteristics (Continued)**

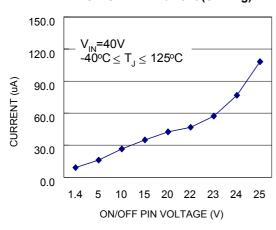
### **Minimum Operating Supply Voltage**



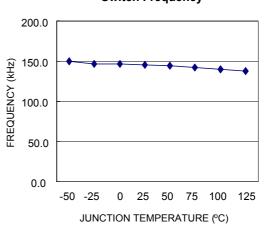
### **ON/OFF Threshold Voltage**



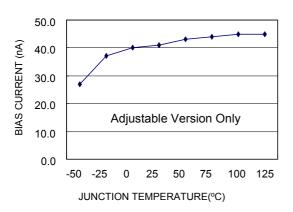
#### **ON/OFF Pin Current (Sinking)**



#### **Switch Frequency**



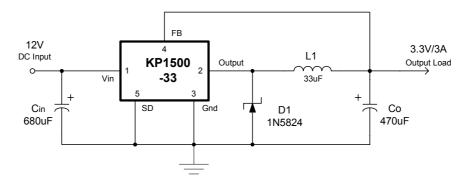
#### **Feedback Pin Bias Current**



Kalai Technology Corp. www.kalai.com.tw Rev.2.0 Feb.15, 2006



### ■ Typical Application Circuit



### **■** Function Description

#### **Pin Functions**

+V<sub>IN</sub>

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be present at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

#### Ground

Circuit ground.

#### Output

Internal switch. The voltage at this pin switches between ( $+V_{IN}-V_{SAT}$ ) and approximately -0.5V, with a duty cycle of approximately  $V_{OUT}/V_{IN}$ . To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be kept a minimum.

#### Feedback

Senses the regulated output voltage to complete the feedback loop.

#### ON/OFF

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 150uA. Pulling this pin below a threshold voltage of approximately 1.3V turns the regulator on, and pulling this pin above 1.3V (up to a maximum of 25V) shuts the regulator down. If this shutdown feature is not needed, the  $\overline{\text{ON}}/\text{OFF}$  pin can be wired to the ground pin or it can be left open, in either case the regulator will be in the ON condition.

#### **Thermal Considerations**

The KP1500 is available in two packages, a 5-pin TO-220 and a 5-pin surface mount TO-263.

The TO-220 package needs a heat sink under most conditions. The size of the heatsink depends on the input voltage, the output voltage, the load current and the ambient temperature. The KP1500 junction temperature rises above ambient temperature for a 3A load and different input and output voltages. The data for these curves was taken with the KP1500 (TO-220 package) operating as a buck switching regulator in an ambient temperature of 25°C (still air). These temperature rise numbers are all approximate and there are many factors that can affect these temperatures. Higher ambient temperatures require more heat sinking.

The TO-263 surface mount package tab is designed to be soldered to the copper on a printed circuit board. The copper and the board are the heat sink

for this package and the other heat producing components, such as the catch diode and inductor. The PC board copper area that the package is soldered to should be at least 0.8 in², and ideally should have 2 or more square inches of 2 oz. Additional copper area improves the thermal characteristics, but with copper areas greater than approximately 6 in², only small improvements in heat dissipation are realized. If further thermal improvements are needed, double sided, multilayer PC board with large copper areas and/or airflow are recommended.



### **■ Function Description (Continued)**

The KP1500 (TO-263 package) junction temperature rise above ambient temperature with a 2A load for various input and output voltages. This data was taken with the circuit operating as a buck switching regulator with all components mounted on a PC board to simulate the junction temperature under actual operating conditions. This curve can be used for a quick check for the approximate junction temperature for various conditions, but be aware that there are many factors that can affect the junction temperature. When load currents higher than 3A are used, double sided or multilayer PC boards with large copper areas and/or airflow might be needed, especially for high ambient temperatures and high output voltages.

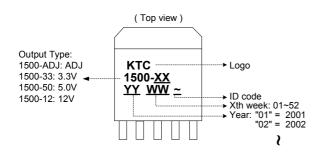
For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper should be used in the board layout. (Once exception to this is the output (switch) pin, which should not have large areas of copper.) Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding

air, and moving air lowers the thermal resistance even further.

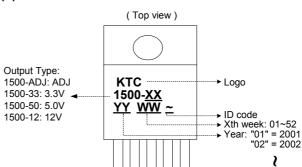
Package resistance thermal and junction temperature rise numbers are all approximate, and there are many factors that will affect these Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multilayer board and the amount of solder on the board. The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving. Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.

### ■ Marking Information

#### (1) TO263-5L



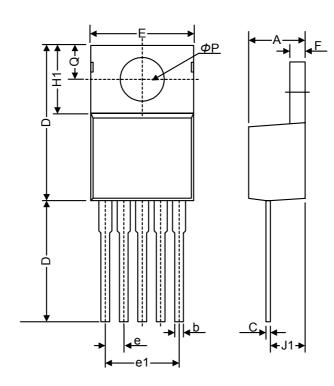
#### (2) TO220-5L





# ■ Package Information

(1) Package Type: TO220-5L

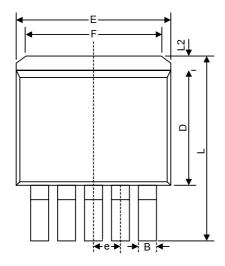


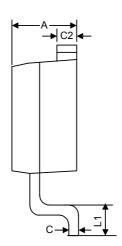
Symbol	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.06	4.83	0.160	0.190	
b	0.76	1.02	0.030	0.040	
С	0.36	0.64	0.014	0.025	
D	14.22	15.49	0.560	0.610	
Е	9.78	10.54	0.385	0.415	
е	1.57	1.85	0.062	0.073	
e(1)	6.68	6.93	0.263	0.273	
F	1.14	1.40	0.045	0.055	
H(1)	5.46	6.86	0.215	0.270	
J(1)	2.29	3.18	0.090	0.125	
L	13.21	14.73	0.520	0.580	
ΦP	3.68	3.94	0.145	0.155	
Q	2.54	2.92	0.100	0.115	

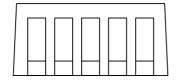


# ■ Package Information (Continued)

(2) Package Type: TO263-5L







Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.48	4.68	0.176	0.184	
В	0.72	0.92	0.028	0.036	
С	0.36	0.41	0.014	0.016	
C2	1.18	1.38	0.046	0.054	
D	8.33	8.53	0.327	0.335	
E	9.96	10.36	0.392	0.407	
е	1.60	1.80	0.062	0.070	
F	6.80	7.20	0.267	0.283	
L	15.14	15.74	0.596	0.619	
L1	2.00	2.40	0.078	0.094	
L2	1.40	1.80	0.055	0.070	