

Low Input Voltage 3A LDO Regulator

NO.EA-203-140522

OUTLINE

The RP108J is a CMOS-based voltage regulator IC featuring 3A output with low ON-resistance.

The RP108J consists of a voltage reference unit, an error amplifier, resistor-net for voltage setting, a fold-back protection circuit, and a thermal shutdown circuit. The RP108J features both low supply current and high output current, and the dropout voltage is much smaller than bi-polar's. The minimum input voltage is as low as 1.6V and the output voltage can be set from 0.8V, therefore it can be connected with the DC/DC converter as the latter power supply for high density LSI that is operated by low output voltage.

The output voltage of RP108J081x is externally adjustable by using external divide resistors. The CE pin of the RP108J can switch the regulator to standby mode. In addition to a fold-back protection circuit, which is already built in the conventional regulators, The RP108J contains a thermal shutdown circuit, a constant slope circuit as a soft-start function and a reverse current protection circuit. Ceramic capacitors can be used.

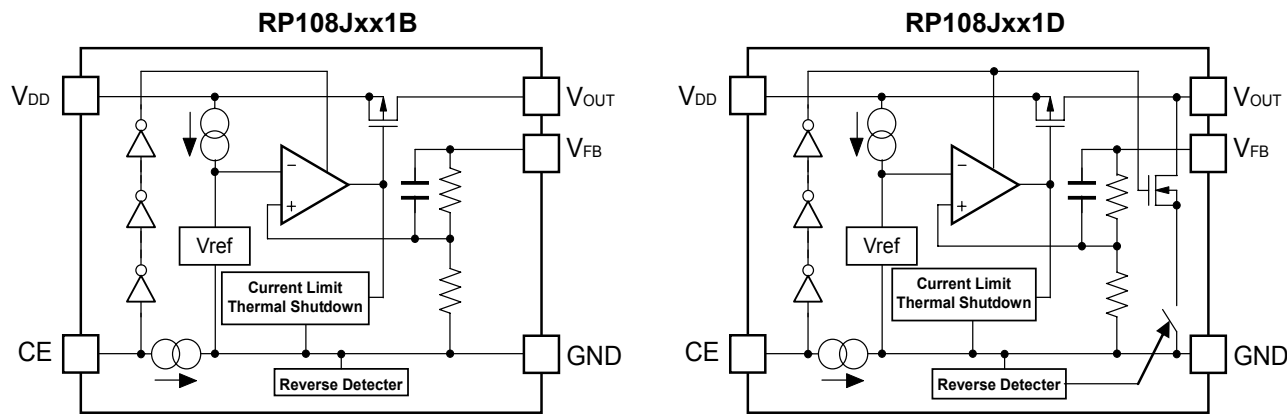
FEATURES

- Output Current Min. 3A
- Supply Current Typ. 350 μ A
- Standby Current Typ. 2 μ A
- Input Voltage Range 1.6V to 5.25V
- Output Voltage Range Fixed Output Voltage Type: 0.8V to 4.2V (0.1V steps)
 * Refer to *MARK INFORMATION*S for other voltages.
 Adjustable Output Voltage Type: 0.8V to 4.2V
- Output Voltage Accuracy $\pm 1.0\%$ (± 15 mV accuracy, When $V_{SET} \leq 1.5$ V)
- Output Voltage Temperature-drift Coefficient Typ. ± 100 ppm/ $^{\circ}$ C
- Ripple Rejection Typ. 65dB ($f = 1$ kHz, $V_{SET} = 2.8$ V)
- Dropout Voltage Typ. 0.51V ($V_{SET} = 2.8$ V)
- Line Regulation Typ. 0.1%/V
- Package TO-252-5-P2
- Built-in Fold-back Protection Circuit Typ. 220mA
- Built-in Thermal Shutdown Circuit Stops at 165 $^{\circ}$ C
- Built-in Constant Slope Circuit
- Built-in Reverse Current Protection Circuit
- Ceramic capacitors are recommended to be used with this IC 10 μ F or more

APPLICATIONS

- Power source for battery-powered equipments.
- Power source for portable communication equipments such as cameras and VCRs.
- Power source for electrical home appliances.

BLOCK DIAGRAMS



SELECTION GUIDE

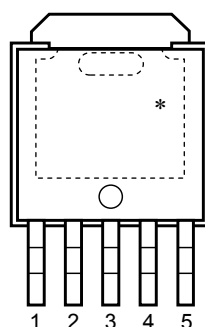
The output voltage, auto discharge function for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP108Jxx1*(y) -T1-FE	TO-252-5-P2	3,000 pcs	Yes	Yes
xx: The set output voltage (V_{SET}) can be designated in the range of 0.8V(08) to 4.2V(42) in 0.1V steps. y: If the output voltage includes the 3rd digit, indicate the digit of 0.01V. (1.25V, 1.85V, 2.85V) Ex. If the output voltage is 1.25V, RP108J121*5-T1-FE. If the output voltage is 1.85V, RP108J181*5-T1-FE. If the output voltage is 2.85V, RP108J281*5-T1-FE. *: Designation of auto-discharge function (B) No Auto-discharge function (D) Auto-discharge function				

Auto-discharge function quickly lowers the output voltage to 0V, when the chip enable signal is switched from the active mode to the standby mode, by releasing the electrical charge accumulated in the external capacitor.

PIN DESCRIPTIONS

TO-252-5-P2



TO-252-5-P2

Pin No.	Symbol	Description
1	CE	Chip Enable Pin ("H" Active)
2	V _{DD}	Input Pin
3	GND	Ground Pin
4	V _{OUT}	Output Pin
5	V _{FB}	Feedback Pin

*1 The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left floating.

The V_{OUT} pin should be connected to the V_{FB} pin when using RP108J as an internal fixed output voltage type. In case of using RP108J as an external adjustable type, please refer to "Adjustable Output Voltage Type Settings".

ABSOLUTE MAXIMUM RATINGS

Symbol	Item		Rating	Unit
V _{IN}	Input Voltage		6.0	V
V _{CE}	Input Voltage (CE Input Pin)		−0.3 to 6.0	V
V _{FB}	Input Voltage (V _{FB} Pin)		−0.3 to 6.0	V
V _{OUT}	Output Voltage		−0.3 to V _{IN} +0.3	V
P _D	Power Dissipation (TO-252-5-P2)*1	High Wattage Land Pattern	3800	mW
		Standard Land Pattern	1900	
T _a	Operating Temperature		−40 to 85	°C
T _{stg}	Storage Temperature		−55 to 125	°C

*1 For Power Dissipation, please refer to *PACKAGE INFORMATION*.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

ELECTRICAL CHARACTERISTICS

$V_{IN} = V_{SET} + 1.0V$, $I_{OUT} = 1mA$, $C_{IN} = C_{OUT} = 10\mu F$, unless otherwise noted.

The specifications surrounded by are guaranteed by design engineering at $-40^{\circ}C \leq T_a \leq 85^{\circ}C$.

RP108Jxx1B/D

($T_a = 25^{\circ}C$)

Symbol	Item	Conditions		Min.	Typ.	Max.	Unit
V _{OUT}	Output Voltage	Ta = 25°C	V _{SET} > 1.5V	×0.99		×1.01	V
			V _{SET} ≤ 1.5V	-15		15	mV
		-40°C ≤ Ta ≤ 85°C	V _{SET} > 1.5V	×0.97		×1.02	V
			V _{SET} ≤ 1.5V	-45		30	mV
I _{LIM}	Output Current Limit			3.0			A
ΔV _{OUT} /ΔI _{OUT}	Load regulation	1mA ≤ I _{OUT} ≤ 300mA		-15	2.0	20	mV
		1mA ≤ I _{OUT} ≤ 3000mA		-70	3.0	50	
V _{DIF}	Dropout Voltage	Please refer to <i>Dropout Voltage</i> on the next page.					
I _{SS}	Supply Current	I _{OUT} = 0mA			350	500	μA
I _{standby}	Standby Current	V _{CE} = 0V			2.0	5.0	μA
ΔV _{OUT} /ΔV _{IN}	Line Regulation	V _{SET} +0.5V ≤ V _{IN} ≤ 5.25V, I _{OUT} =1mA (When V _{SET} ≤ 1.1V, V _{IN} = 1.6V)			0.10	0.15	%/V
V _{IN}	Input Voltage*1			1.6		5.25	V
RR	Ripple Rejection	f = 1kHz, Ripple 0.2Vp-p I _{OUT} =100mA	V _{SET} ≤ 2.8V		65		dB
			V _{SET} > 2.8V		55		dB
ΔV _{OUT} /ΔTa	Output Voltage Temperature Coefficient	-40°C ≤ Ta ≤ 85°C			±100		ppm/°C
I _{SC}	Short Current Limit	V _{OUT} = 0V			220		mA
I _{PD}	CE Pull-down Current				0.3	0.6	μA
V _{CEH}	CE Input Voltage "H"			1.0			V
V _{CEL}	CE Input Voltage "L"					0.4	V
en	Output Noise	BW = 10Hz to 100kHz			70		μVrms
T _{TSD}	Thermal Shutdown Temperature	Junction Temperature			165		°C
T _{TSR}	Thermal Shutdown Released Temperature	Junction Temperature			95		°C
R _{LOW}	Auto-discharge Nch Tr. ON Resistance (D version)	V _{IN} = 4.0V, V _{CE} = 0V			30		Ω
I _{REV}	Reverse Current Limit	V _{OUT} > 0.5V, 0V ≤ V _{IN} ≤ 5.25V			10		μA

All test items listed under *Electrical Characteristics* are done under the pulse load condition ($T_j \approx T_a = 25^{\circ}C$) except Ripple Rejection, Output Voltage Temperature Coefficient, Output Noise and Thermal Shutdown.

*1 The maximum input voltage listed under *Electrical Characteristics* is 5.25V. If for any reason the input voltage exceeds 5.25V, it has to be no more than 5.5V with 500 cumulative operating hours.

The specifications surrounded by are guaranteed by design engineering at $-40^{\circ}\text{C} \leq T_a \leq 85^{\circ}\text{C}$.

Dropout Voltage by Output Voltage

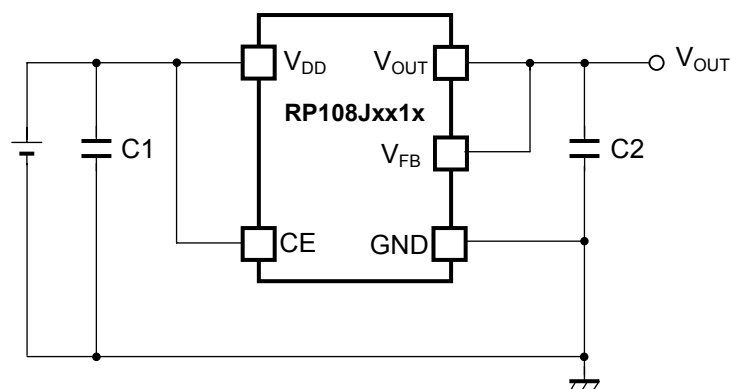
($T_a=25^{\circ}\text{C}$)

Output Voltage V_{SET} (V)	Dropout Voltage V_{DIF} (V)		
	Condition	Typ.	Max.
$0.8 \leq V_{\text{SET}} < 0.9$	$I_{\text{OUT}}=3000\text{mA}$	0.910	1.110
$0.9 \leq V_{\text{SET}} < 1.0$		0.865	1.000
$1.0 \leq V_{\text{SET}} < 1.1$		0.810	0.950
$1.1 \leq V_{\text{SET}} < 1.2$		0.755	0.895
$1.2 \leq V_{\text{SET}} < 1.5$		0.720	0.840
$1.5 \leq V_{\text{SET}} < 2.5$		0.630	0.760
$2.5 \leq V_{\text{SET}} < 3.3$		0.510	0.600
$3.3 \leq V_{\text{SET}} \leq 4.2$		0.480	0.560

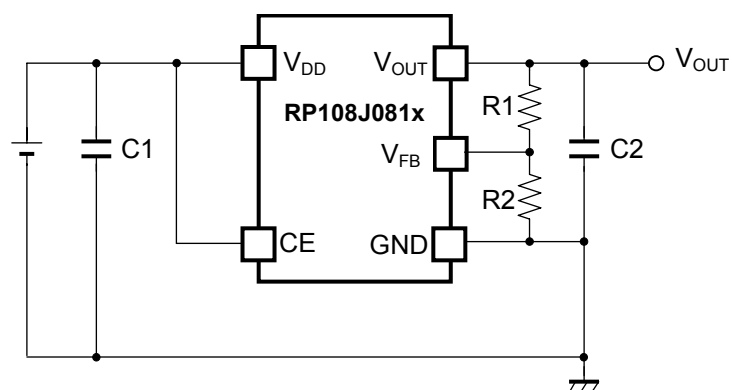
RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

TYPICAL APPLICATIONS



Typical Application



Typical application for adjustable output voltage type

External Components :

Symbol	Descriptions
C1, C2	10 μ F (Ceramic), CM21X7R106M06AB, KYOCERA

TECHNICAL NOTES

When using the RP108J as an internally fixed output voltage type, please connect the V_{OUT} pin to the V_{FB} pin. However, when using it as the Adjustable Output Voltage Type, The output voltage of the externally adjustable output voltage type should be set to 4.2V or less. Also, total resistors value of R1 and R2 should be 20k Ω or less.

Phase Compensation

In the IC, phase compensation is made for securing stable operation even if the load current is varied.

For this purpose, use a 10 μ F or more capacitor C2.

In case of using a tantalum capacitor and its ESR is large, the output may be unstable.

Therefore, select C2 carefully considering its frequency characteristics.

The recommended temperature characteristics for C1 and C2 capacitors are the followings.

- B Characteristics: Temperature range from -25°C to 85°C , Capacitance change of $\pm 10\%$
- X5R Characteristics: Temperature range from -55°C to 85°C , Capacitance change of $\pm 15\%$
- X7R Characteristics: Temperature range from -55°C to 125°C , Capacitance change of $\pm 15\%$

The recommended capacitor's tolerable voltage is twice as large as the voltage of use (C1: Input voltage, C2: Output voltage). The upper limit of the capacitance value for C2 is 100 μ F.

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result.

Connect a capacitor C1 with a capacitance value as much as 10 μ F or more between V_{DD} and GND pin, and as close as possible to the pins.

Set external components, especially the output capacitor C2, as close as possible to the ICs and make wiring as short as possible.

Transient Response

When using the Adjustable Output Voltage Type, the transient response could be affected by the external resistors. Evaluate the circuit taking the actual conditions of use into account.

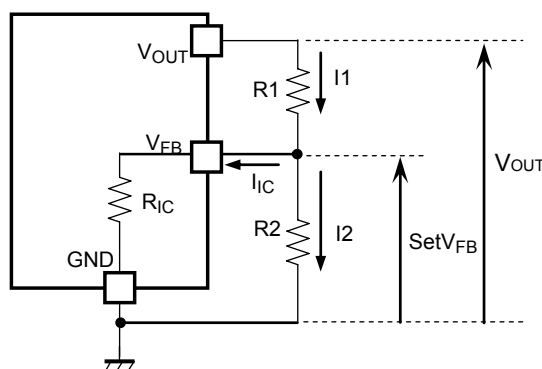
ADJUSTABLE OUTPUT VOLTAGE TYPE SETTINGS

• Output Voltage Setting Method

RP108J081x can be adjusted the output voltage by using the external divider resistors.

If the V_{FB} voltage fixed into the IC is described as $SetV_{FB}$, the output voltage can be set by using the following equations

$SetV_{FB}$ is equal to 0.8V. The V_{OUT} pin of RP108J081x should be connected to the V_{FB} pin.



$$I_1 = I_{IC} + I_2 \dots\dots\dots (1)$$

$$I_2 = SetV_{FB} / R_2 \dots\dots\dots (2)$$

Thus,

$$I_1 = I_{IC} + SetV_{FB} / R_2 \dots\dots\dots (3)$$

Therefore,

$$V_{OUT} = SetV_{FB} \times R_1 \times I_1 \dots\dots\dots (4)$$

Put Equation (3) into Equation (4), then

$$V_{OUT} \dots\dots\dots = SetV_{FB} + R_1(I_{IC} + SetV_{FB} / R_2) \\ = SetV_{FB} \times (1 + R_1 / R_2) + R_1 \times I_{IC} \dots\dots\dots (5)$$

In Equation (5), $R_1 \times I_{IC}$ is the error-causing factor in V_{OUT} .

As for I_{IC} ,

$$I_{IC} = SetV_{FB} / R_{IC} \dots\dots\dots (6)$$

Therefore, the error-causing factor $R_1 \times I_{IC}$ can be described as follows.

$$R_1 \times I_{IC} \dots\dots\dots = R_1 \times SetV_{FB} / R_{IC} \\ = SetV_{FB} \times R_1 / R_{IC} \dots\dots\dots (7)$$

For better accuracy, choosing R_1 ($\ll R_{IC}$) reduces this error.

Without the error-causing factor $R_1 \times I_{IC}$, the output voltage can be calculated by the following equation

$$V_{OUT} = SetV_{FB} \times ((R_1 + R_2) / R_2) \dots\dots\dots (8)$$

R_{IC} of RP108J is approximately Typ.1.6M Ω ($T_a=25^\circ\text{C}$, this value is guaranteed by design.).

The value could be affected by the temperature, therefore evaluate the circuit taking the actual conditions of use into account.

REVERSE CURRENT PROTECTION CIRCUIT

The RP108J includes a Reverse Current Protection Circuit, which stops the reverse current from V_{OUT} pin to V_{DD} pin or to GND pin when V_{OUT} becomes higher than V_{IN} .

Usually, the LDO using Pch output transistor contains a parasitic diode between V_{DD} pin and V_{OUT} pin. Therefore, if V_{OUT} is higher than V_{IN} , the parasitic diode becomes forward direction. As a result, the current flows from V_{OUT} pin to V_{DD} pin.

The RP108J switches the mode to the reverse current protection mode before V_{IN} becomes lower than V_{OUT} by connecting the parasitic diode of Pch output transistor to the backward direction, and connecting the gate to V_{OUT} pin. As a result, the Pch output transistor is turned off and the all the current pathways from V_{OUT} pin to GND pin are shut down to maintain the reverse current lower than $10\mu A$.

Switching to either the normal mode or to the reverse current protection mode is determined by the magnitude of V_{IN} voltage and V_{OUT} voltage. For the stable operation, offset and hysteresis are set as the threshold. Offset is set to 30mV (Typ.25°C) and hysteresis is set to 5mV (Typ.25°C).

Therefore, the minimum dropout voltage under the small load current condition is restricted by the value of 35mV (Typ.25°C).

Fig.1 and Fig.2 show the diagrams of each mode, and Fig.3 shows the load characteristics of each mode. When giving the V_{OUT} pin a constant-voltage and decreasing the V_{IN} voltage, the dropout voltage will become lower than 30mV (Typ.25°C). As a result, the reverse current protection starts to function to stop the load current. By increasing the dropout voltage higher than 35mV (Typ.25°C), the protection mode will be released to let the load current to flow. If the dropout voltage to be used is lower than 30mV (Typ.25°C), the detection and the release may be repeated.

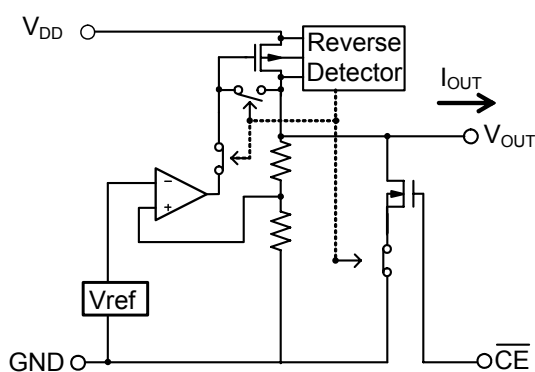


Fig.1 Normal Mode

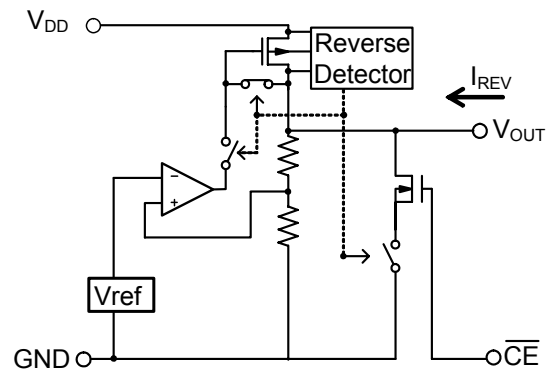


Fig.2 Reverse Current Protection Mode

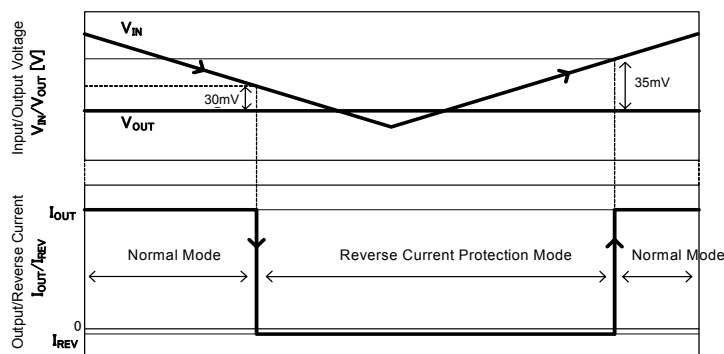


Fig. 3 Reverse Current Protection Mode Detection/ Release & Reverse Current/ Output Current Characteristics

CONSTANT SLOPE CIRCUIT

The RP108J has a Constant Slope Circuit as a function which prevents the overshoot of the output voltage.

The constant slope is a kind of soft-start circuit which allows the output voltage to start up gradually.

The capacitor to create the start-up slope is built-in the IC that doesn't require any external components.

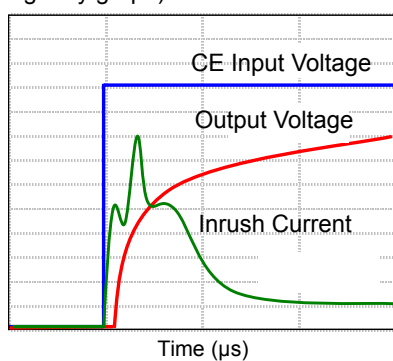
The start-up time and start-up slope angle are fixed inside the IC.

If the capacitance of the external output capacitor becomes more than the certain capacitance, the output current limit circuit minimizes the incoming current of the output capacitor at the start-up. As a result, the start-up time becomes longer and the start-up slope angle becomes more gentle.

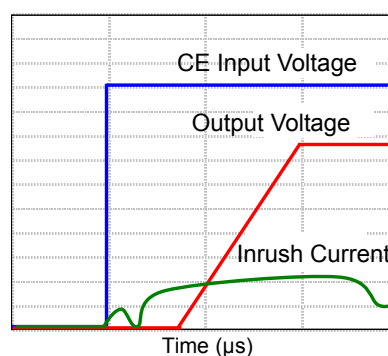
The constant slope circuit is an especially effective function for large current product like 3A output, because driver's ability is high, and it allows a large current to flow at the start-up.

Inrush current limit circuit of conventional products

(Imaginary graph)



Constant Slope Circuit



PACKAGE INFORMATION

Power Dissipation (TO-252-5-P2)

Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the Measurement Conditions below.

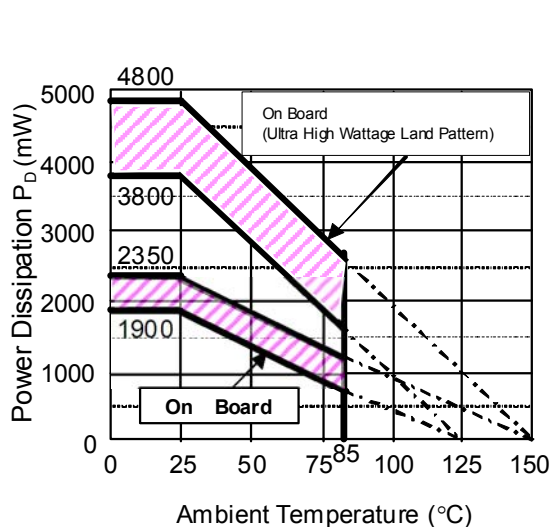
Measurement conditions

	Ultra High Wattage Land Pattern	Standard Land Pattern
Environment	Mounting on board (Wind velocity 0m/s)	
Board Material	Glass cloth epoxy plastic (Four-layers)	Glass cloth epoxy plastic (Double layers)
Board Dimensions	76.2mm x 114.3mm x 0.8mm	50mm x 50mm x 1.6mm
Copper Ratio	Top, Back side: 50mm Square, Approx.96%, 2 nd , 3 rd Layer: 50mm Square, Approx.100%	Top side: Approx. 50%, Back side: Approx. 50%
Through - Hole	ϕ 0.4mm x 30pcs	ϕ 0.5mm x 24pcs

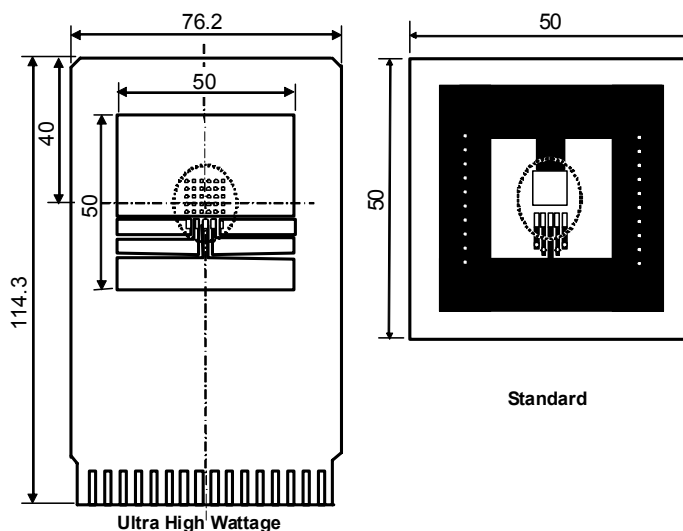
Measurement Results

($T_a=25^\circ\text{C}$, $T_{j\text{max}}=125^\circ\text{C}$)

	Ultra High Wattage Land Pattern	Standard Land Pattern
Power Dissipation	3800mW	1900mW
Thermal Resistance	$\theta_{ja} = (125-25^\circ\text{C})/3.8\text{W} = 26^\circ\text{C/W}$	$\theta_{ja} = (125-25^\circ\text{C})/1.9\text{W} = 53^\circ\text{C/W}$
	$\theta_{jc} = 7^\circ\text{C/W}$	$\theta_{jc} = 17^\circ\text{C/W}$



Power Dissipation



Measurement Board Pattern

IC Mount Area (Unit : mm)

The above graph shows the Power Dissipation of the package based on $T_{j\text{max}}=125^\circ\text{C}$ and $T_{j\text{max}}=150^\circ\text{C}$. Operating the IC in the shaded area in the graph might have an influence its lifetime.

Operating time must be within the time limit described in the table below, in case of operating in the shaded area.

Operating Time	Estimated years (Operating 4 hours/day)
13,000 hours	9 years

RP108J

NO.EA-203-140522

RP108Jxx1B

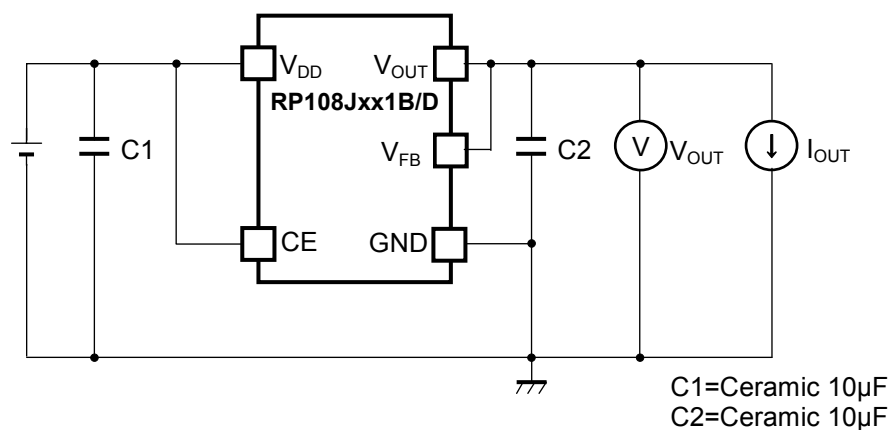
Part Number	①②③④⑤⑥⑦⑧	V _{SET}
RP108J081B	E 1 J 0 8 1 B	0.8V
RP108J091B	E 1 J 0 9 1 B	0.9V
RP108J101B	E 1 J 1 0 1 B	1.0V
RP108J111B	E 1 J 1 1 1 B	1.1V
RP108J121B	E 1 J 1 2 1 B	1.2V
RP108J131B	E 1 J 1 3 1 B	1.3V
RP108J141B	E 1 J 1 4 1 B	1.4V
RP108J151B	E 1 J 1 5 1 B	1.5V
RP108J161B	E 1 J 1 6 1 B	1.6V
RP108J171B	E 1 J 1 7 1 B	1.7V
RP108J181B	E 1 J 1 8 1 B	1.8V
RP108J191B	E 1 J 1 9 1 B	1.9V
RP108J201B	E 1 J 2 0 1 B	2.0V
RP108J211B	E 1 J 2 1 1 B	2.1V
RP108J221B	E 1 J 2 2 1 B	2.2V
RP108J231B	E 1 J 2 3 1 B	2.3V
RP108J241B	E 1 J 2 4 1 B	2.4V
RP108J251B	E 1 J 2 5 1 B	2.5V
RP108J261B	E 1 J 2 6 1 B	2.6V
RP108J271B	E 1 J 2 7 1 B	2.7V
RP108J281B	E 1 J 2 8 1 B	2.8V
RP108J291B	E 1 J 2 9 1 B	2.9V
RP108J301B	E 1 J 3 0 1 B	3.0V
RP108J311B	E 1 J 3 1 1 B	3.1V
RP108J321B	E 1 J 3 2 1 B	3.2V
RP108J331B	E 1 J 3 3 1 B	3.3V
RP108J341B	E 1 J 3 4 1 B	3.4V
RP108J351B	E 1 J 3 5 1 B	3.5V
RP108J361B	E 1 J 3 6 1 B	3.6V
RP108J371B	E 1 J 3 7 1 B	3.7V
RP108J381B	E 1 J 3 8 1 B	3.8V
RP108J391B	E 1 J 3 9 1 B	3.9V
RP108J401B	E 1 J 4 0 1 B	4.0V
RP108J411B	E 1 J 4 1 1 B	4.1V
RP108J421B	E 1 J 4 2 1 B	4.2V
RP108J121B5	E 1 J 1 2 1 B 5	1.25V
RP108J181B5	E 1 J 1 8 1 B 5	1.85V
RP108J281B5	E 1 J 2 8 1 B 5	2.85V

RP108Jxx1D

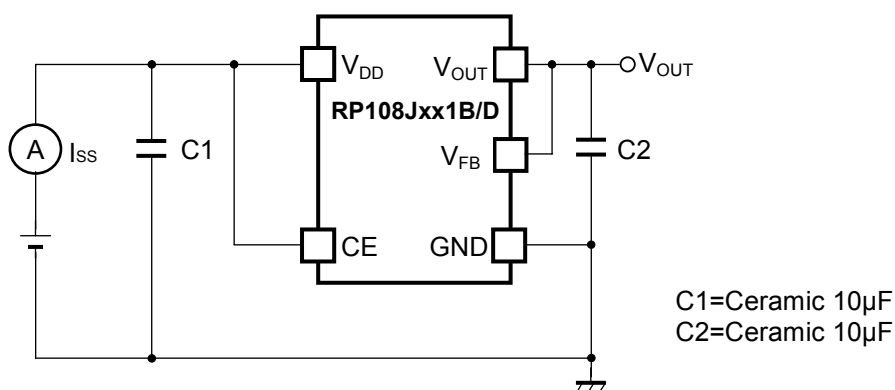
Part Number	①②③④⑤⑥⑦⑧	V _{SET}
RP108J081D	E 1 J 0 8 1 D	0.8V
RP108J091D	E 1 J 0 9 1 D	0.9V
RP108J101D	E 1 J 1 0 1 D	1.0V
RP108J111D	E 1 J 1 1 1 D	1.1V
RP108J121D	E 1 J 1 2 1 D	1.2V
RP108J131D	E 1 J 1 3 1 D	1.3V
RP108J141D	E 1 J 1 4 1 D	1.4V
RP108J151D	E 1 J 1 5 1 D	1.5V
RP108J161D	E 1 J 1 6 1 D	1.6V
RP108J171D	E 1 J 1 7 1 D	1.7V
RP108J181D	E 1 J 1 8 1 D	1.8V
RP108J191D	E 1 J 1 9 1 D	1.9V
RP108J201D	E 1 J 2 0 1 D	2.0V
RP108J211D	E 1 J 2 1 1 D	2.1V
RP108J221D	E 1 J 2 2 1 D	2.2V
RP108J231D	E 1 J 2 3 1 D	2.3V
RP108J241D	E 1 J 2 4 1 D	2.4V
RP108J251D	E 1 J 2 5 1 D	2.5V
RP108J261D	E 1 J 2 6 1 D	2.6V
RP108J271D	E 1 J 2 7 1 D	2.7V
RP108J281D	E 1 J 2 8 1 D	2.8V
RP108J291D	E 1 J 2 9 1 D	2.9V
RP108J301D	E 1 J 3 0 1 D	3.0V
RP108J311D	E 1 J 3 1 1 D	3.1V
RP108J321D	E 1 J 3 2 1 D	3.2V
RP108J331D	E 1 J 3 3 1 D	3.3V
RP108J341D	E 1 J 3 4 1 D	3.4V
RP108J351D	E 1 J 3 5 1 D	3.5V
RP108J361D	E 1 J 3 6 1 D	3.6V
RP108J371D	E 1 J 3 7 1 D	3.7V
RP108J381D	E 1 J 3 8 1 D	3.8V
RP108J391D	E 1 J 3 9 1 D	3.9V
RP108J401D	E 1 J 4 0 1 D	4.0V
RP108J411D	E 1 J 4 1 1 D	4.1V
RP108J421D	E 1 J 4 2 1 D	4.2V
RP108J121D5	E 1 J 1 2 1 D 5	1.25V
RP108J181D5	E 1 J 1 8 1 D 5	1.85V
RP108J281D5	E 1 J 2 8 1 D 5	2.85V

TEST CIRCUITS

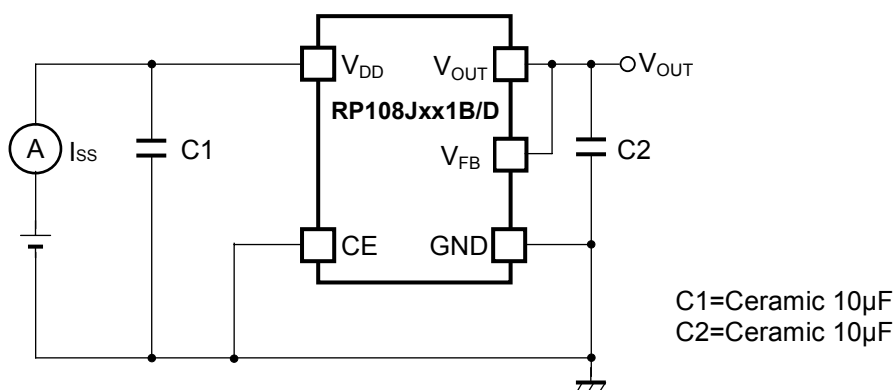
Fixed Output Voltage Type (RP108Jxx1x)



Basic Test Circuit

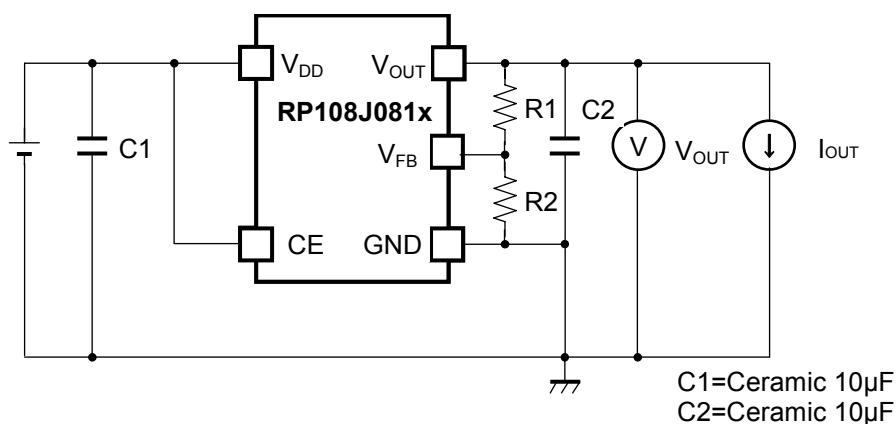


Test Circuit for Supply Current

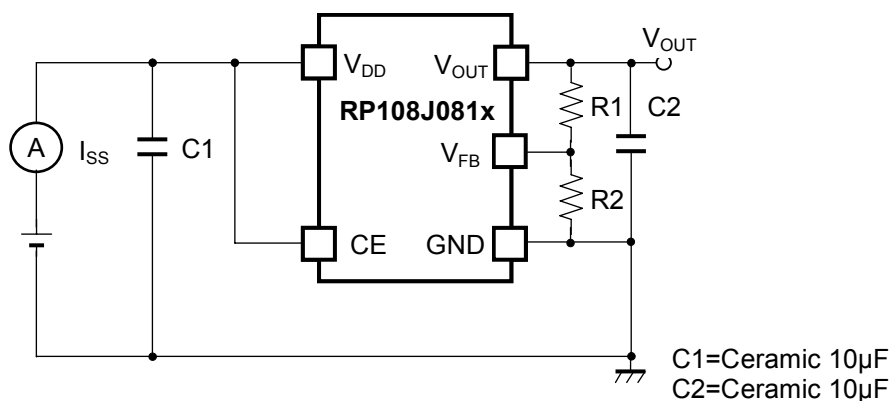


Test Circuit for Standby Current

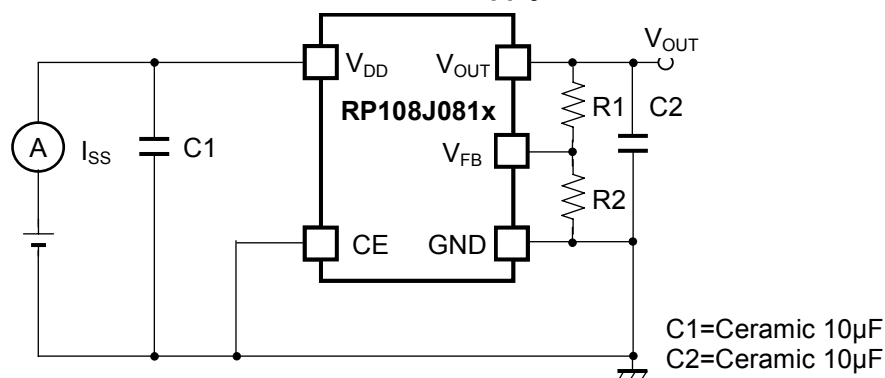
Adjustable Output Voltage Setting Type (RP108J081x)



Basic Test Circuit



Test Circuit for Supply Current



Test Circuit for Standby Current

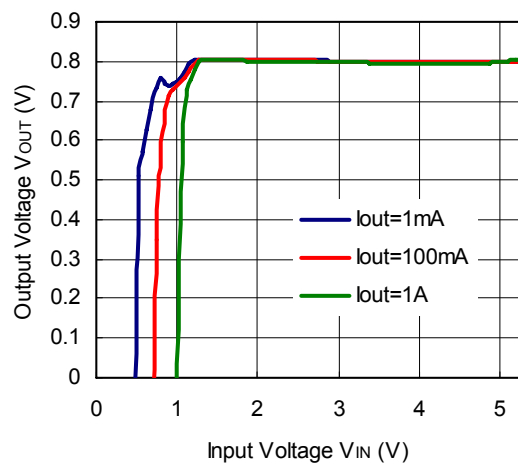
Note : Refer to *Adjustable Output Voltage Type Settings* for R1 and R2.

TYPICAL CHARACTERISTICS

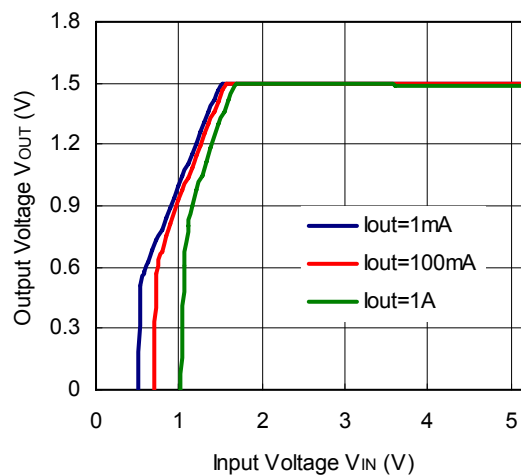
Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

1) Output Voltage vs. Input Voltage ($C_1=C_2$ =Ceramic10 μ F, $T_a=25^\circ\text{C}$)

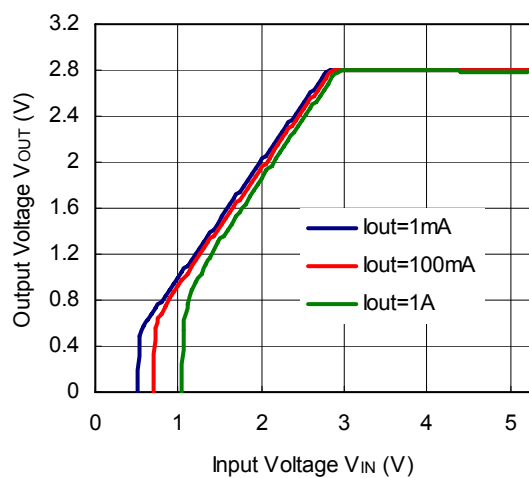
RP108J081x



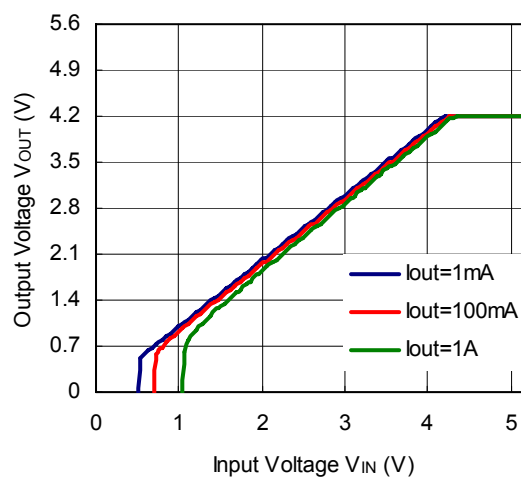
RP108J151x



RP108J281x



RP108J421x

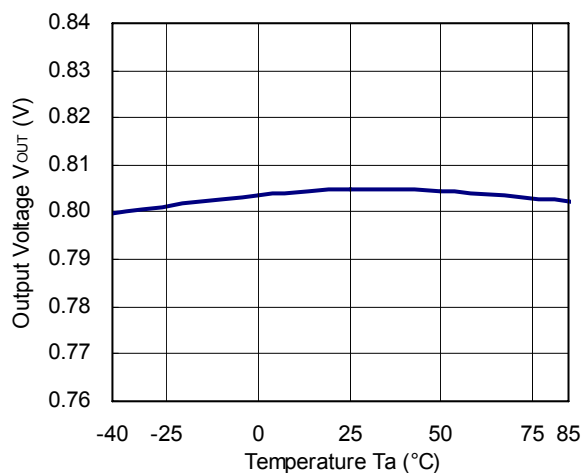


RP108J

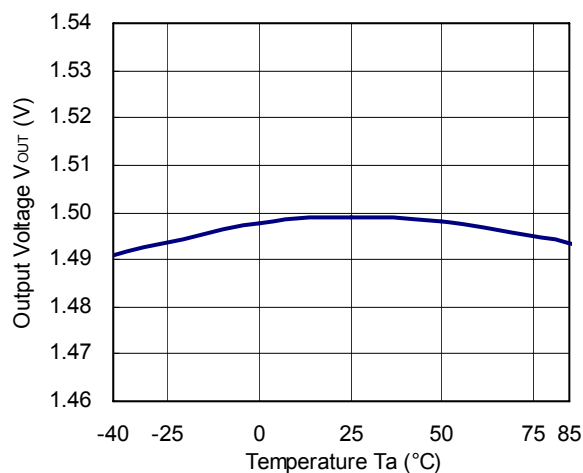
NO.EA-203-140522

2) Output Voltage vs. Temperature (C1=C2=Ceramic10 μ F, $-40^{\circ}\text{C} \leq T_a \leq 85^{\circ}\text{C}$)

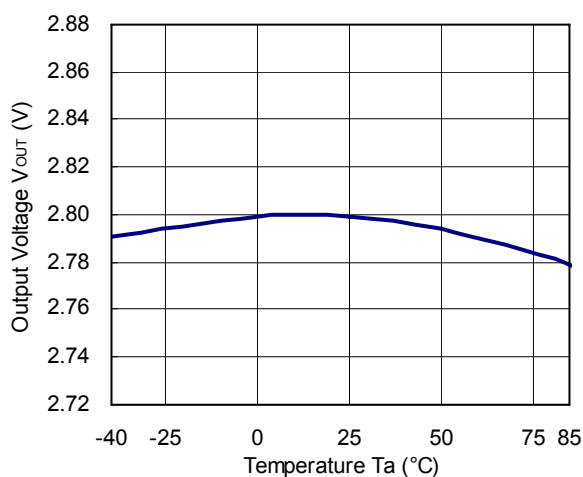
RP108J081x



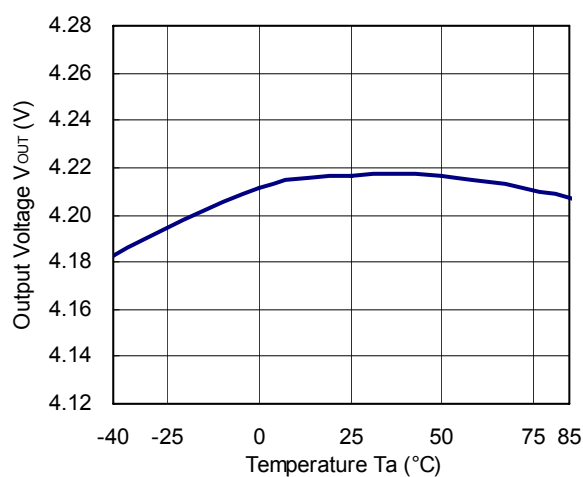
RP108J151x



RP108J281x

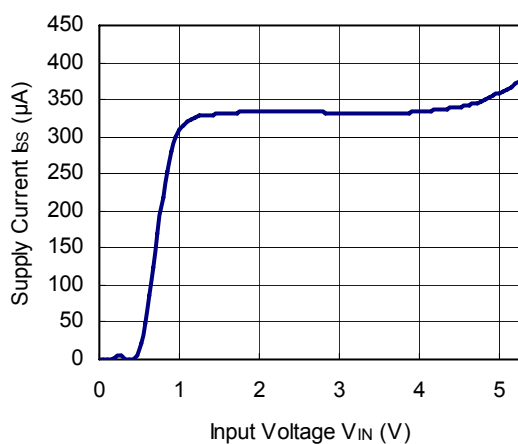


RP108J421x

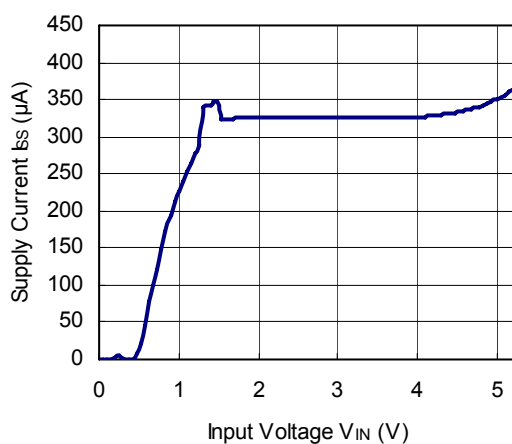


3) Supply Current vs. Input Voltage (C1=C2=Ceramic 10 μ F, $I_{OUT}=0\text{mA}$, $T_a=25^{\circ}\text{C}$)

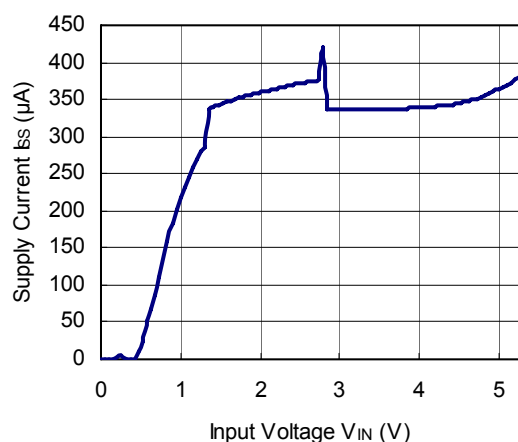
RP108J081x



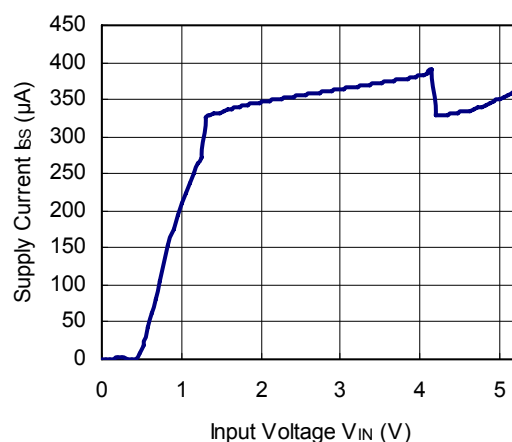
RP108J151x



RP108J281x



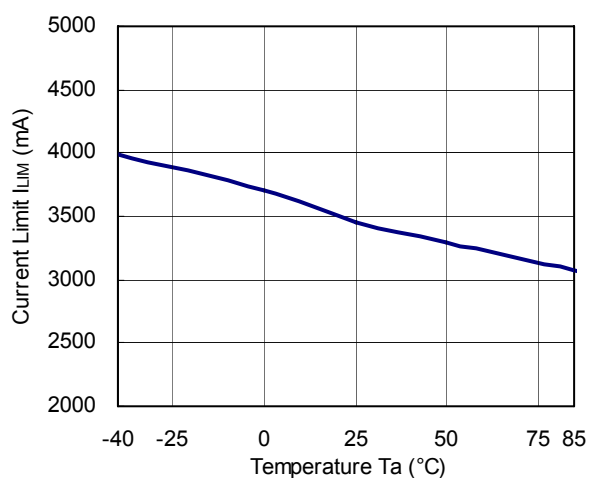
RP108J421x



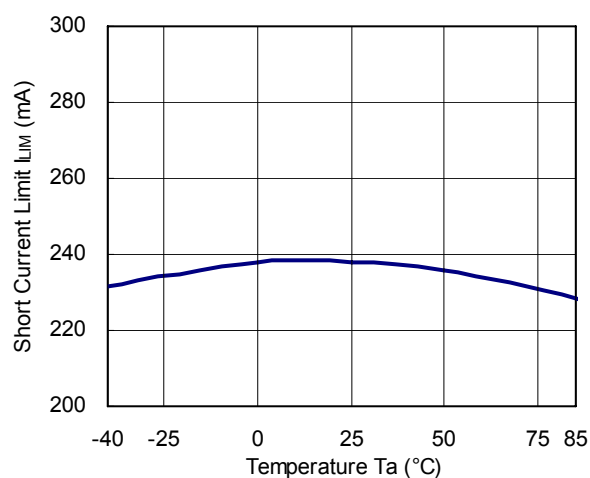
4) Short Current Limit vs. Temperature/ Current Limit vs. Temperature

RP108J includes a Fold-back Protection Circuit. Under conditions during a Fold-back Protection Circuit works, Thermal Shutdown Circuit starts to operate in order to prevent the self-heat generation. Therefore, RP108J isn't able to test "Output voltage vs. Output Current".

RP108J081x



RP108J081x

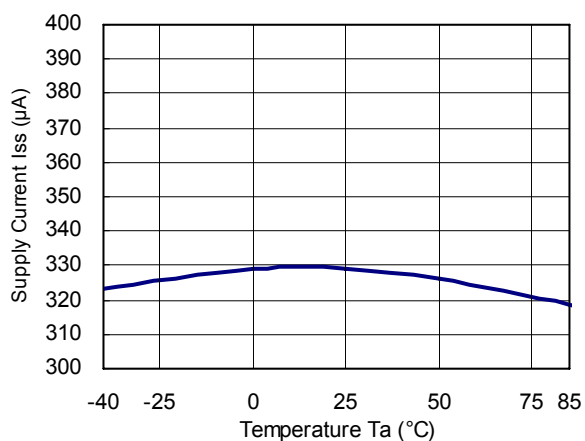


RP108J

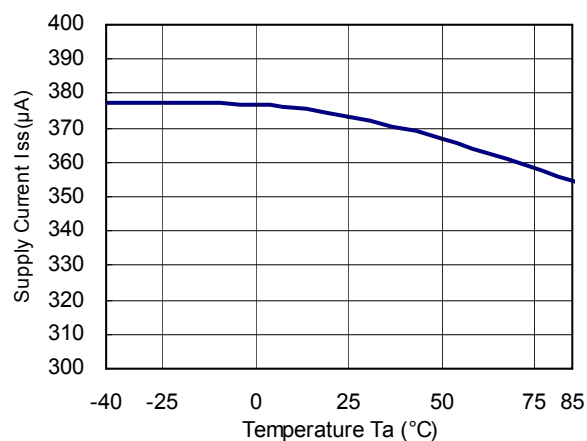
NO.EA-203-140522

5) Supply Current vs. Temperature (C1= C2=Ceramic 10 μ F, I_{OUT}=0mA)

RP108J081x

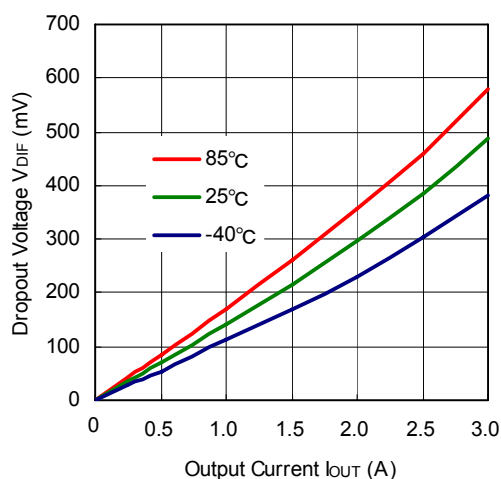


RP108J151x



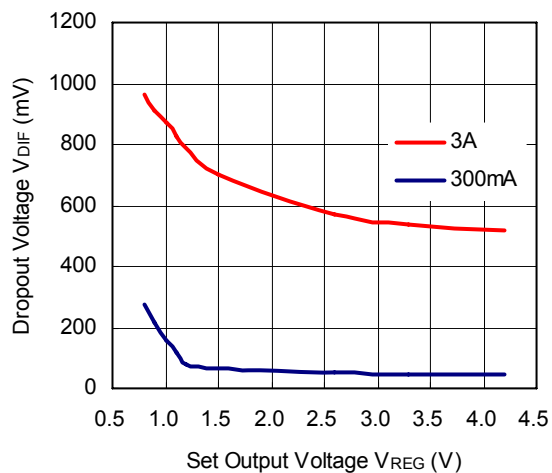
6) Dropout Voltage vs. Output Current (C1=C2=Ceramic 10 μ F)

RP108J251x



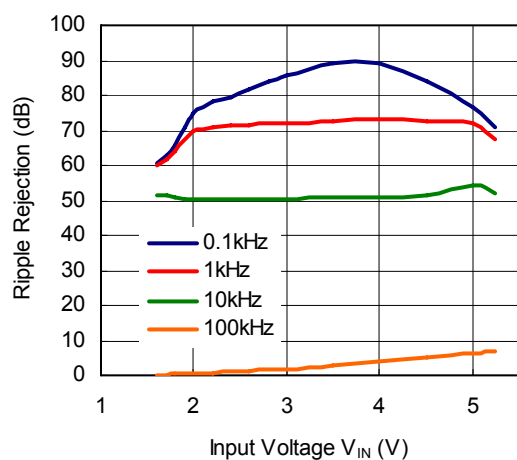
7) Dropout Voltage vs. Set Output Voltage (C1=C2=Ceramic 10 μ F, T_a =25 $^{\circ}$ C)

RP108J251x

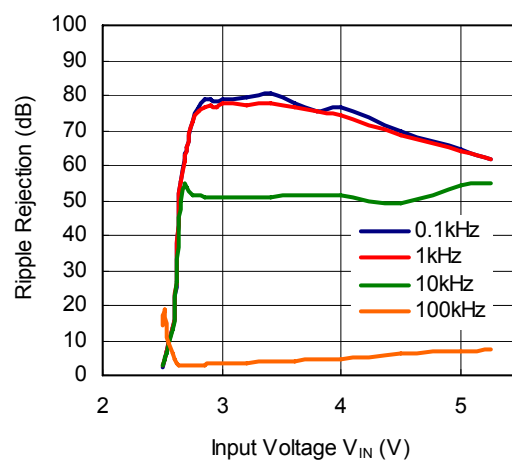


8) Ripple Rejection vs. Input Voltage ($C_1=C_2=10\mu\text{F}$, $\text{Ripple}=0.2\text{Vp-p}$, $I_{\text{OUT}}=100\mu\text{A}$, $T_a=25^\circ\text{C}$)

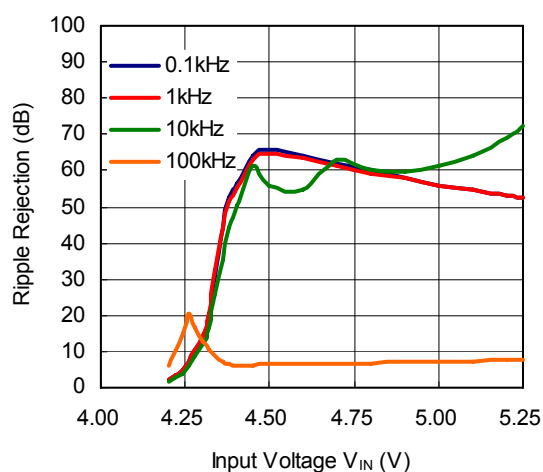
RP108J081x



RP108J251x

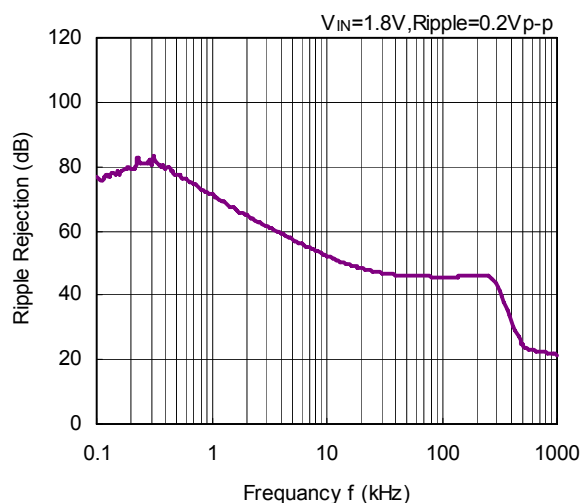


RP108J421x

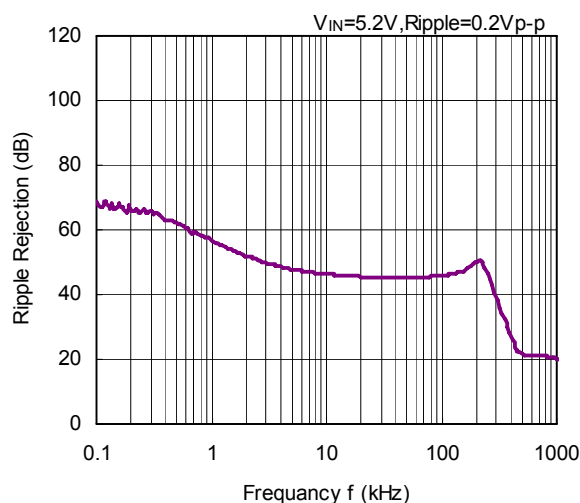


9) Ripple Rejection vs. Frequency ($C_1=\text{none}$, $C_2=10\mu\text{F}$, $I_{\text{OUT}}=100\text{mA}$, $T_a=25^\circ\text{C}$)

RP108J081x

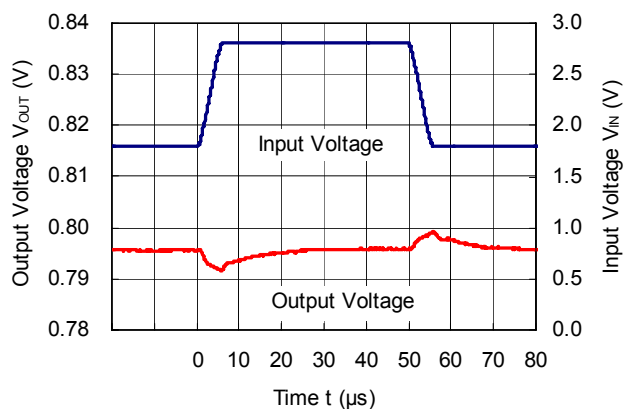


RP108J421x

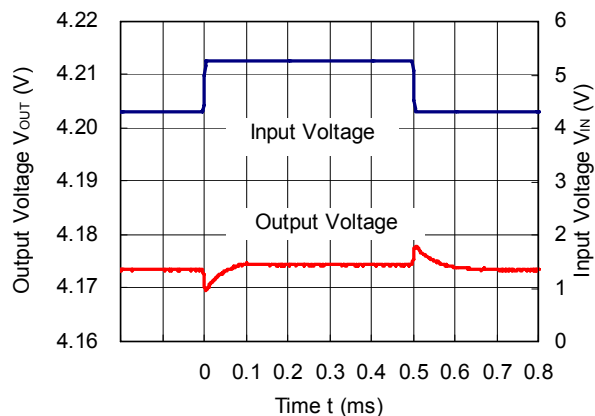


10) Input Transient Response (C_1 =none, $C_2=10\mu\text{F}$, $I_{\text{OUT}}=30\text{mA}$, $t_r=t_f=5\mu\text{s}$, $T_a=25^\circ\text{C}$)

RP108J081x

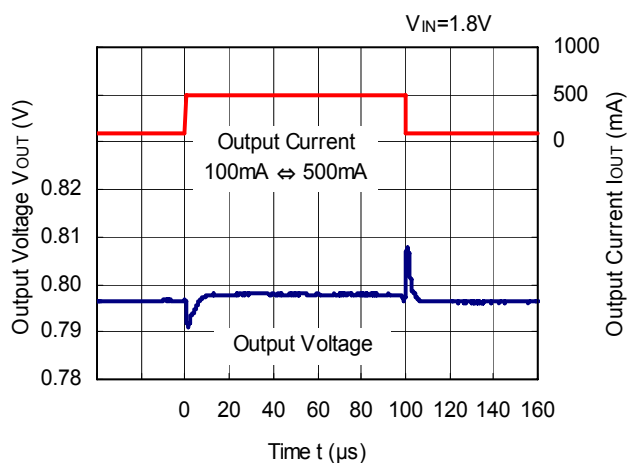


RP108J421x

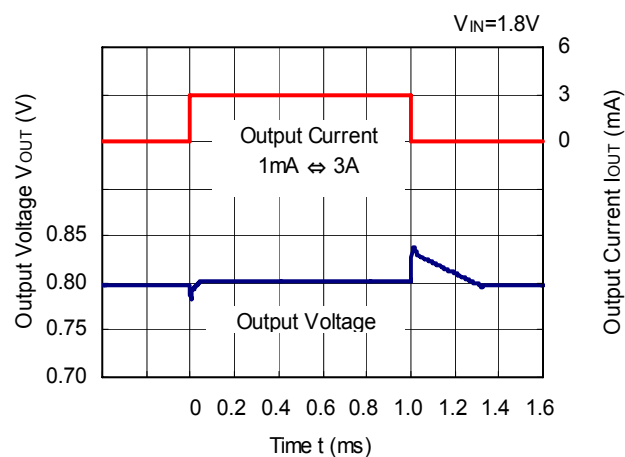


11) Load Transient Response ($C_1=C_2=10\mu\text{F}$, $t_r=t_f=0.5\mu\text{s}$, $T_a=25^\circ\text{C}$)

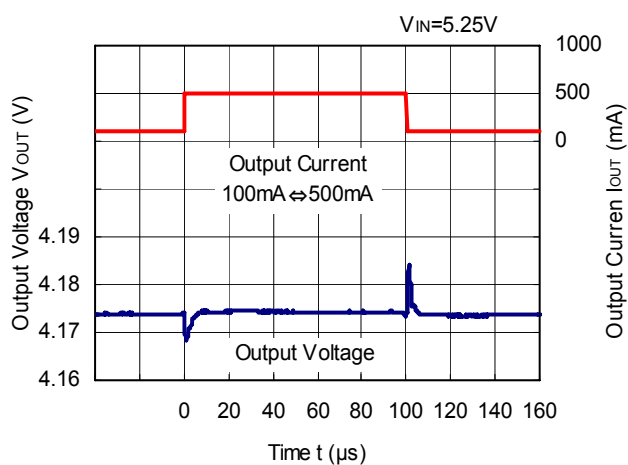
RP108J081x



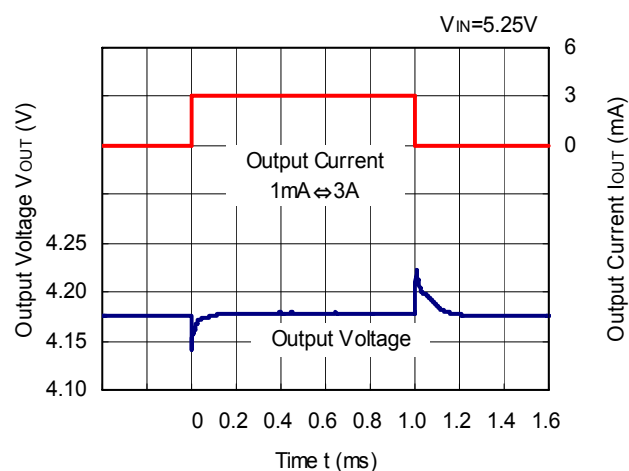
RP108J081x



RP108J421x

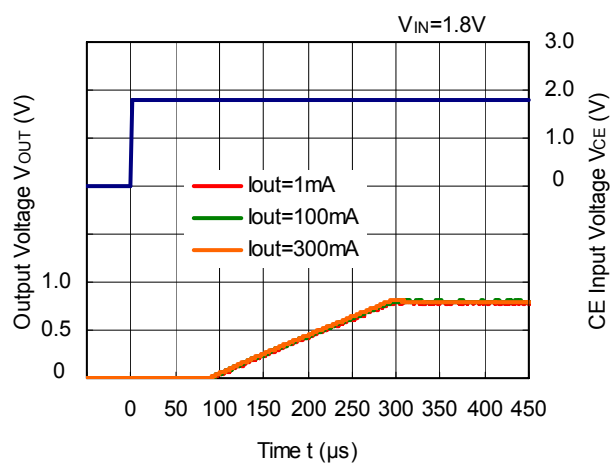


RP108J421x

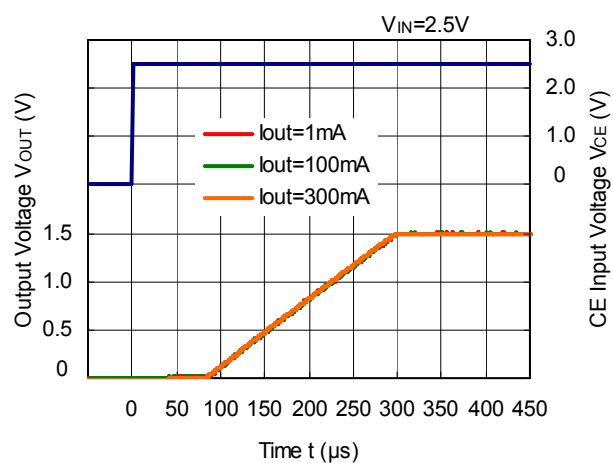


12) Turn on Speed with CE pin ($C_1=C_2$ =Ceramic $10\mu\text{F}$, $T_a=25^\circ\text{C}$)

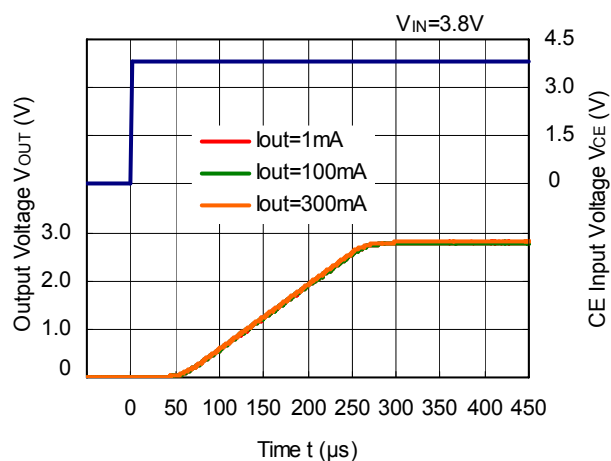
RP108J081x



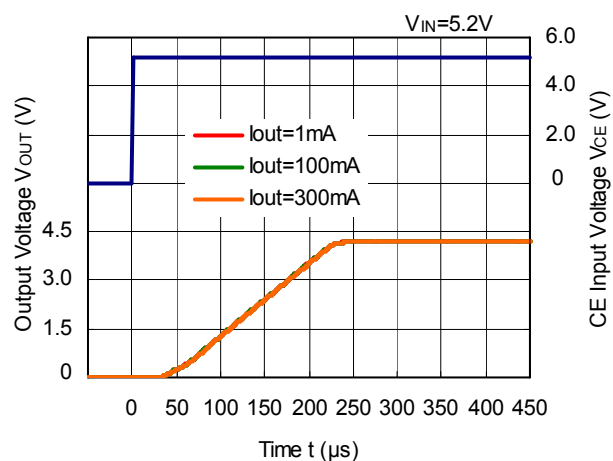
RP108J151x



RP108J281x

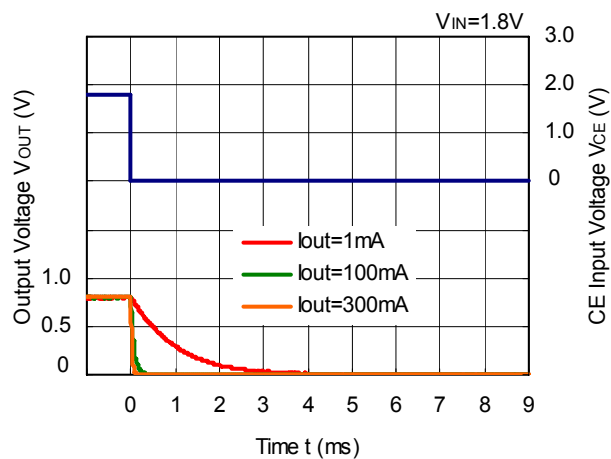


RP108J421x

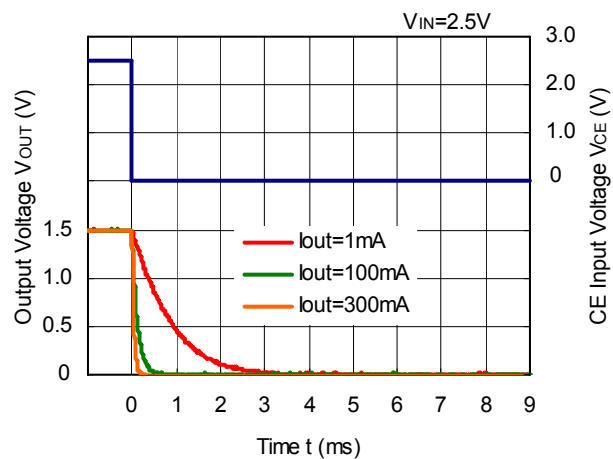


13) Turn off Speed with CE pin ($C_1=C_2$ =Ceramic $10\mu\text{F}$, $T_a=25^\circ\text{C}$)

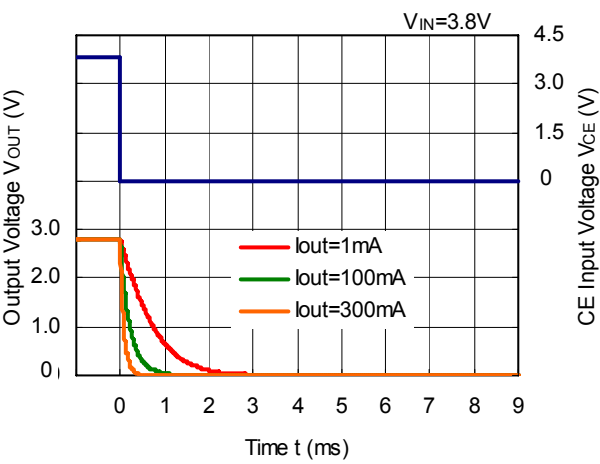
RP108J081x



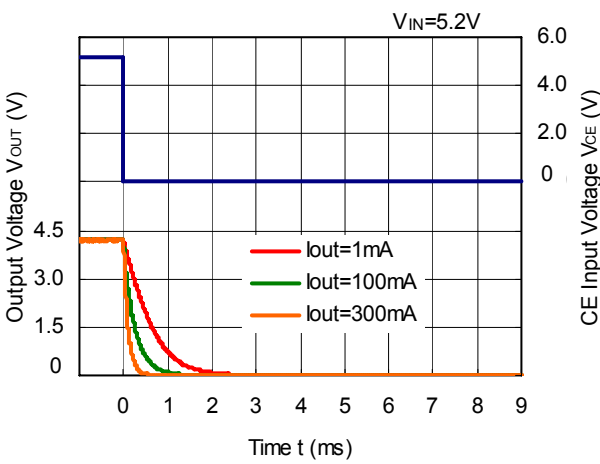
RP108J151x



RP108J281x

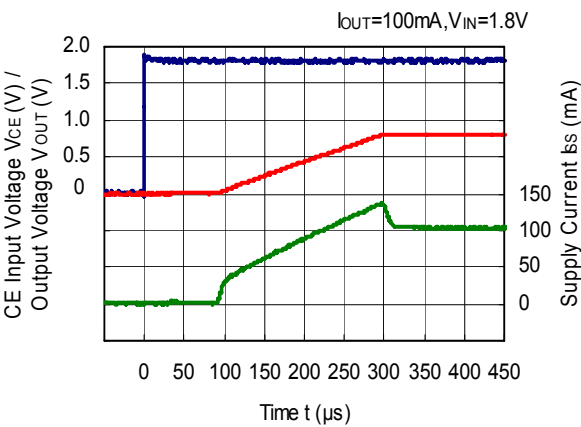


RP108J421x

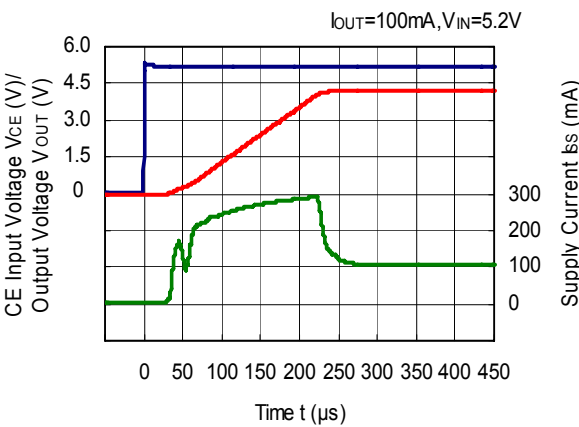


14) Inrush Current

RP108J081x



RP108J421x



ESR vs. Output Current

When using the IC, consider the following points: The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below. The conditions when the white noise level is under $40\mu\text{V}$ (Avg.) are marked as the hatched area in the graph.

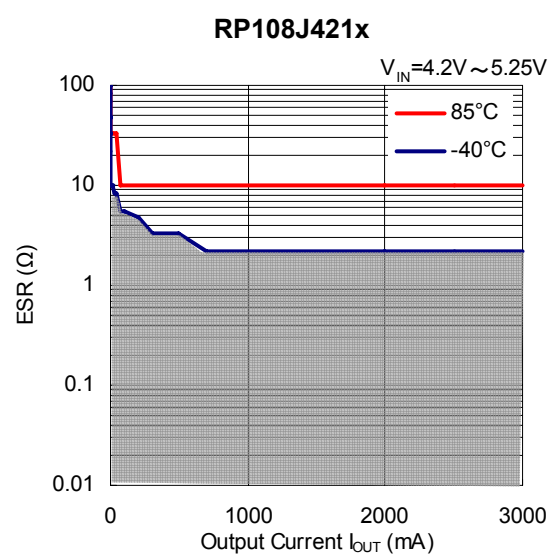
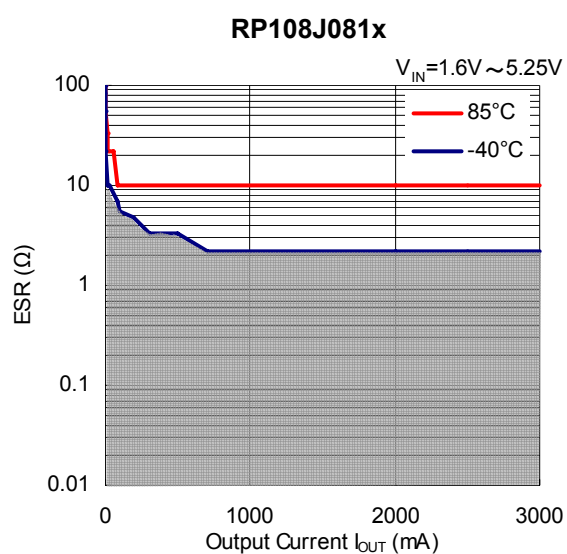
Measurement Conditions

Frequency Band : 10Hz to 2MHz

Temperature : -40°C to 85°C

Hatched area : Noise level is under $40\mu\text{V}$

C1, C2 : $10.0\mu\text{F}$ or more





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For the conservation of the global environment, Ricoh is advancing the decrease of the negative environmental impact material.
After Apr. 1, 2006, we will ship out the lead free products only. Thus, all products that will be shipped from now on comply with RoHS Directive.
Basically after Apr. 1, 2012, we will ship out the Power Management ICs of the Halogen Free products only. (RicoH Halogen Free products are also Antimony Free.)

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RICOH COMPANY, LTD.

Electronic Devices Company

● Higashi-Shinagawa Office (International Sales)

3-32-3, Higashi-Shinagawa, Shinagawa-ku, Tokyo 140-8655, Japan
Phone: +81-3-5479-2857 Fax: +81-3-5479-0502

RICOH EUROPE (NETHERLANDS) B.V.

● Semiconductor Support Centre

"Nieuw Kronenburg" Prof. W.H. Keesomlaan 1, 1183 DJ, Amstelveen, The Netherlands
P.O.Box 114, 1180 AC Amstelveen
Phone: +31-20-5474-309 Fax: +31-20-5474-791

RICOH ELECTRONIC DEVICES KOREA Co., Ltd.

11 floor, Haesung 1 building, 942, Daechidong, Gangnamgu, Seoul, Korea
Phone: +82-2-2135-5700 Fax: +82-2-2135-5705

RICOH ELECTRONIC DEVICES SHANGHAI Co., Ltd.

Room403, No.2 Building, 690#Bi Bo Road, Pu Dong New district, Shanghai 201203,
People's Republic of China
Phone: +86-21-5027-3200 Fax: +86-21-5027-3299

RICOH COMPANY, LTD.

Electronic Devices Company

● Taipei office

Room109, 10F-1, No.51, Hengyang Rd., Taipei City, Taiwan (R.O.C.)
Phone: +886-2-2313-1621/1622 Fax: +886-2-2313-1623