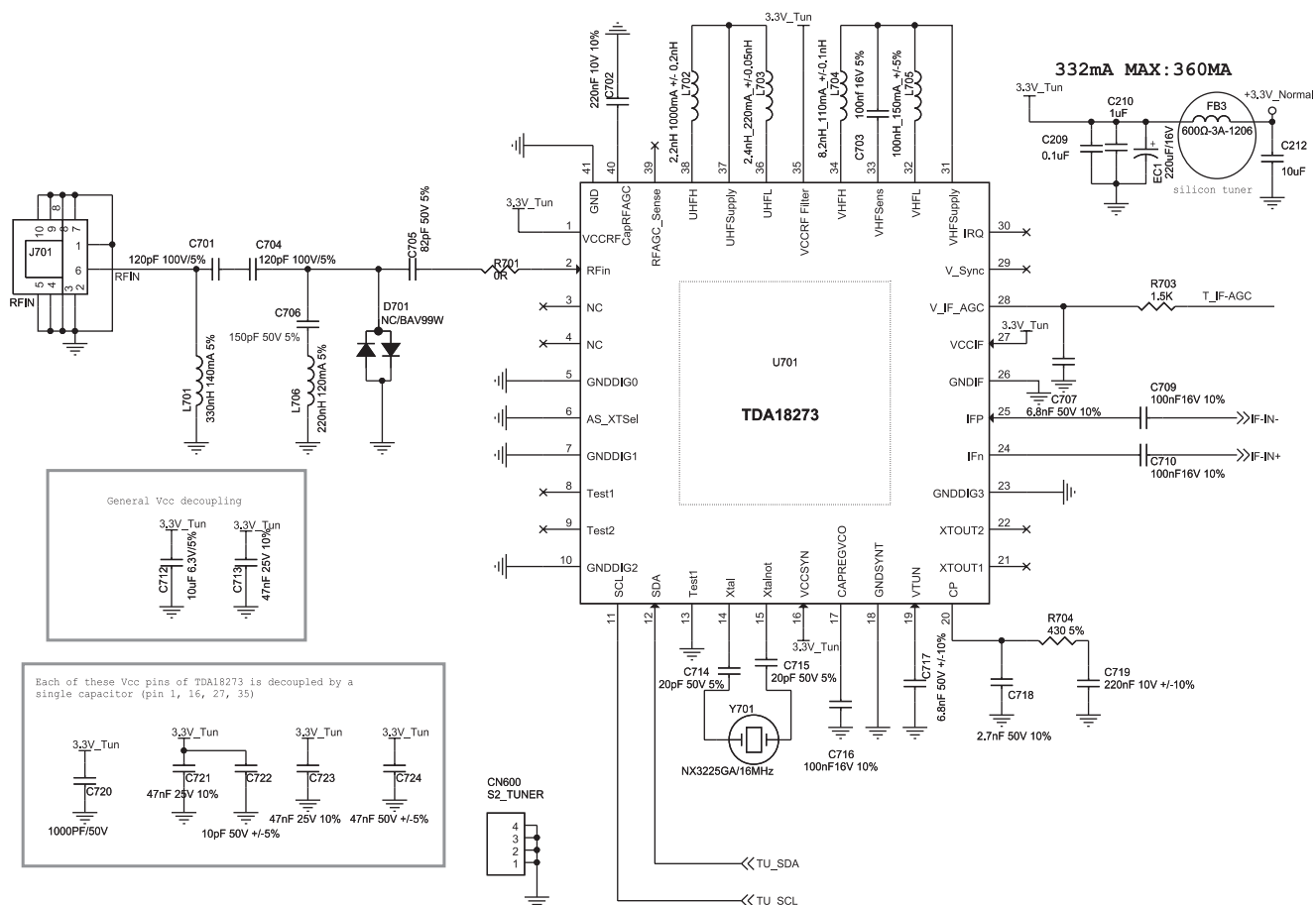
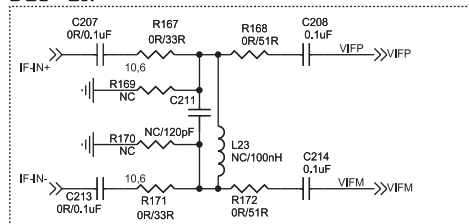


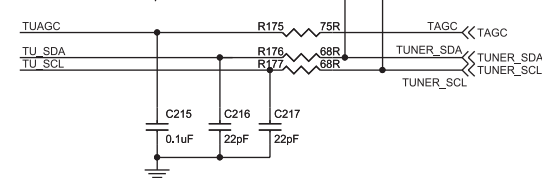
Принципиальная электрическая схема ЖК телевизоров с LED-подсветкой «Haier LE39M600SF/LE46M600SF/LE50M600SF»



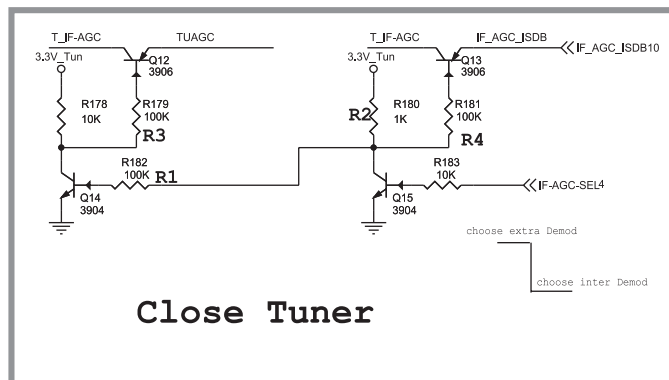
DIF IN

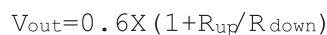
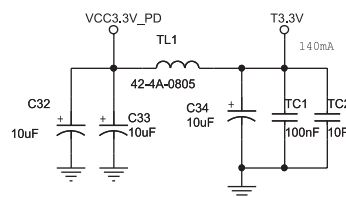


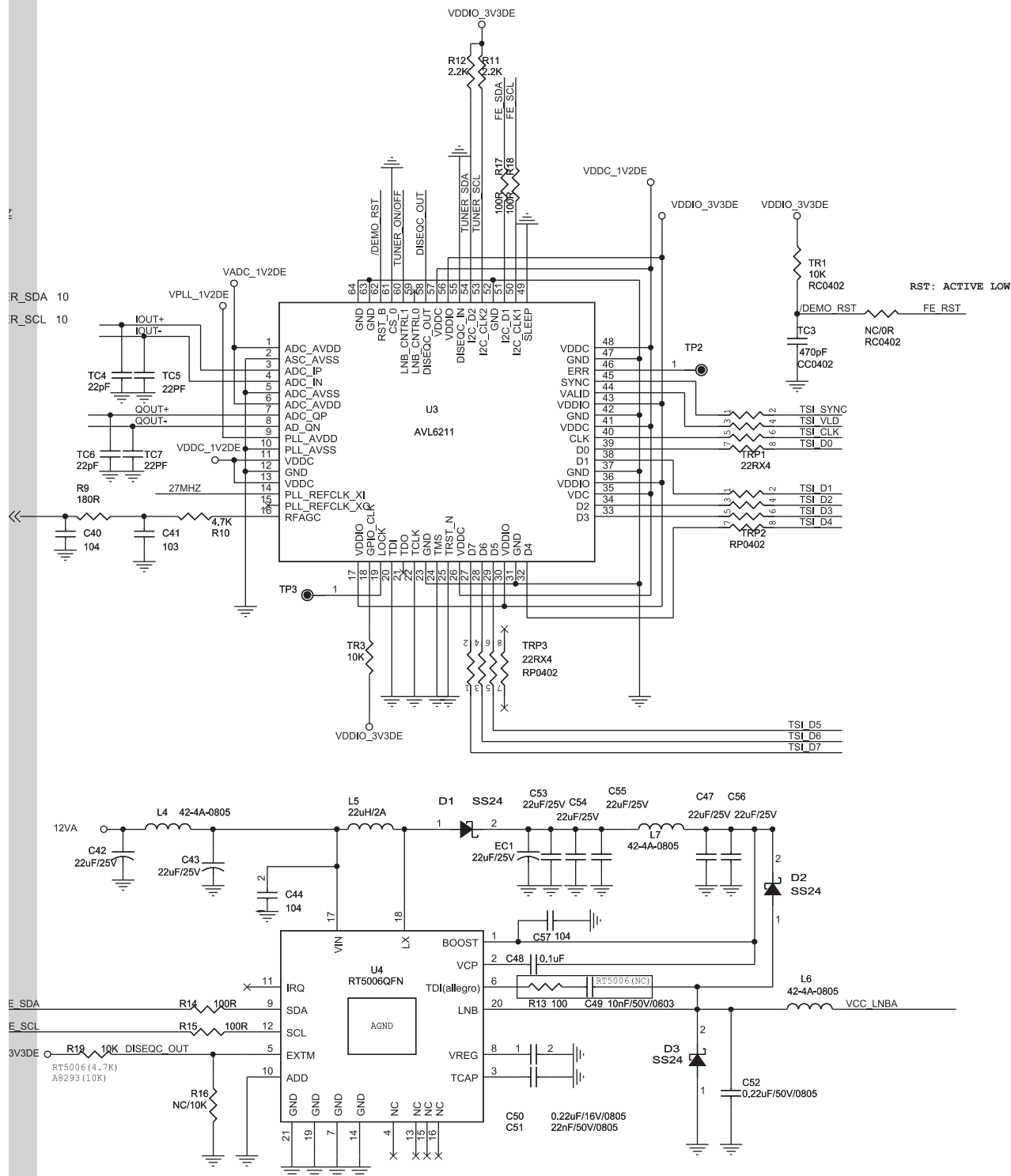
TUNER AGC, Close TUNER



IF AGC Switch

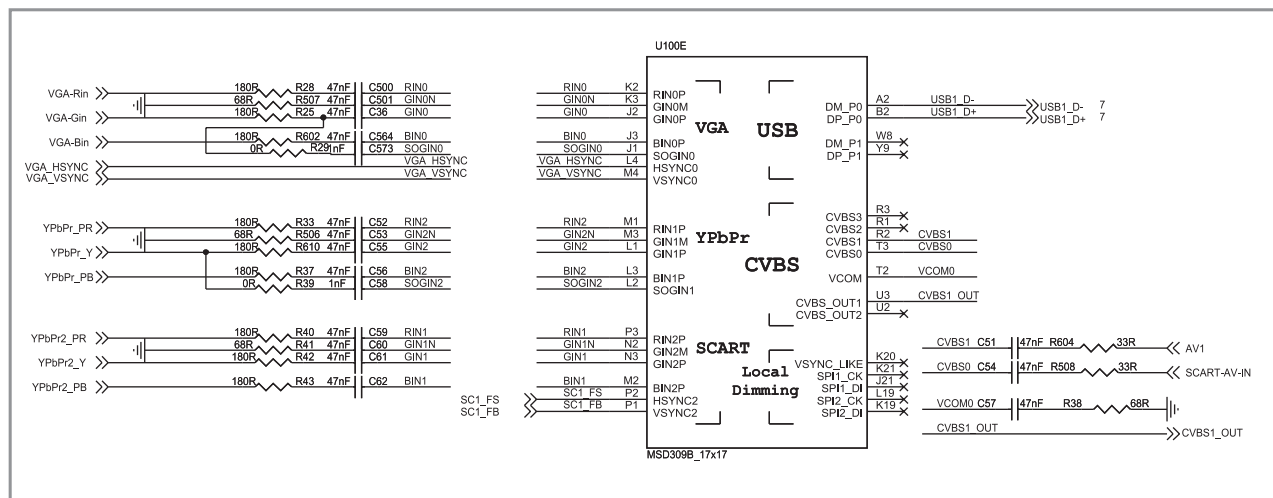




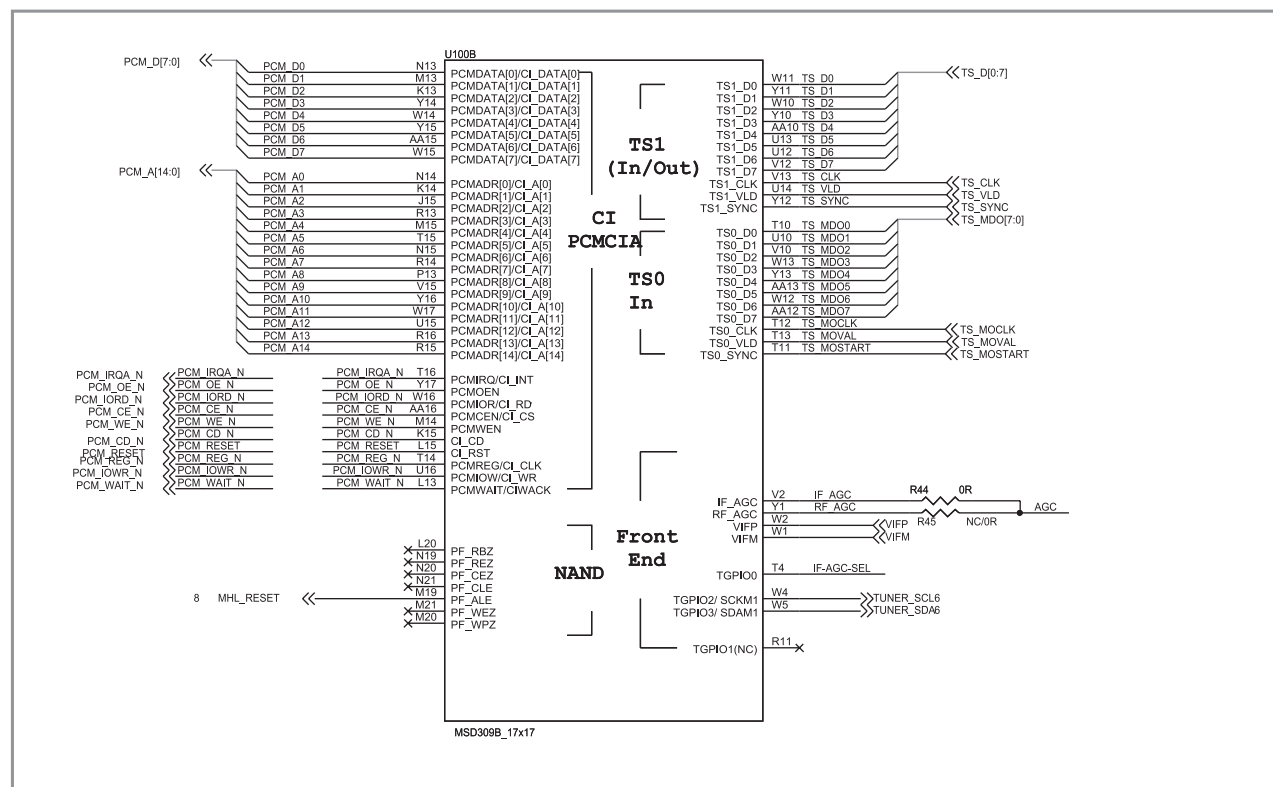


Фрагменты схемы телевизоров, связанные с тюнером на ИМС AV2012 и AVL6211

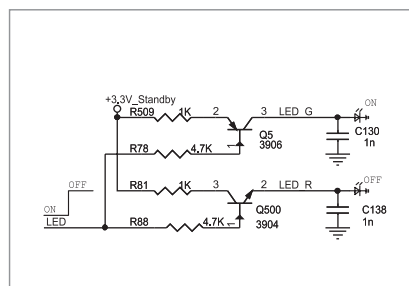
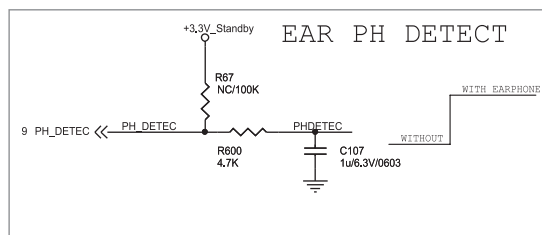
RGB & CVBS & USB



CI & TS & Front End

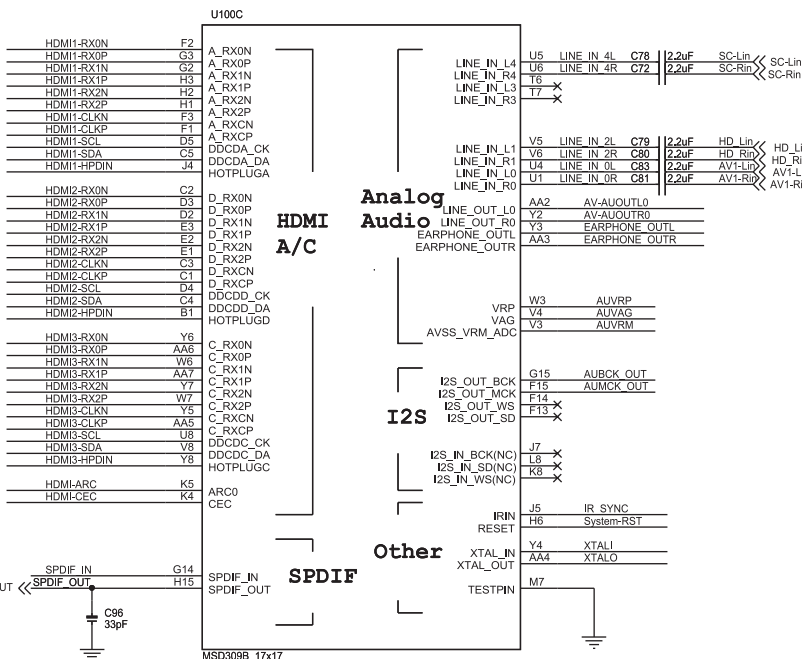
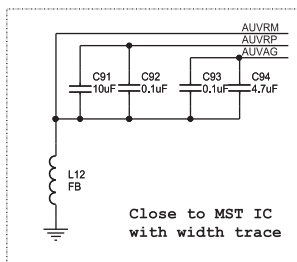
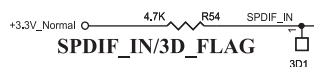
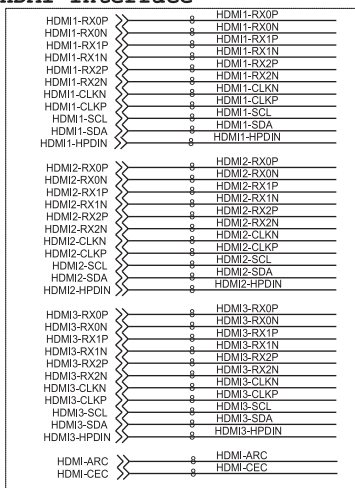


LED

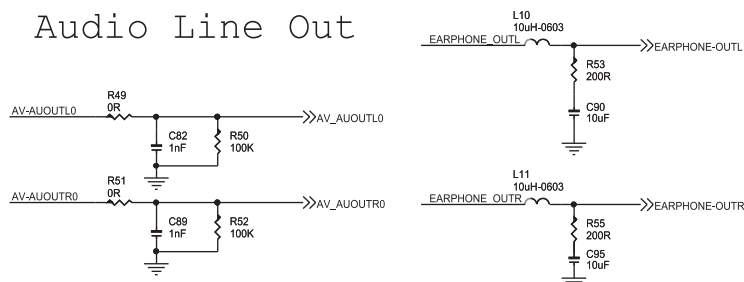


HDMI & Audio

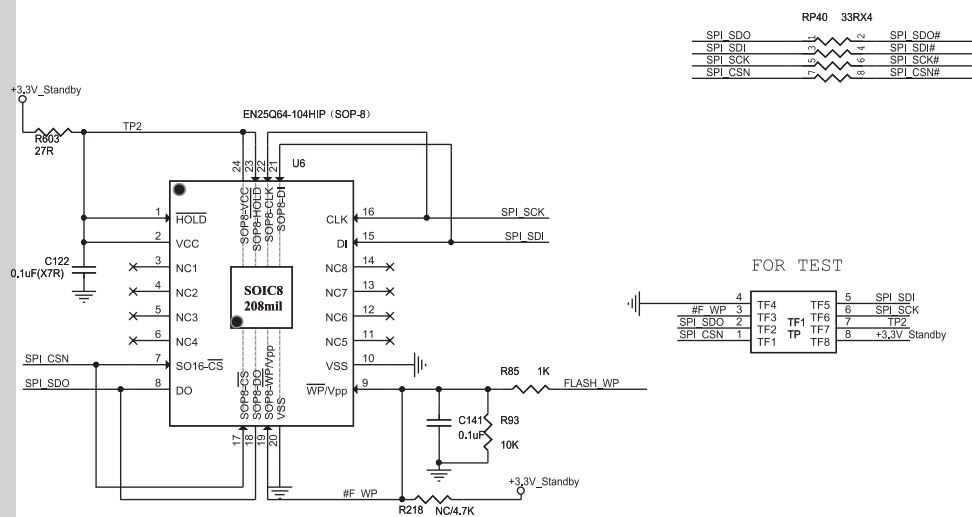
HDMI Interface



Audio Line Out

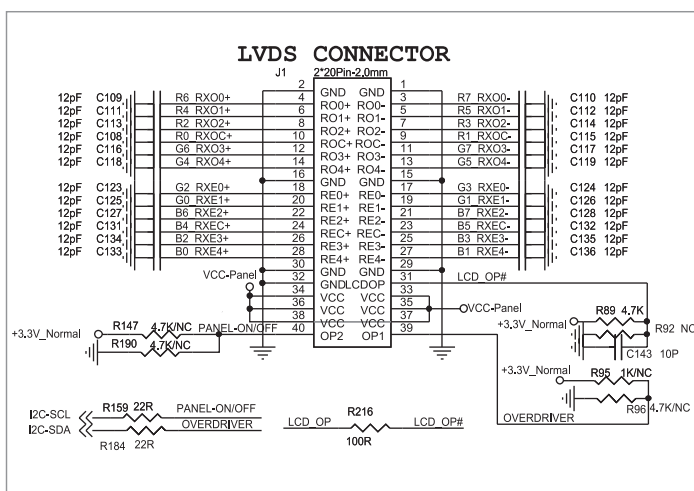
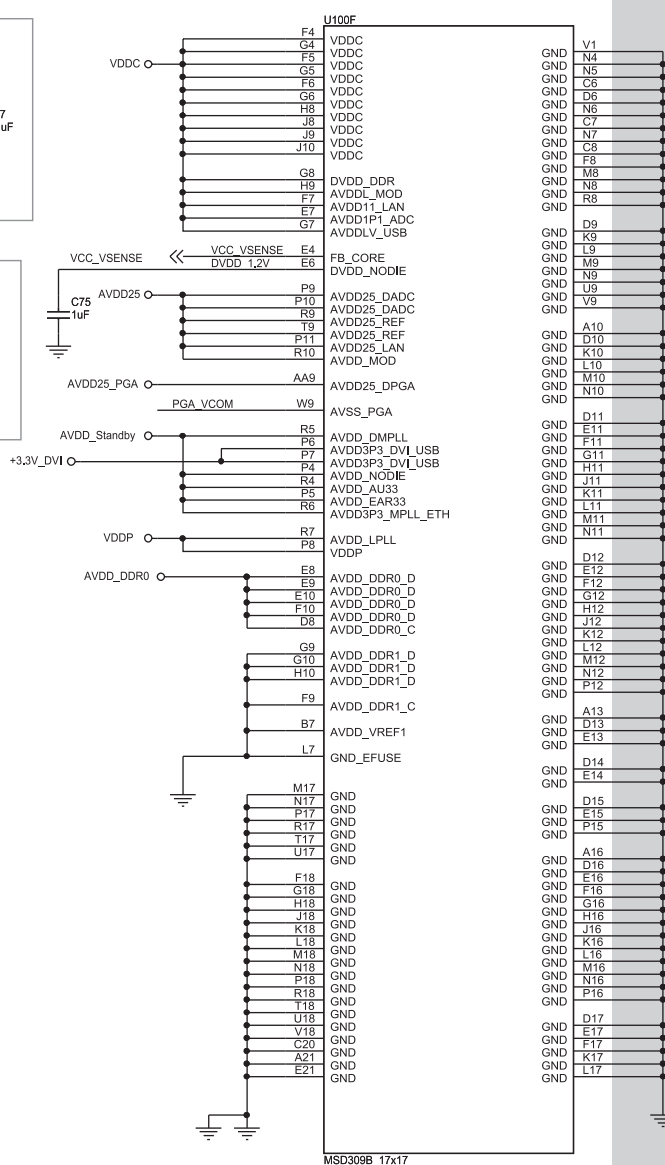
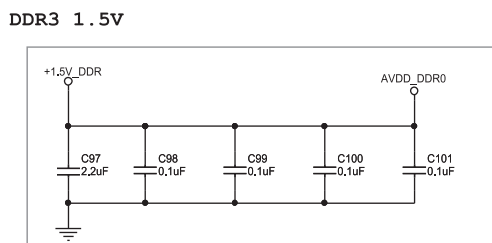
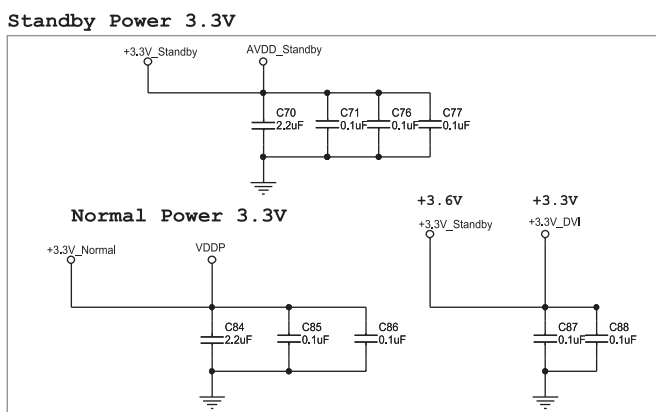
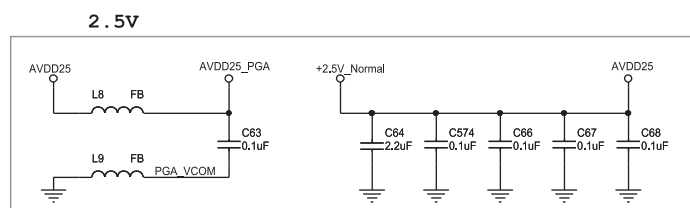
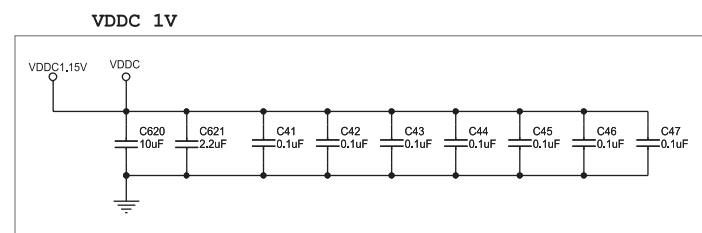


FLASH

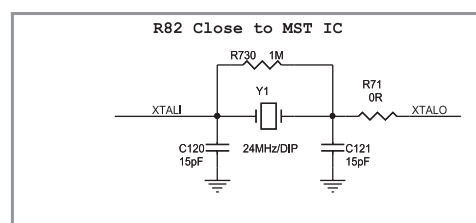


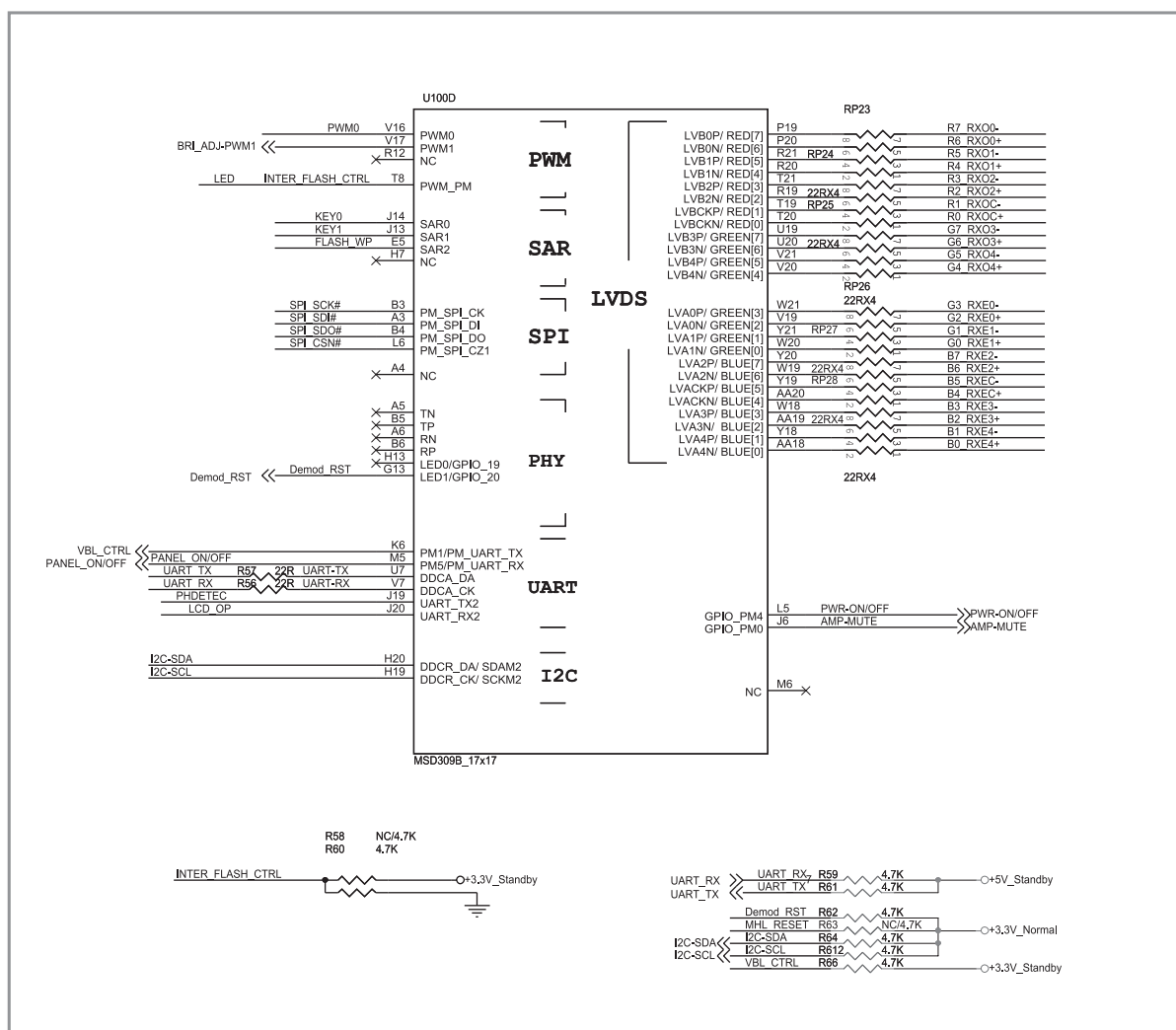
Фрагменты схемы телевизоров, демонстрирующие подключение различных сигналов и питающих напряжений к выводам процессора (1/2)

Power interface

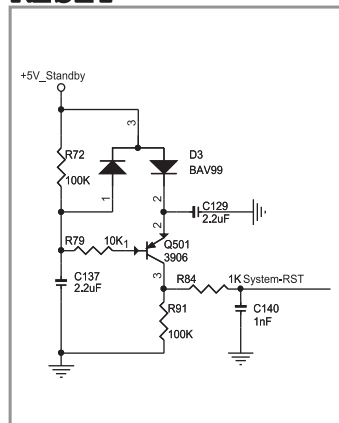


System XTAL

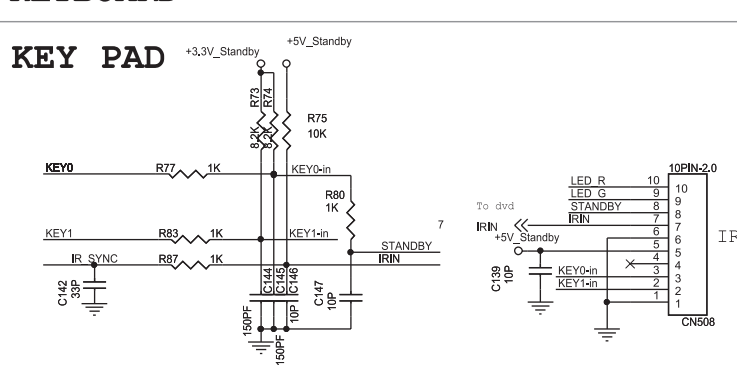


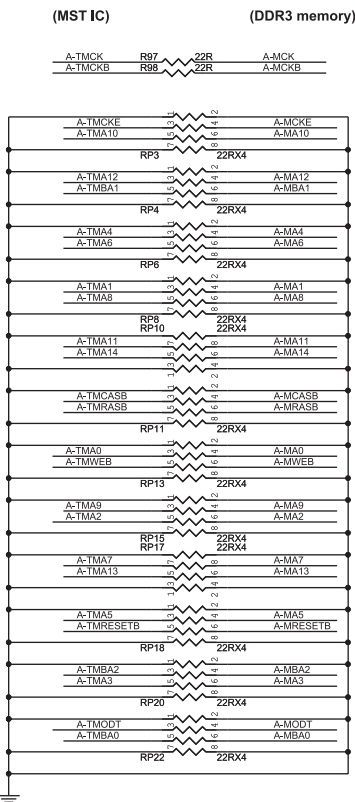
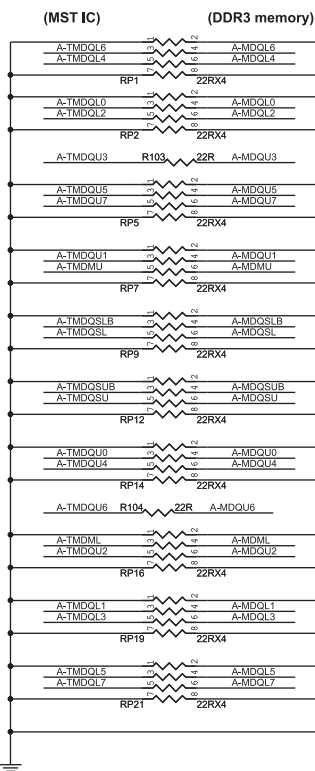
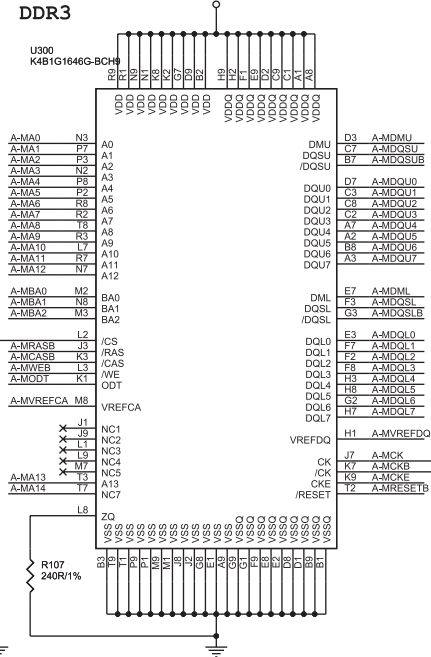
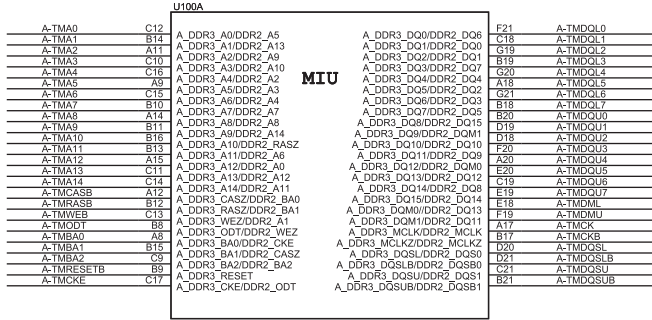


RESET

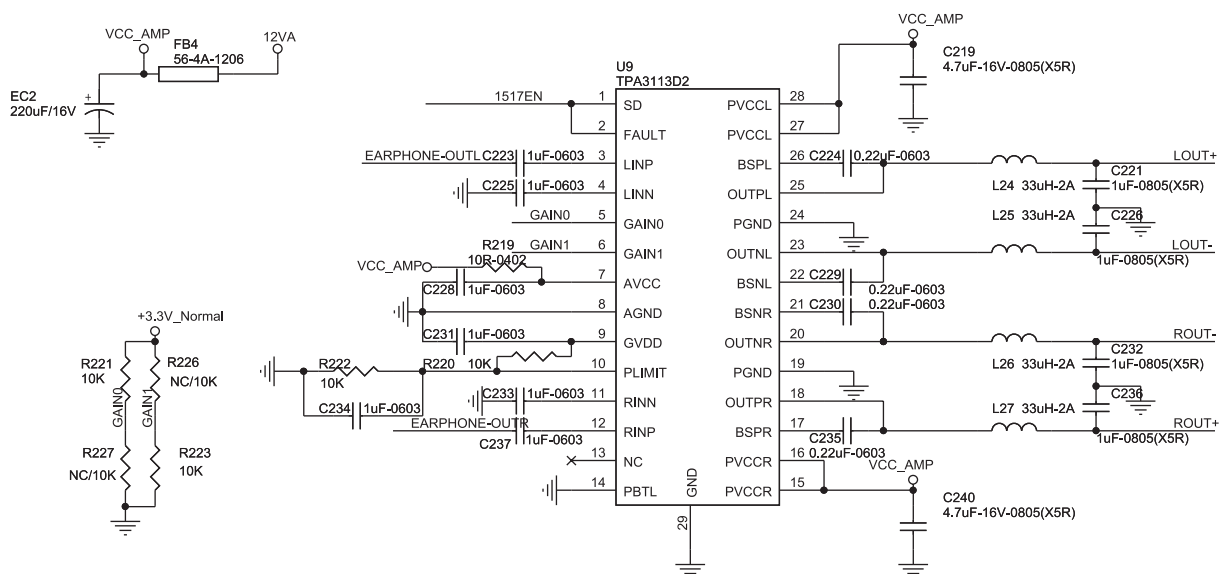


KEYBOARD

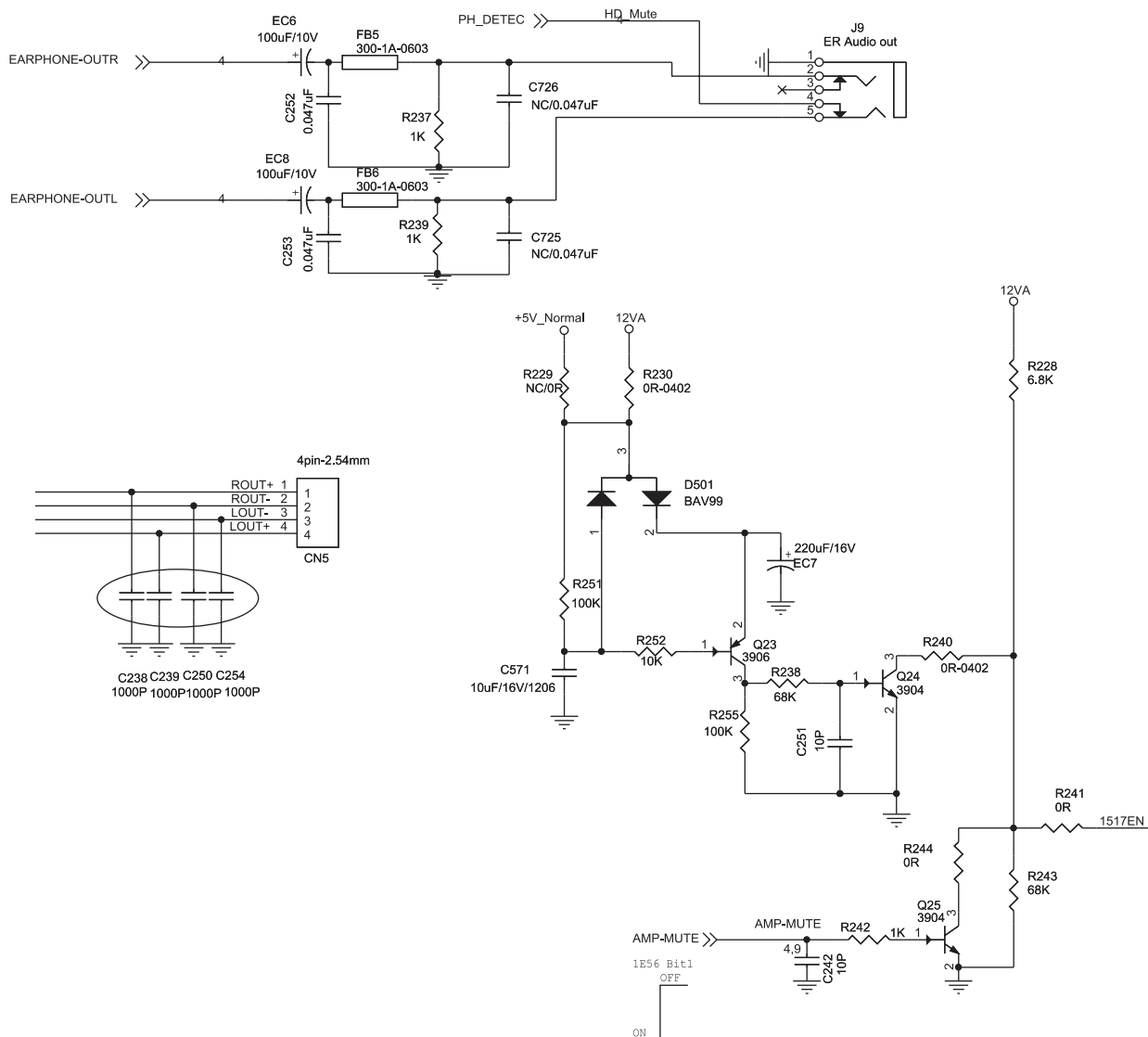


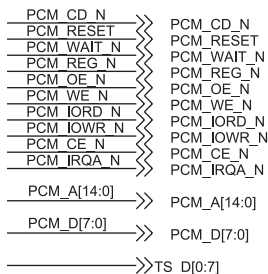


Принципиальная схема подключения сигналов и напряжения питания к памяти



EARPHONE OUT



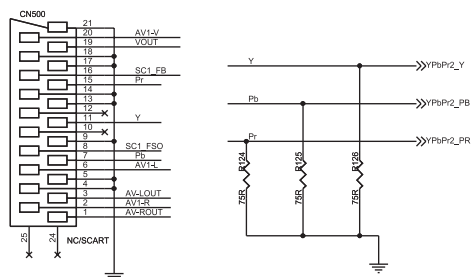


The schematic diagram shows the internal circuitry of the PCM CD N pin. It is connected to a +5V_Normal supply through a network of resistors (R245, R246, R248, R249) and capacitors (C243). The pin is also connected to ground through a 1nF capacitor (C243). The table to the right shows the pin's behavior for different PCM_CD1_N, PCM_CD2_N, and PCM_CD_N inputs.

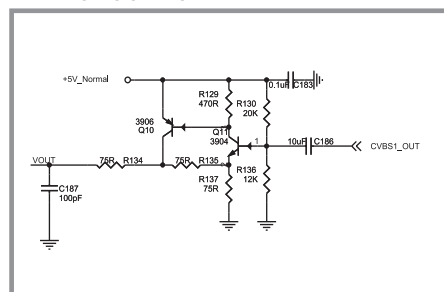
PCM_CD1_N	PCM_CD2_N	PCM_CD_N
GND	GND	L 0V
GND	NC	H 2.5V
NC	GND	H 2.5V
NC	NC	H 5V

Pinout diagram for the J13 connector of the DIP-20(2*10) module. The diagram shows a 20-pin DIP package with pins numbered 1 to 20. Pins 1-8 are labeled TS D1 through TS CLK. Pins 9-16 are labeled TS VLD, TS SYNC, and TUNER SCL. Pins 17-20 are labeled Demod_RST, TUNER SDA, IF-IN+, and IF-IN-. Power pins include +5V_Normal, +3.3V_Normal, and 12VA. Ground connections are shown at pins 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20.

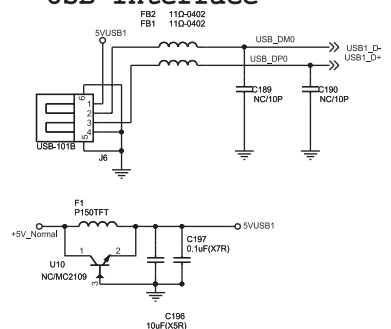
TS_SYNC:38KHz



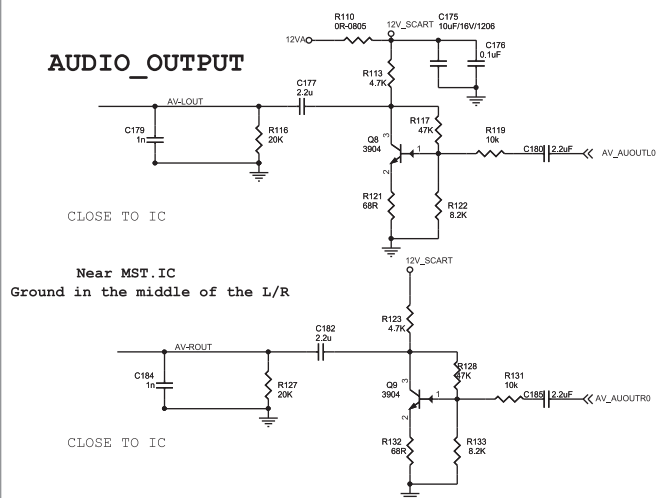
VIDEO OUT OP



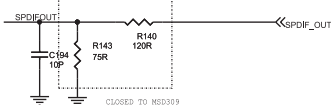
USB Interface



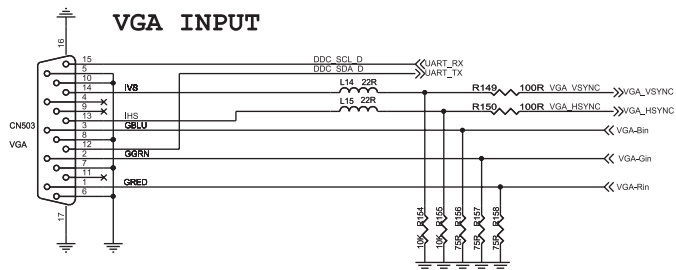
AUDIO OUTPUT



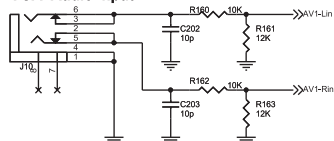
SPDIF Output



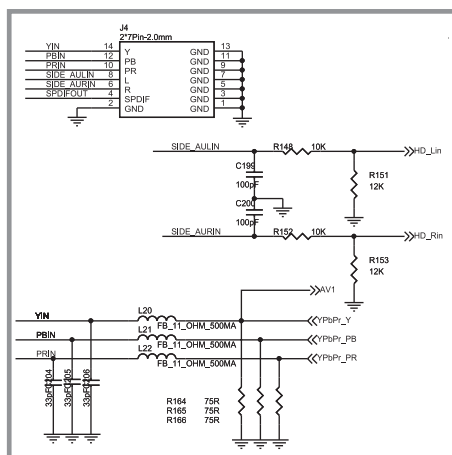
VGA INPUT



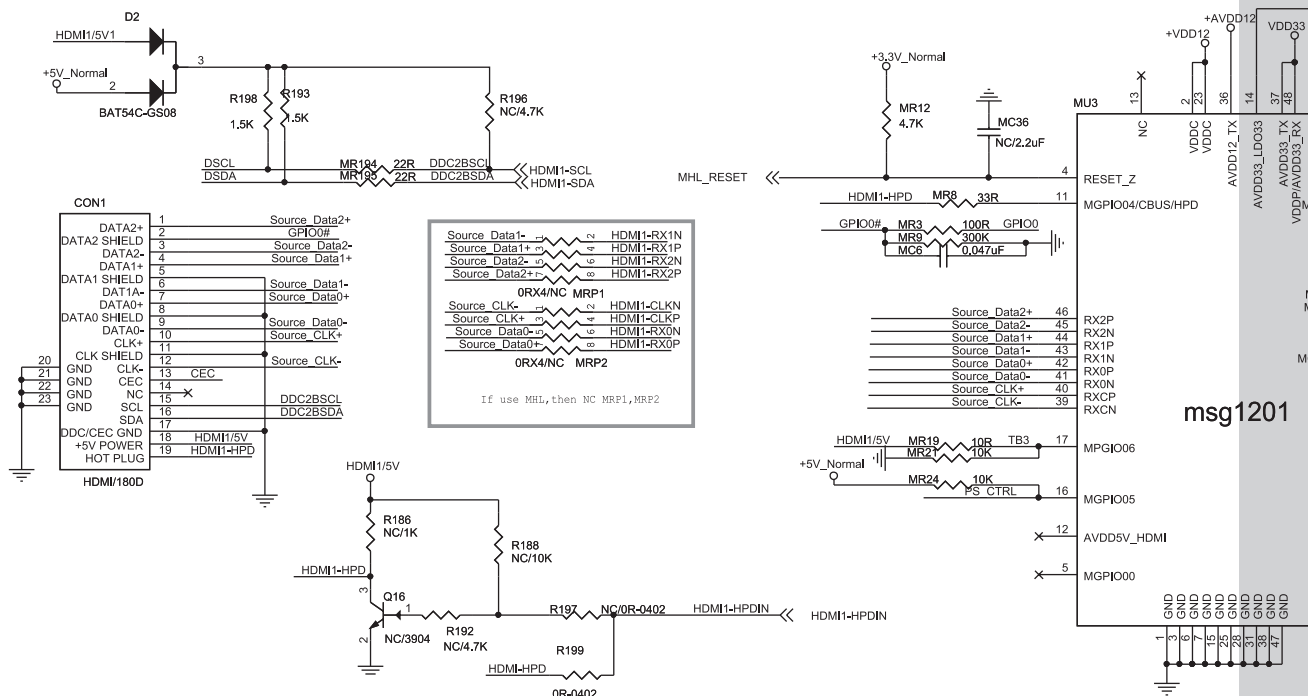
VGA Audio Input



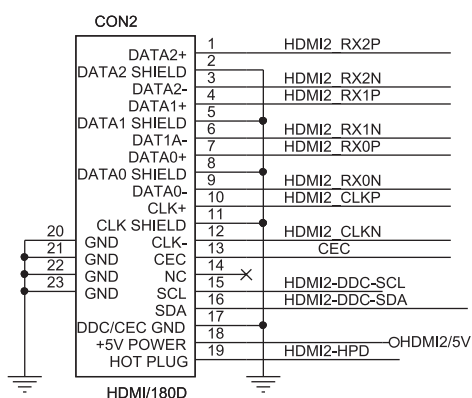
AV1/YPbPr



HDMI 2 / MHL

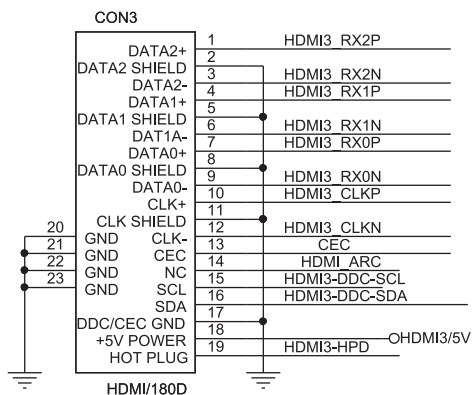


HDMI 3

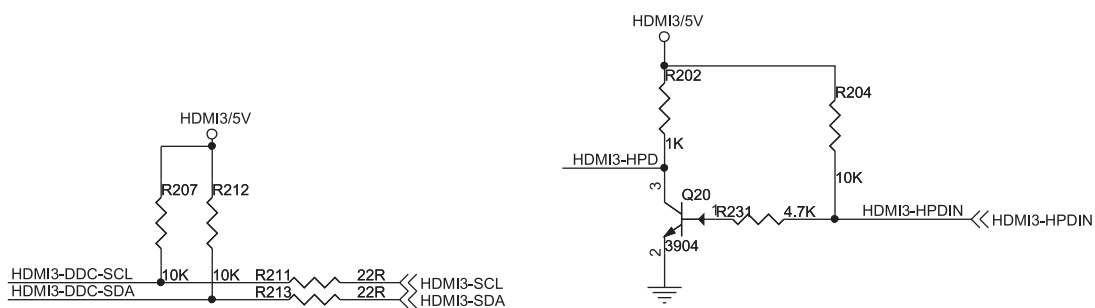
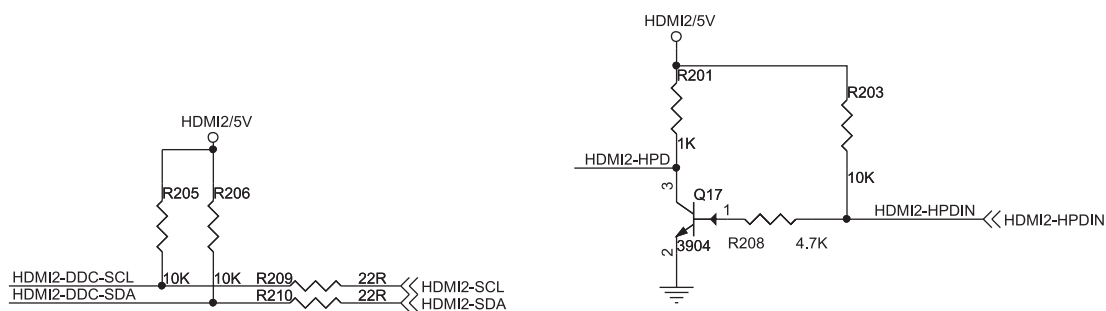
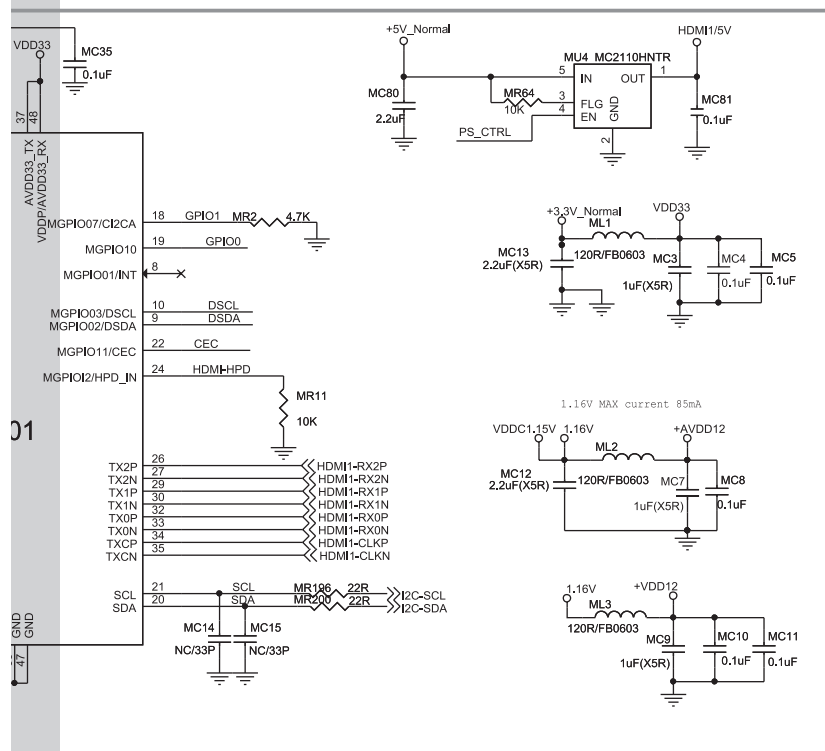


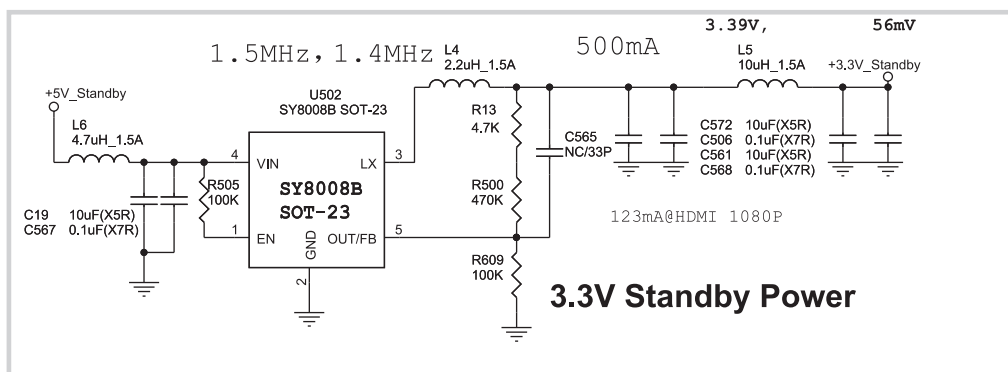
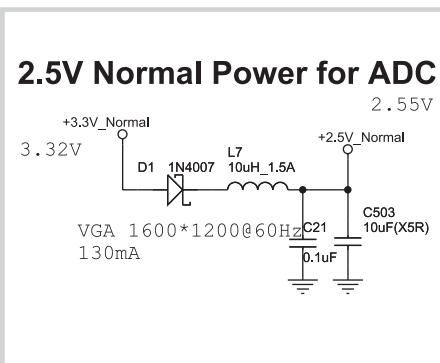
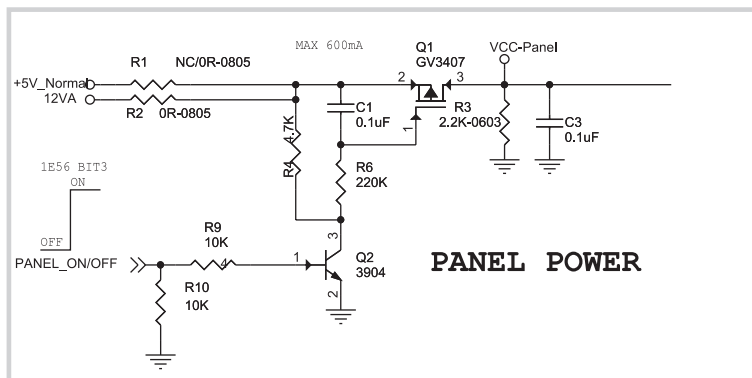
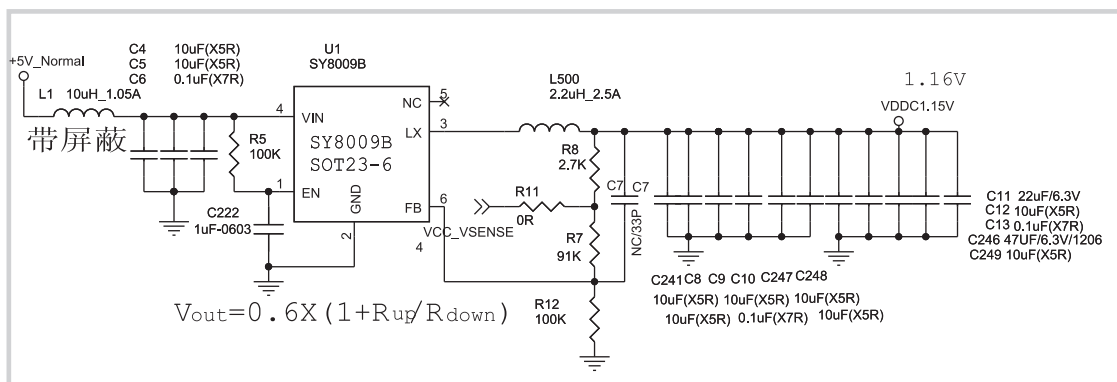
HDMI2_RX2P	HDMI2-RX2P	⌌ HDMI2-RX2P
HDMI2_RX2N	HDMI2-RX2N	⌌ HDMI2-RX2N
HDMI2_RX1P	HDMI2-RX1P	⌌ HDMI2-RX1P
HDMI2_RX1N	HDMI2-RX1N	⌌ HDMI2-RX1N
HDMI2_RX0P	HDMI2-RX0P	⌌ HDMI2-RX0P
HDMI2_RX0N	HDMI2-RX0N	⌌ HDMI2-RX0N
HDMI2_CLKP	HDMI2-CLKP	⌌ HDMI2-CLKP
HDMI2_CLKN	HDMI2-CLKN	⌌ HDMI2-CLKN

HDMI1

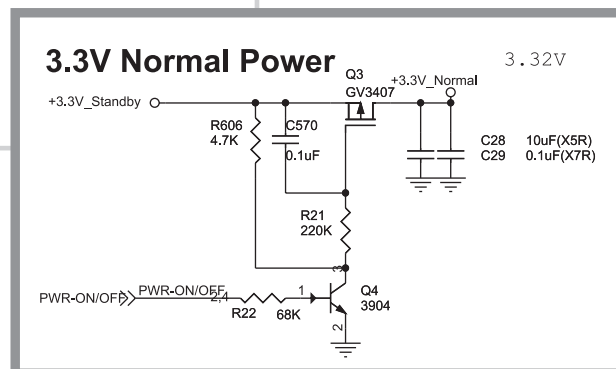
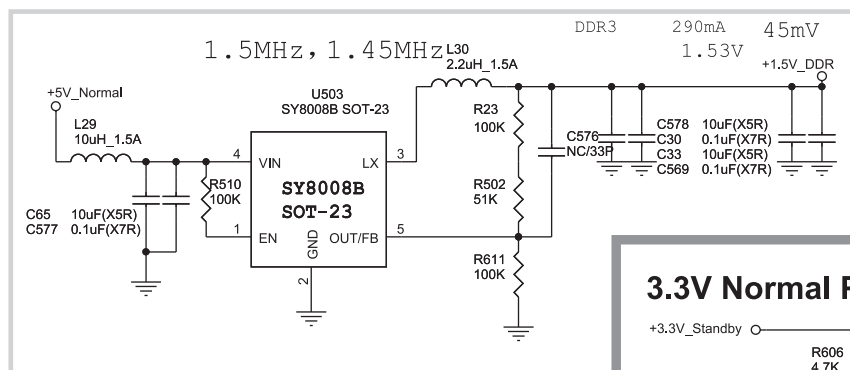


HDMI3_RX2P	HDMI3-RX2P	⋈ HDMI3-RX2P
HDMI3_RX2N	HDMI3-RX2N	⋈ HDMI3-RX2N
HDMI3_RX1P	HDMI3-RX1P	⋈ HDMI3-RX1P
HDMI3_RX1N	HDMI3-RX1N	⋈ HDMI3-RX1N
HDMI3_RX0P	HDMI3-RX0P	⋈ HDMI3-RX0P
HDMI3_RX0N	HDMI3-RX0N	⋈ HDMI3-RX0N
HDMI3_CLKP	HDMI3-CLKP	⋈ HDMI3-CLKP
HDMI3_CLKN	HDMI3-CLKN	⋈ HDMI3-CLKN



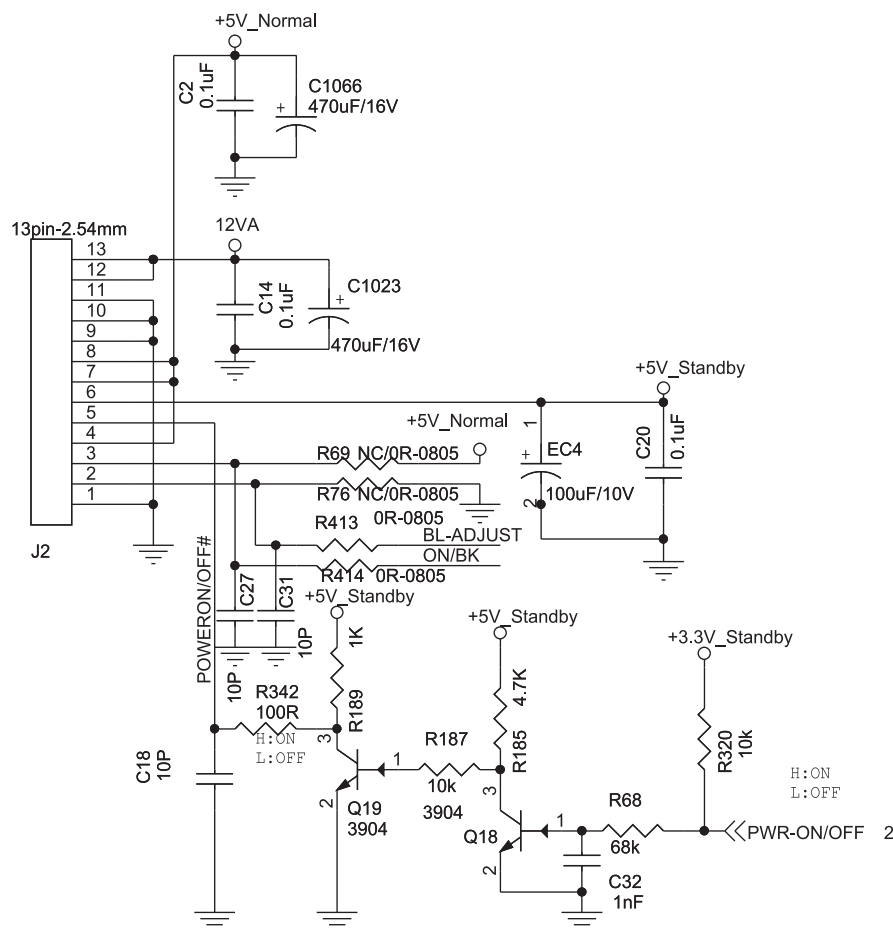


1.5V Normal Power for DDR3



Принципиальные схемы дополнительных узлов источника питания

Power Input



Inverter Interface

