

SCHEMATIC DIAGRAMS

PRODUCT NAME    SL833/SL863/UL863

UNIT NAME    MAIN

UNIT NO.    PE0982

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	FUNCTION	REV	PAGE	FUNCTION	REV	PAGE	FUNCTION	REV	PAGE	FUNCTION	REV
1	SEINEBL I2C/SD/USB/JTAG		26	SPI FLASH		51	DSUB15P PC IN		76	TVM(LED/RMT/RGB/TOUCH)	
2	SEINEBL DDR		27	EBUS NAND FLASH		52	EURO COMPONENT IN		77	TVM(PANEL I/F)	
3	SEINEBL EBUS/SPI		28	L2SW (MISC.)		53	EURO SCART OUT		78	TVM(PIR)	
4	SEINEBL UART/ETHER		29	L2SW (FMI1 I/F)		54	AV INPUT1		79	TVM(KEY CONNECTOR)	
5	SEINEBL TS		30	RJ45 CONNECTOR		55	AV INPUT2		80	TUNER	
6	SEINEBL MCU		31	WLAN I/F		56	AV INPUT3		81	SYNC-DET(TV-OUT)	
7	SEINEBL AUDIO		32	WLAN POWER		57	SYNC DETECT		82	TUNER DVB-S/S2	
8	SEINEBL LVDS OUT		33	USB CONNECTOR		58	PANEL I/F LVDS OUT		83	DEMODO DVB-T/C	
9	SEINEBL HDMI/DAC		34	USB HUB 1/2		59	PANEL I/F LVDS OUT		84	DEMODO DVB-T2/T/C	
10	SEINEBL ADC		35	USB HUB 2/2		60	PANEL I/F DIMMER CON		85	DEMODO DVB-S2/S	
11	SEINEBL ANALOG POWER		36	USB POWER		61	PANEL I/F POWER		86	DISEQC LNB POWER	
12	SEINEBL DIGITAL IO POWER		37	AUDIO RESISTOR		62	HDMI Connector		87	TUNER POWER	
13	SEINEBL MEMORY POWER		38	AUDIO I2S CLK		63	HDMI SW 1/3		88	CI TS-SELECTOR	
14	SEINEBL DIGITAL POWER		39	AUDIO MUTE		64	HDMI SW 2/4		89	CI POWER/CONNECTOR	
15	SEINEBL BOOT-CONFIG		40	AUDIO DAC (HEADPHONE)		65	HDMI SW 3/4		90	CI ADDRESS BUFFER	
16	SEINEBL XTAL/VREF		41	AUDIO Linedr1vr(TVOUT)		66	HDMI SW 4/4		91	CI DATA BUFFER	
17	SEINEBL PLL/DAC		42	AUDIO CONNECTOR		67	HDMI ARC		92	CI CONTROL BUFFER	
18	SEINEBL CAPACITOR		43	AUDIO DSP(CIRPUS)		68	HDMI Power		93	CI TS OUTPUT BUFFER	
19	SEINESL PMR-REG		44	AUDIO D-AMP		69	HDMI TX PMR5		94	POWER 1.5V	
20	PERI (CLK/VCXO)		45	AUDIO AMP FILTER		70	HDMI TX PMR5		95	POWER 1.1V	
21	PERI (PIO/IRQ)		46	AUDIO D-AMP (woofer)		71	TVM(MICON)		96	POWER 3.3V	
22	PERI (I2C/UART)		47	AUDIO AMP FILTER (woofer)		72	TVM(MICON2)		97	POWER 5V/5V	
23	DDR3 SDRAM		48	Headphone AMP		73	TVM(RESET I2C)		98	POWER CONNECTOR	
24	DDR3 VTT		49	EURO SCART		74	TVM(CEC)		99	EMC CONTROL	
25	EBUS RESISTOR		50	EURO SIDE AV		75	TVM(FAN CONTROL)		100		

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE		TOTAL	PAGE NO.	REV. MARK	DRAWING. NO.
IWANO	IWANO	SHIMIZU	CIRCUIT DIAGRAM		99	TITLE	PP	

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2011.01.14 10:26

V46A00062200

DTV SYSTEM LSI 3/14  
(V-U90432)  
IC100

131	M-EBDAT15	F25	EBCLKI	EBCLKO	A20	M-EBADD8	131
131	M-EBDAT14	A24	EBDAT15/CIAREG-N	EBADD8/CIDAT7/NAALE	A26	M-EBADD8	131
131	M-EBDAT13	C24	EBDAT14/CIADD14	EBADD7/CIDAT6/NAACLE	B26	M-EBADD7	131
115	131	C24	EBDAT13/CIADD13	EBADD6/CIDAT5	D26	M-EBADD6	131
115	131	D24	EBDAT12/CIADD12	EBADD5/CIDAT4	A25	M-EBADD5	131
115	131	A23	EBDAT11/CIADD11	EBADD4/CIDAT3	B25	M-EBADD4	131
115	131	B23	EBDAT10/CIADD10	EBADD3/CIDAT2	C25	M-EBADD3	131
115	131	B23	EBDAT9/CIADD9	EBADD2/CIDAT1	D25	M-EBADD2	115 131
131	M-EBDAT9	D23	EBDAT8/CIADD8	EBADD1/CIDAT0			
131	M-EBDAT8	D23	EBDAT7/CIADD7/NADAT7		E21	M-CICS-N	794
131	M-EBDAT6	D23	EBDAT6/CIADD6/NADAT6	EBCE2-N/CICS-N/PIO52	D21	M-NACE-N	131
131	M-EBDAT5	A22	EBDAT5/CIADD5/NADAT5	EBCE1-N/NACE-N	C21		
131	M-EBDAT4	B22	EBDAT4/CIADD4/NADAT4	EBCEO-N			
131	M-EBDAT3	C22	EBDAT3/CIADD3/NADAT3	EBBE1-N/CIIOHD-N/NAME-N	B21	M-CIIORD-N	795
131	M-EBDAT2	D22	EBDAT2/CIADD2/NADAT2	EBBE0-N/CIIOWR-N/NAME-N	A21	M-CIIOWR-N	795
131	M-EBDAT1	E22	EBDAT1/CIADD1/NADAT1	EBBE-N/CIIE-N	E20	M-EBWE-N	131
131	M-EBDAT0		EBDAT0/CIADD0/NADAT0	EBBE-N/CIIE-N	D20	M-EBBE-N	131
131	M-NABSY-N	B19	EBINT/NABSY-N	EBBE-N/CIIE-N			
131	M-NAME-N	C20	EBMAIT-N/NAME-N/PIO53	EBBE-N/CIIE-N	B20	M-NAME-N	131

[SPI]

132	M-SPI-MOSI	P34	SPI-MOSI/MCU-P14	SPI-CLK	N34	M-SPICK	132
132	M-SPI-MISO	P34	SPI-MISO/MCU-P12	SPI-CS0-N	M34	M-SPI-CS0	132
132	M-SPI-I02	R34	SPI-I02/MCU-P13				
132	M-SPI-I03	R34	SPI-I03/MCU-P14	SPI-CS1-N/MCU-P10	N33		

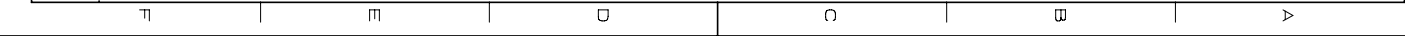
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101P  
101P

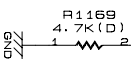
TC90432WB6 (OF 1203)

DESIGNED BY	2011/01/05	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU		MAIN UNIT	S5L EBUS/SPI	103	3	PP	PE0982

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V46A00062200

DTV SYSTEM LSI 7/14

[V-U90A32]

IC100

1  
0  
pin1  
pin15

120	M-POLK-SEL0	AC3
120	M-POLK-SEL1	AC2
120		AC1

AMCLKI/AMCLKOF PD  
ALRCKIA/PIO65  
ABCKIA/PIO66  
ASDIA/TSIXRE/PIO67

1  
0  
pin1  
pin16

AC4	M-AMCLKOX	300
AG5	M-ALRCKOX	300
AG4	M-ABCKOX	300
AH2	M-ASDOX0	300
AH3	M-ASDOX1	300
AH5	M-ASDOX2	300

Open when Unused  
ALRCKOX  
ABCKOX  
ASDOX0  
ASDOX1/PIO68  
ASDOX2/PIO69

Open when Unused  
AMCLKOX  
ALRCKOX/PIO71  
ABCKOX/PIO70  
ASDOY/PIO72

ATX0

AJ3	M-ATX0	300
AD4	M-AMUTEX	302 301
AD5		
DE		
E6		
H7	M-AADVCML	116

Open when Unused  
AMUTEX-N  
SIF-ADC-CLKI

606	A-E1-L-SBL	A4
606	A-E1-R-SBL	B4
606	A-E2-L-SBL	A5
606	A-E2-R-SBL	B5
606	A-E3-L-SBL	A6
607	A-E3-R-SBL	B6
607		A7
607		B7
607		A8
607		B8

ALIN1 Open when Unused  
ALIN1 Open when Unused  
ALIN2 Open when Unused  
ALIN3 Open when Unused  
ALIN4 Open when Unused  
ALIN5 Open when Unused  
ALIN6 Open when Unused  
ALIN7 Open when Unused  
ALIN8 Open when Unused  
ALIN9 Open when Unused  
ALIN0 Open when Unused  
ALIN1 Open when Unused  
ALIN2 Open when Unused  
ALIN3 Open when Unused  
ALIN4 Open when Unused  
ALIN5 Open when Unused  
ALIN6 Open when Unused  
ALIN7 Open when Unused  
ALIN8 Open when Unused  
ALIN9 Open when Unused  
ALIN0 Open when Unused

Open when Unused  
ALMONIDUT  
ADVCML  
ALSPOUT  
AFSPOUT  
ALTUNEOUT  
ARTUNEOUT

C1	M-ALTUNEOUT	315
C2	M-ARTUNEOUT	316
C3		
C4		
C5		
C6		
C7		
C8		
C9		
C0		

790	SIF-SBL	U1
116	M-VREFIN	U2

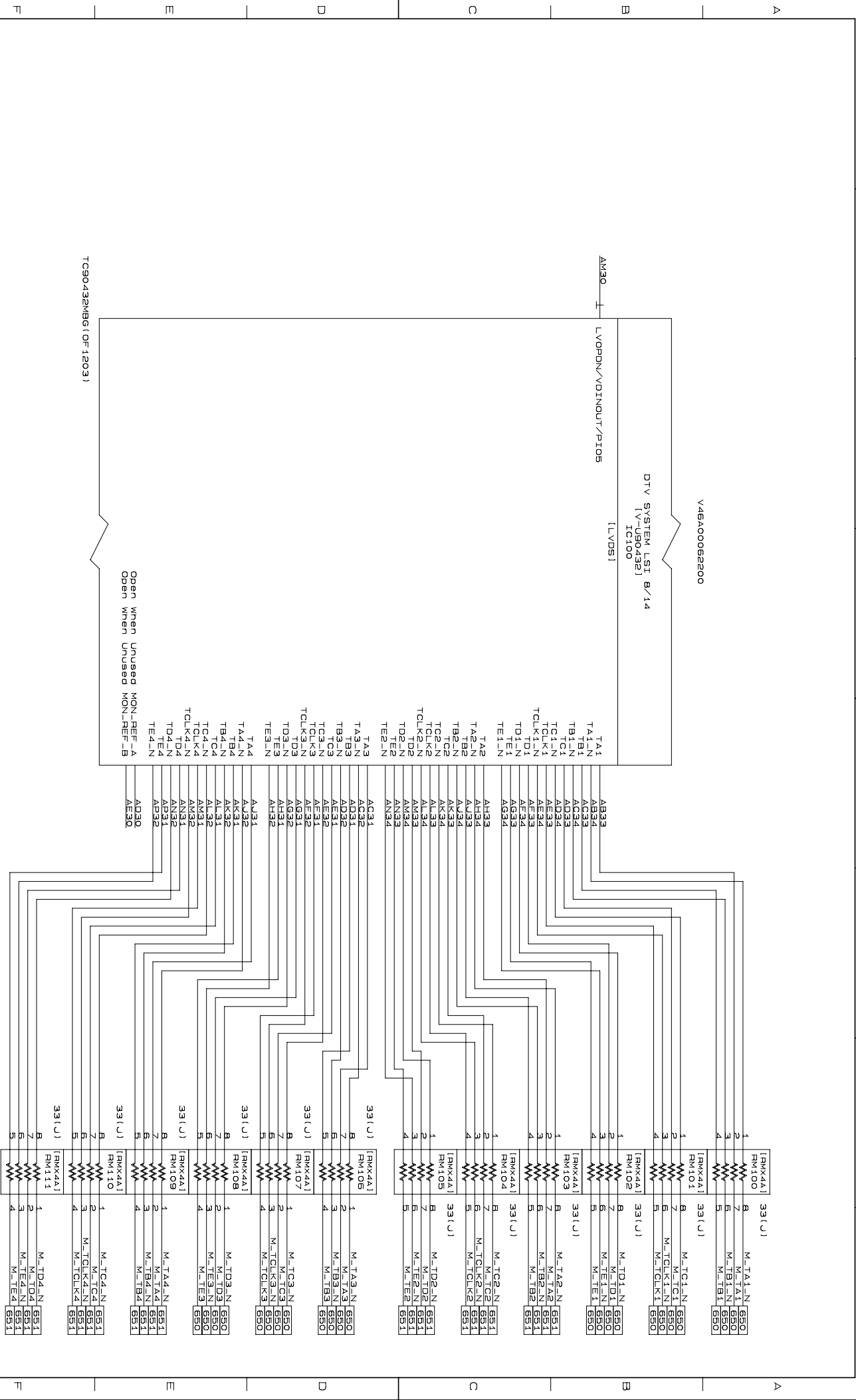
SIFIN Open when Unused  
VREFIN

TC90432MBG (OF 1203)

DESIGNED BY	2011/01/05	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU		MAIN UNIT	SBL AUDIO	107	7	PP	PE0982

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DESIGNED BY	2011/01/05	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU		MAIN UNIT	S&L LVDS	108	8	PP	PE0982

V46A00062200

DTV SYSTEM LSI 9/14  
[V-U90432]  
IC100

[HDMI]

Open When Unused  
HPLUG  
OPEN TOUTP  
TOUTN

E12  
D11  
E11

661 H\_RXDSINK\_CP B12  
661 H\_RXDSINK\_CN B12  
661 H\_RXDSINK\_ON B11  
661 H\_RXDSINK\_ON B11  
661 H\_RXDSINK\_1P B10  
661 H\_RXDSINK\_1N B10  
661 H\_RXDSINK\_2P B9  
661 H\_RXDSINK\_2N B9

RXP0 RXNC  
RXP0 RXNC  
RXP1 RXN1  
RXP2 RXN2  
RXN2 RXN2  
REXT REXT

116 M\_REXT H13

REXT REXT

662 H\_PWR\_STINK D12

DDCSV PU When Unused  
SCL\_DDC PU When Unused  
SDA\_DDC PU When Unused

117 M\_PACKIN V4

PACKIN

V7 M\_P3FIL 147  
V5 M\_DOUT3 147  
U7 M\_BIASDA3 117

[VCROUT]

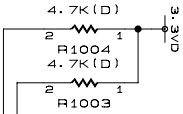
116 M\_DAVVREF K2  
116 M\_DAVIREF L2  
116 M\_DAVVB U7

DAVVREF  
DAVIREF  
DAVVB

P3FIL  
DOUT3  
BIASDA3

E1 M\_DAVOUTP 605  
E2 M\_DAVOUTN 605

TC90432MB6 (OF 1203)



DESIGNED BY	2011/01/05	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU		MAIN UNIT	S&L HDMI	109	9	PP	PE0982

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V46A00062200

DTV SYSTEM LSI 10/14  
[V-U90A32]  
IC100

[ADC]

XOUT42

AB2 M-XOUT42 116  
T3 M-VMONOUT 630  
T4 M-VTUNEOUT 605  
P7 M-VBG 117

MONOUT

VTUNEOUT

VBG

VBG

VBG

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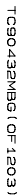
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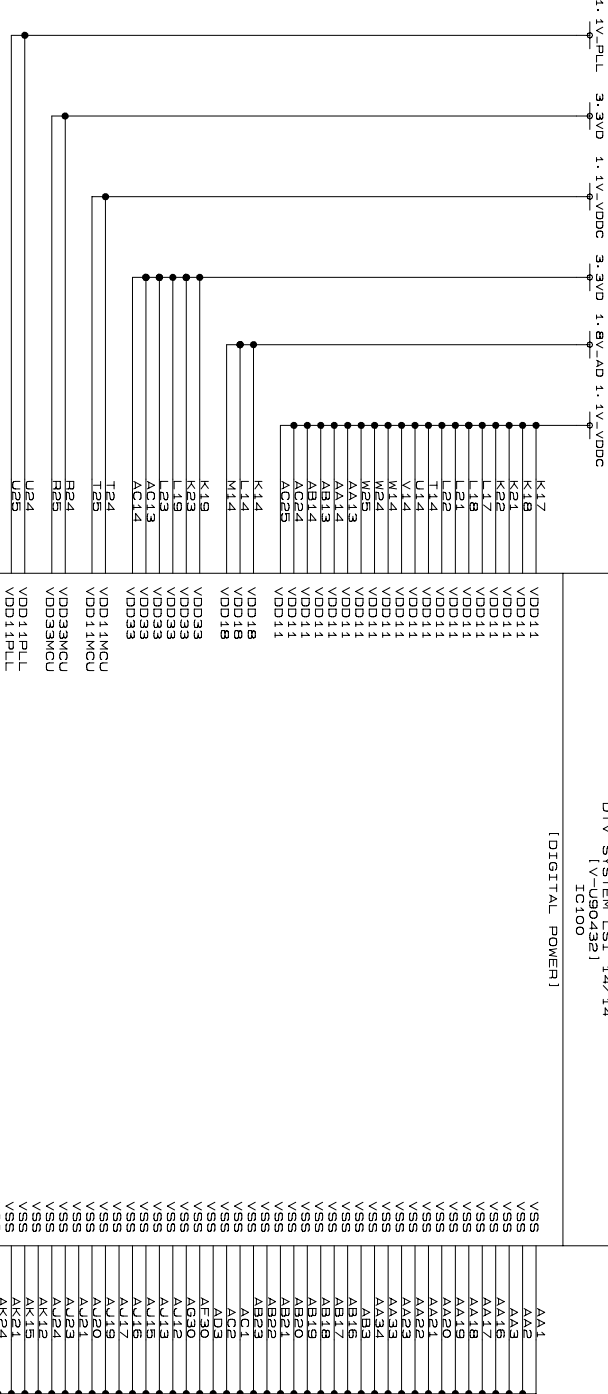
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V46A00062200

DTV SYSTEM LSI  
(V-HS042E)  
IC100

[DIGITAL POWER]



TC90432M8G (OF 1203)

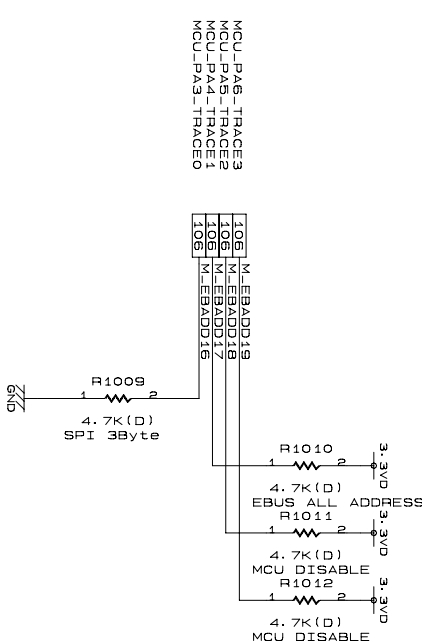
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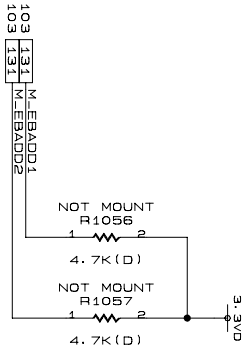
DESIGNED BY T. SHIMIZU	2011/01/05	TITLE MAIN UNIT	FUNCTION S&L DIGITAL POW	SH. NO. 4	PAGE NO. 14	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14	10:26						TOSHIBA CORPORATION

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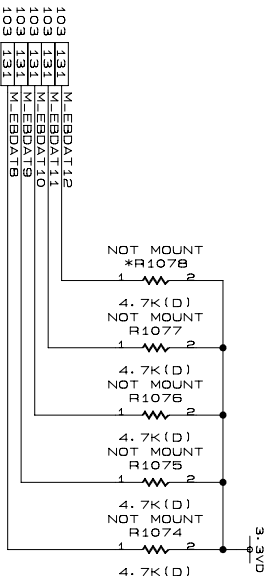
BOOT CONFIG (MCU)



BOOT CONFIG



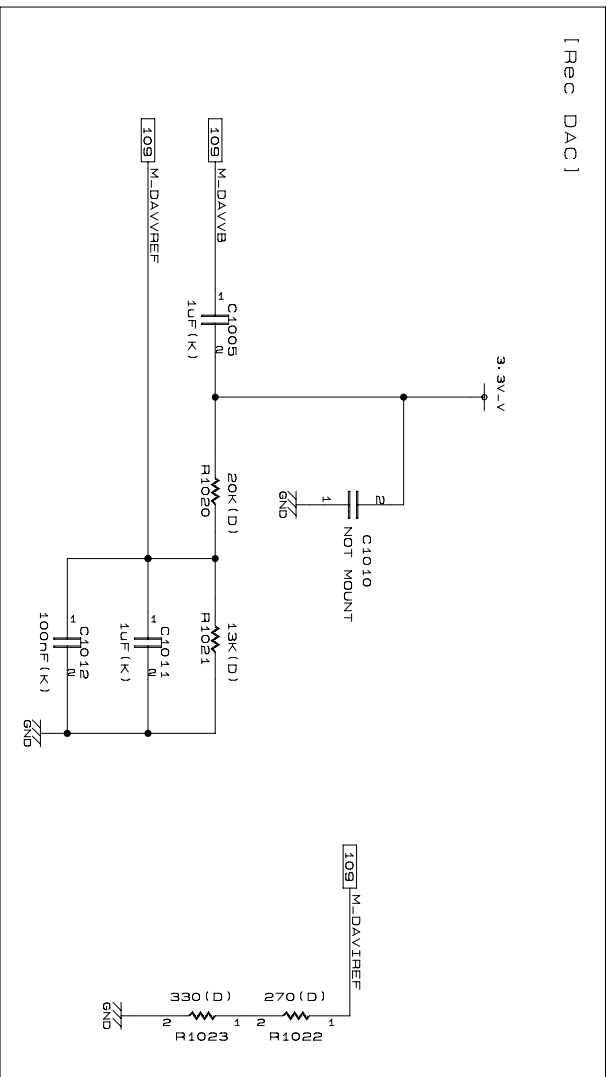
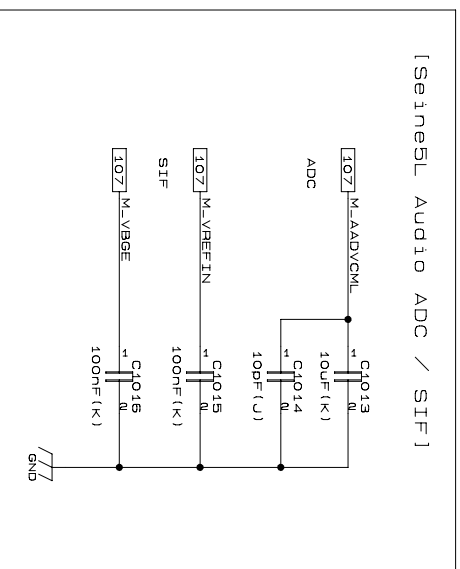
BOOT CONFIG (BOARD REVISION)

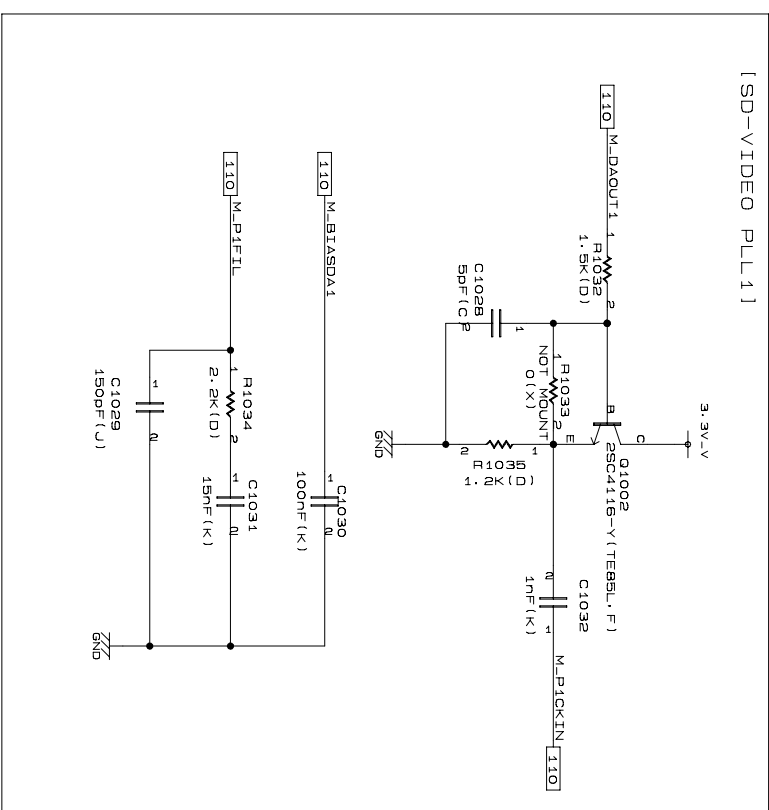
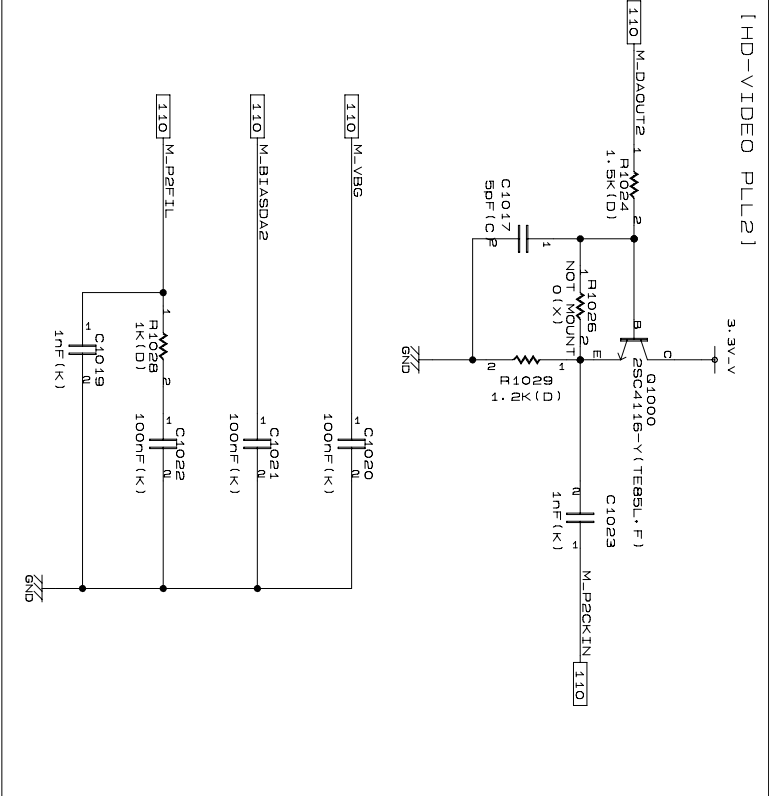
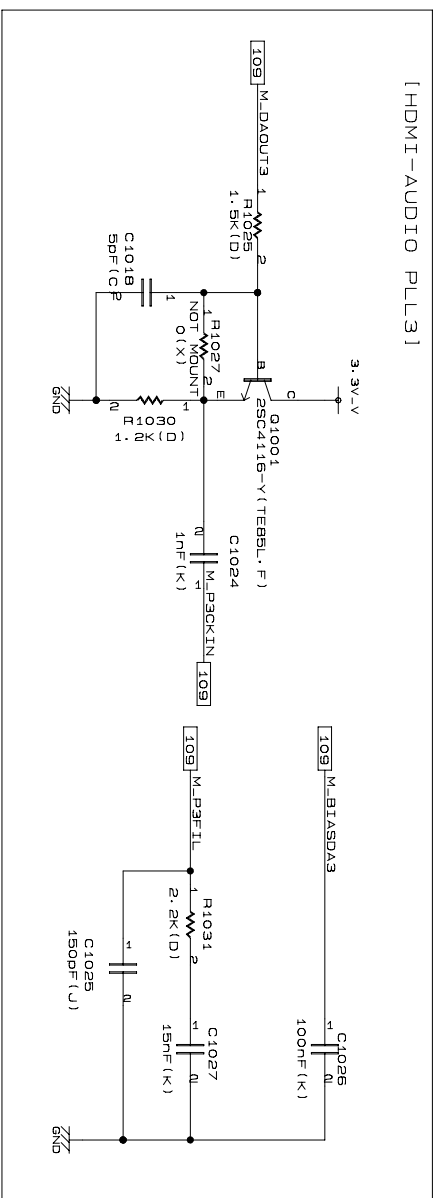


BOARD REVISION	
REV	COMMENT
5' 00xxxx	DDR3 SDRAM = 2Gb1tx2
5' 01xxxx	DDR3 SDRAM = 1Gb1tx1

DESIGNED BY T. SHIMIZU	2011/01/11	TITLE MAIN UNIT	FUNCTION S5L Boot-Config	SH. NO. 115	PAGE NO. 15	REV. MARK PP	DRAWING. NO. PE0982
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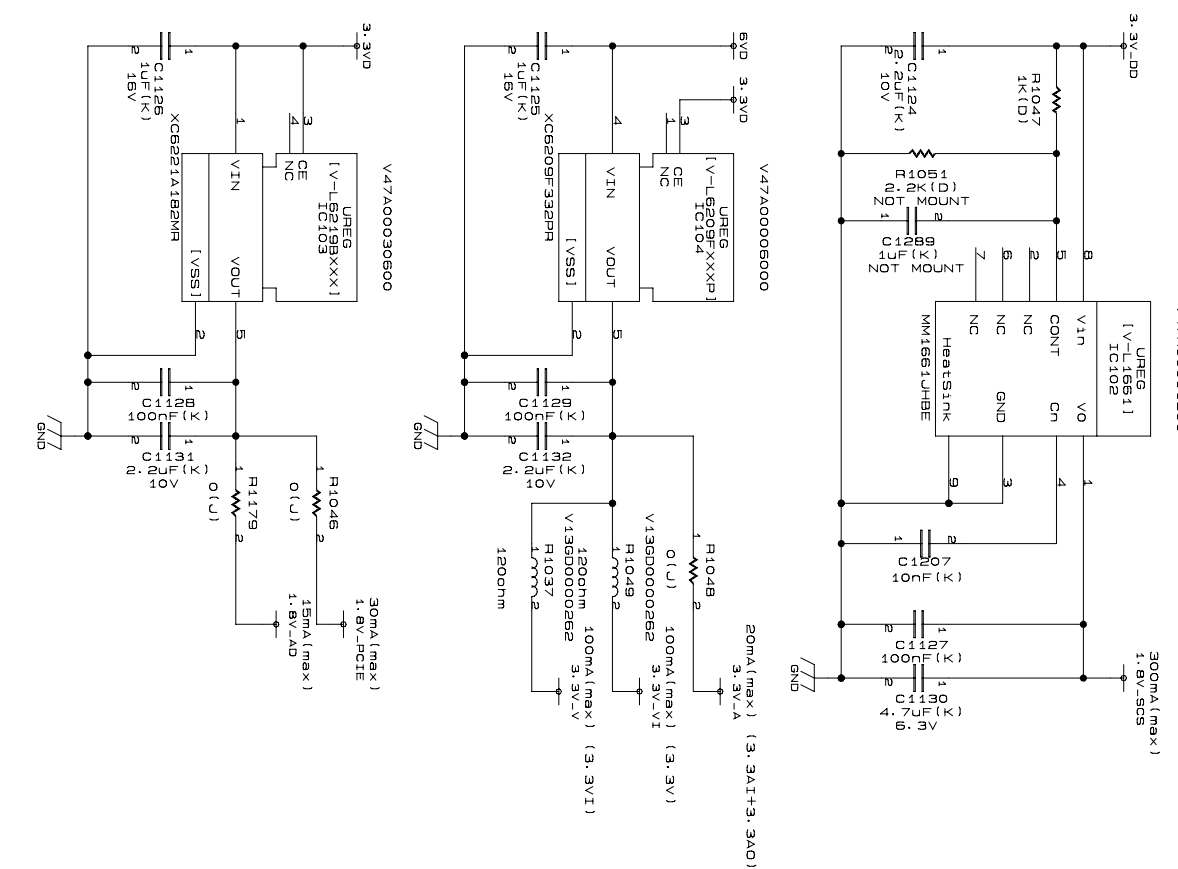
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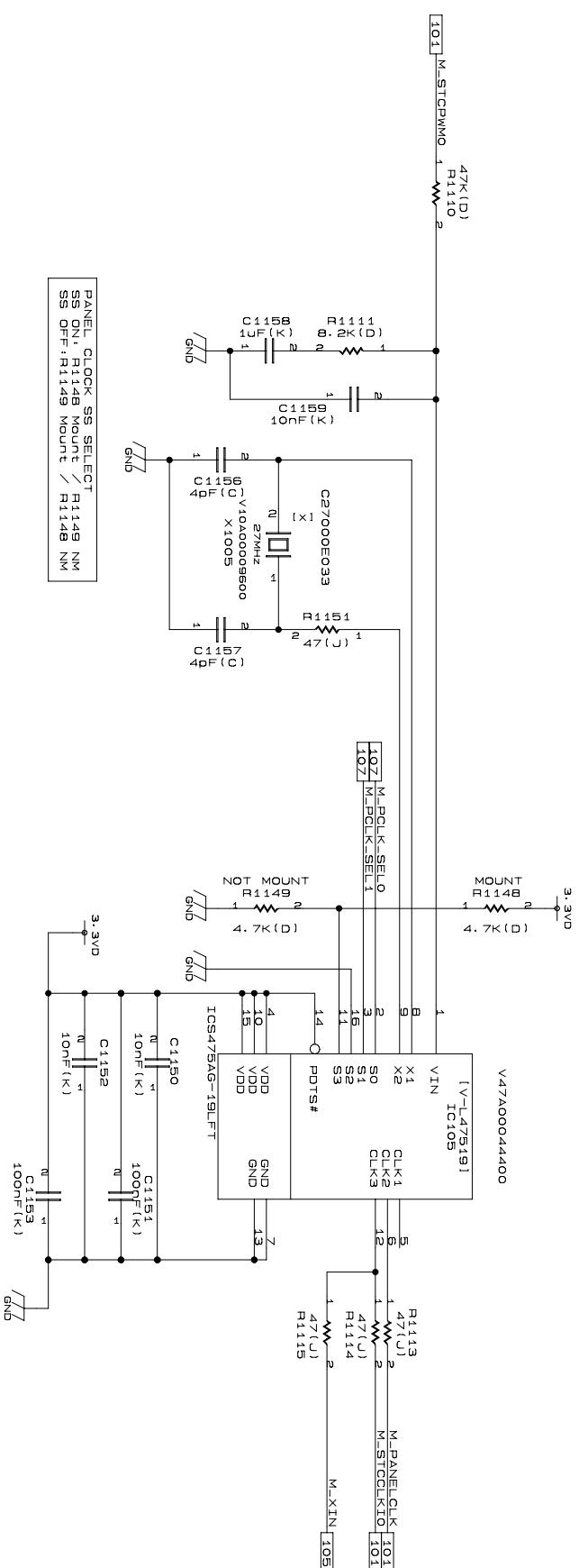
DESIGNED BY	2011/01/11	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU		MAIN UNIT	S5L PLL/DAC	117	17	PP	PE0982
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DRAWING. NO.  
PEO982

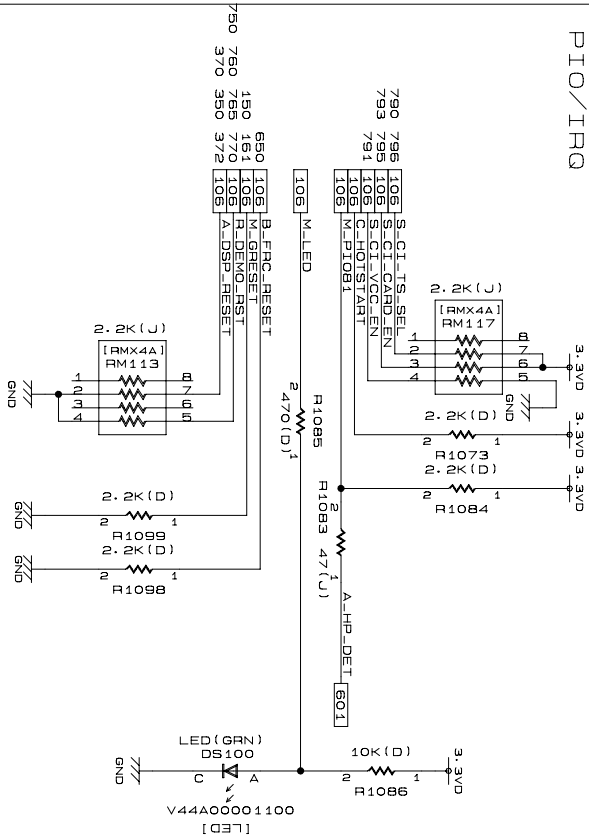


DESIGNED BY	2011/01/11	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU		MAIN UNIT	SSL A-PWR-REG	119	19	PP	PE0982
2011.01.14	10:26	TOSHIBA CORPORATION					

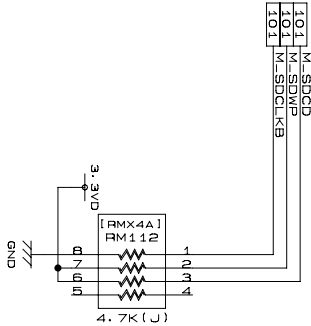
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PIO/IRQ



TERMINATION



DESIGNED BY T. SHIMIZU

2011/01/05  
MAIN UNIT

FUNCTION  
PIO/IRQ

SH. NO.  
122

PAGE NO.  
21

REV. MARK  
PP

DRAWING. NO.  
PE0982

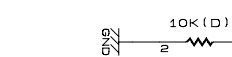
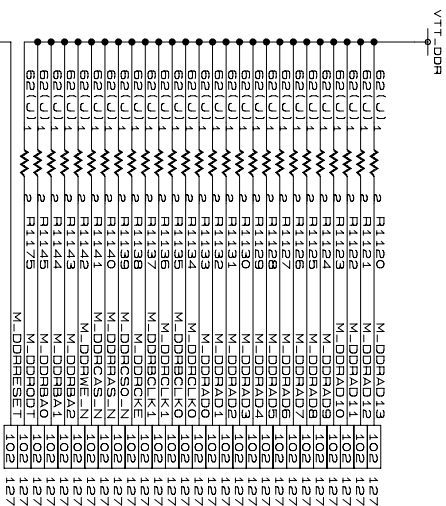
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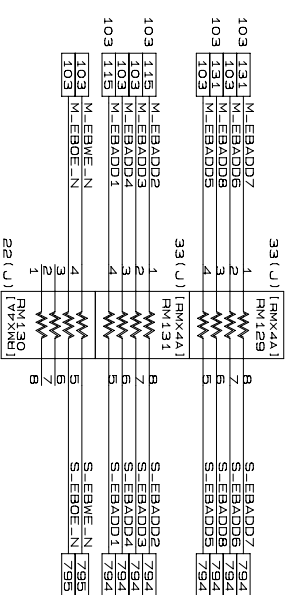






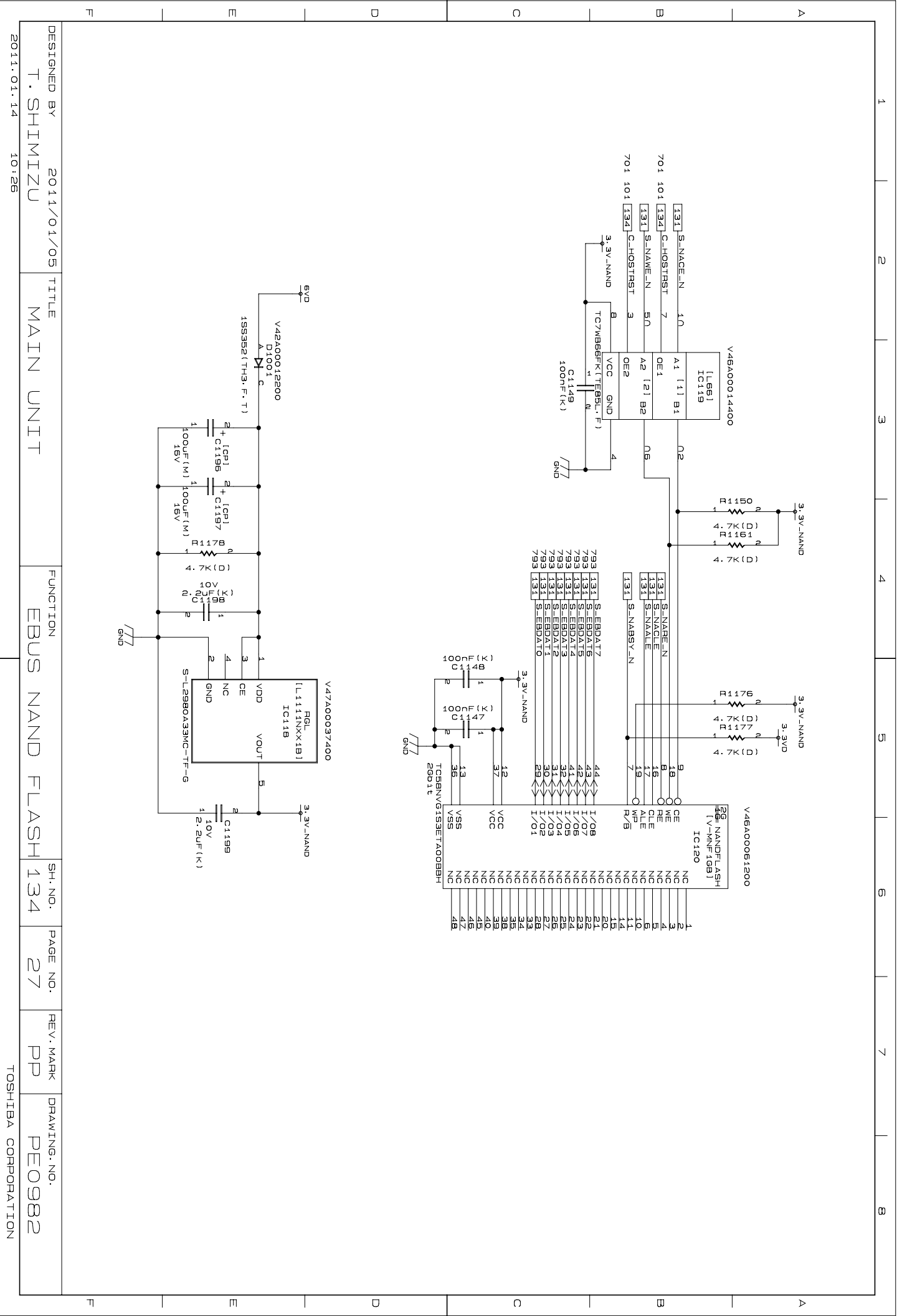
MARK	DRAWING. NO.
P	PE0982

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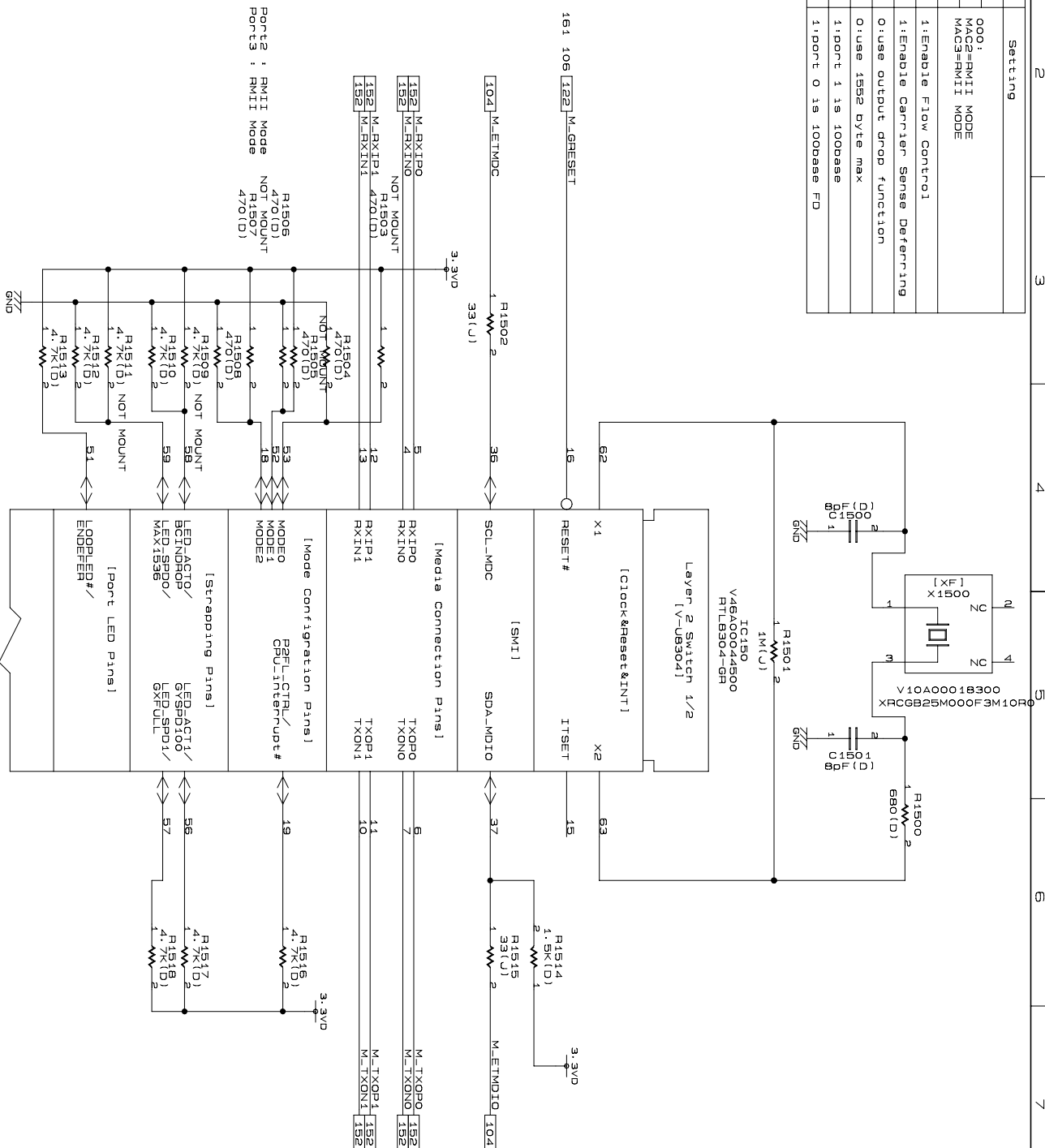


DRAWING. NO.  
PEO982

TOSHIBA CORPORATION



Name	Shared pin	Setting
Mode2	-	000; PWM1 MODE
Mode1	-	MACS-PWM1 MODE
Mode0	-	
P2FL-CTRL	CPU-INTERRUPT#	1:Enable Flow Control
ENDEFER	LOOPLED#	1:Enable Carrier Sense Deferring
BCINDROP	LED-ACT0	0:Use output drop function
MAX1536	LED-SPD0	0:Use 1552 byte max
GSPD100	LED-ACT1	1:port 1 is 100base
GXFULL	LED-SPD1	1:port 0 is 100base FD



DESIGNED BY  
KOJIMA

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2011.01.14 10:26

FILE  
MAIN UNIT

L2SW (MISC.)

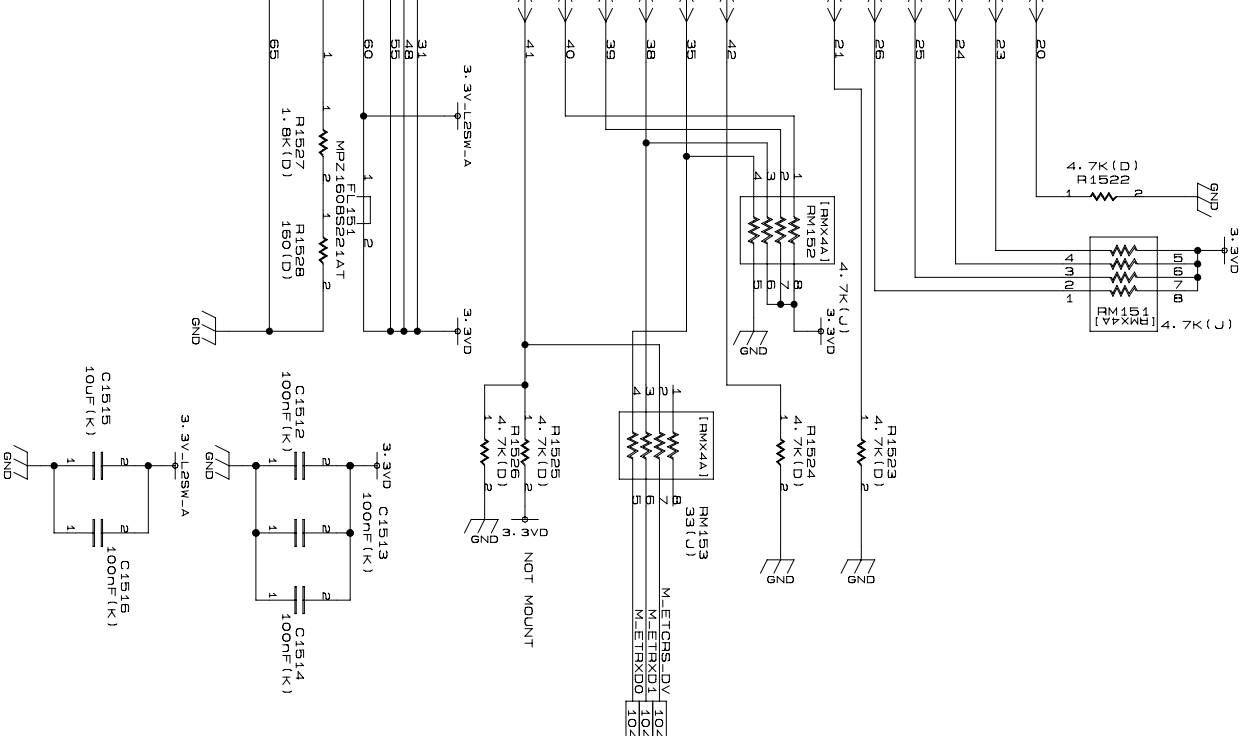
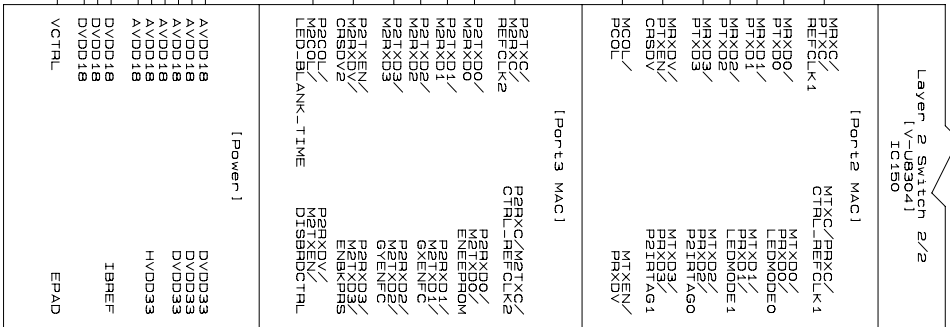
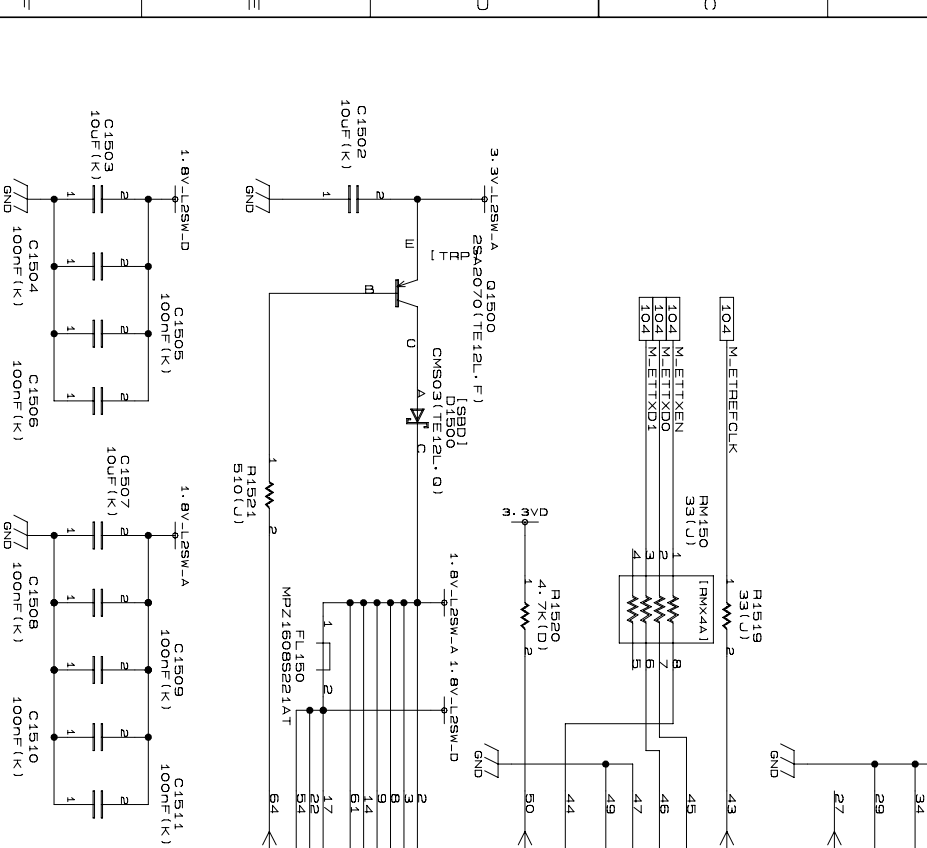
SH. NO.  
150

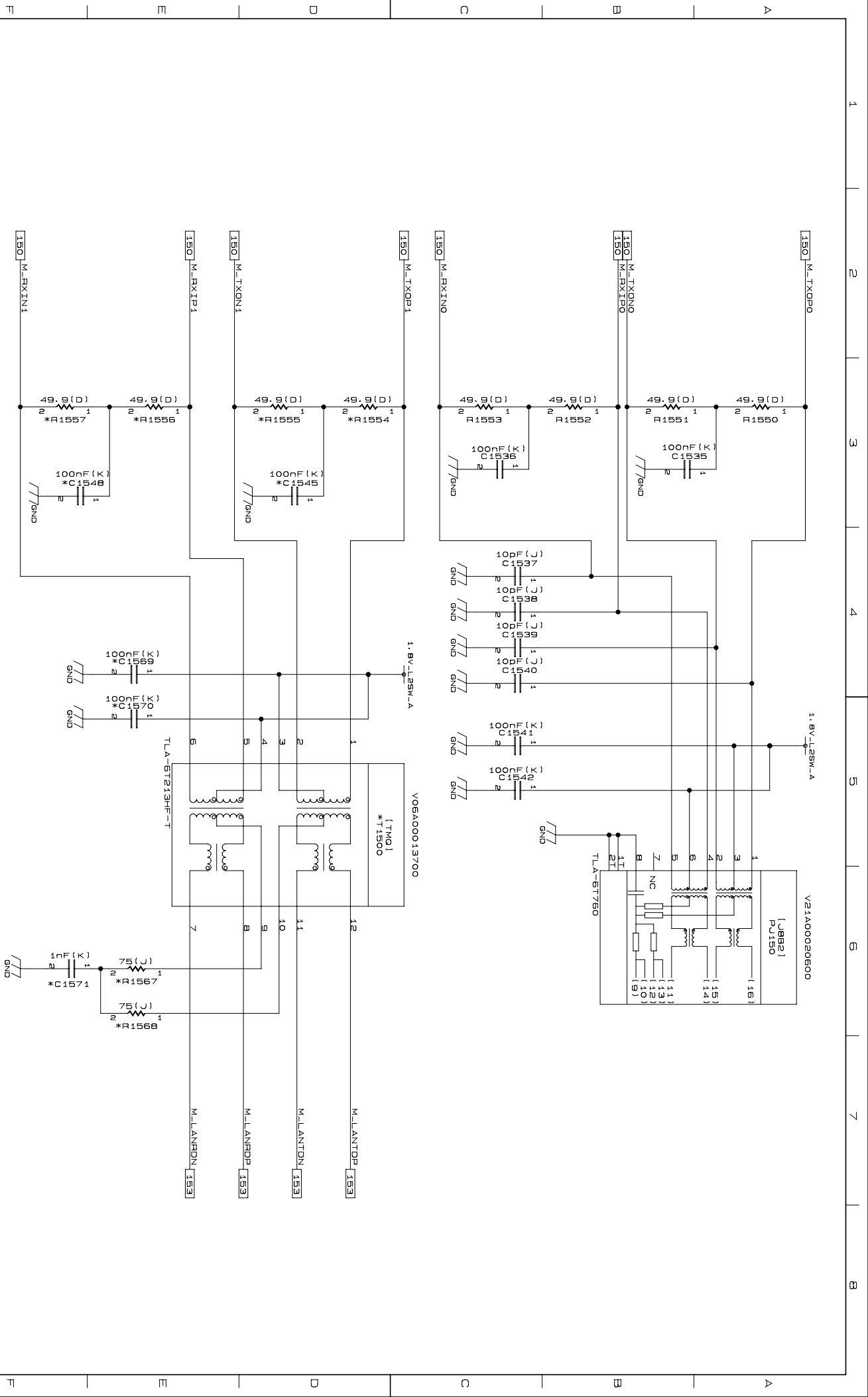
PAGE NO. 28

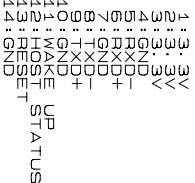
REV. MARH  
PP

DRAWING. NO.  
PEO982

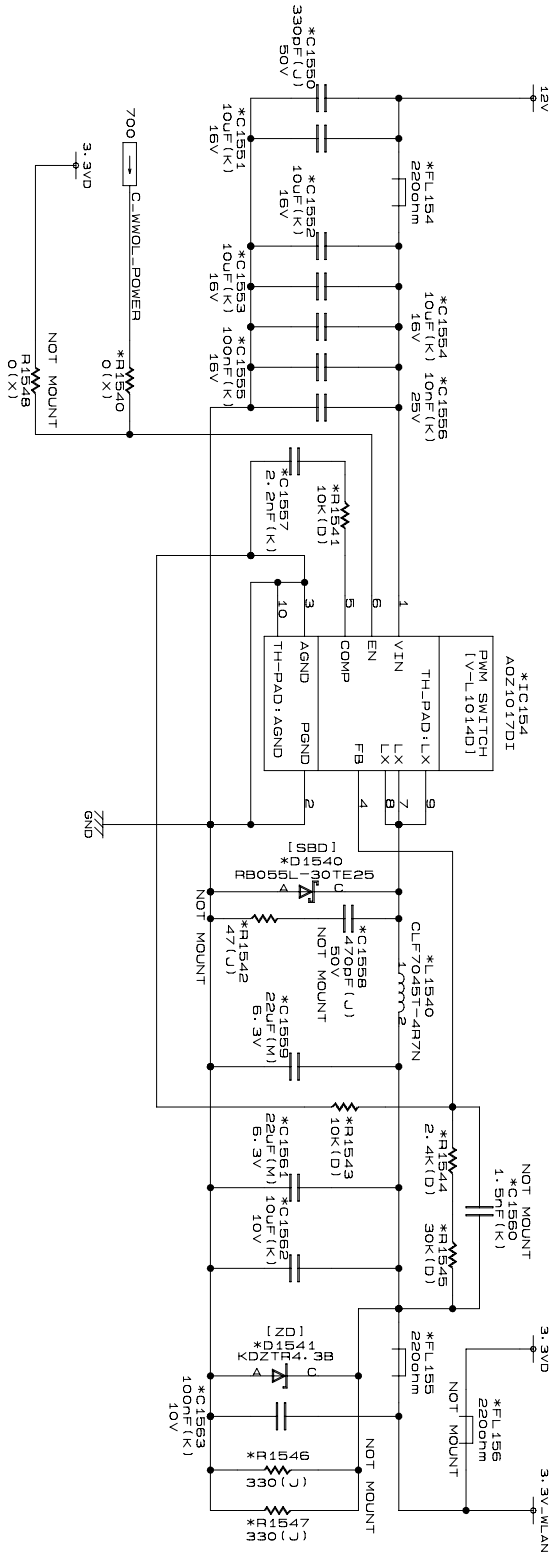
Name	shared pin	Setting
P2:IRTAG[1]	P2:RXD3	1:Do not insert/remove VLAN tag
P2:IRTAG[0]	P2:RXD2	
LEDMODE[1]	P2:RXD1	1:Speed+Link/ACT
LEDMODE[0]	P2:RXD0	
CTRL-REFCLK1	P2:C-REFCLK1	0:Output 50MHz REFCLK
LED_BLNK_TIME	P3:COL	Blink time is 43ms
CTRL-REFCLK2	P3:C-REFCLK2	0:Output 50MHz REFCLK
ENAKPRS	P3:RXD3	1:Enable backpressure
GXENFC	P3:RXD2	1:Enable prot 1 Nway(etc)
GXENFC	M-ETRXD1	1:Enable prot 0 Nway(etc)
EEPROM	M-ETRXD0	0:Disable EEPROM
DISBDDCTRL	M-ETGRS-DV	1:Disable Broadcast Strom Control





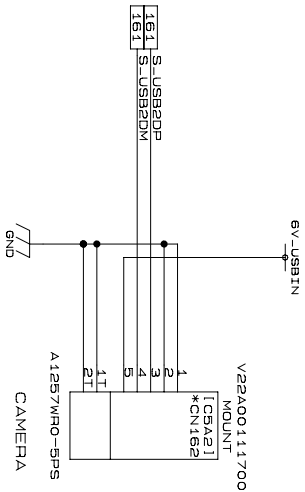
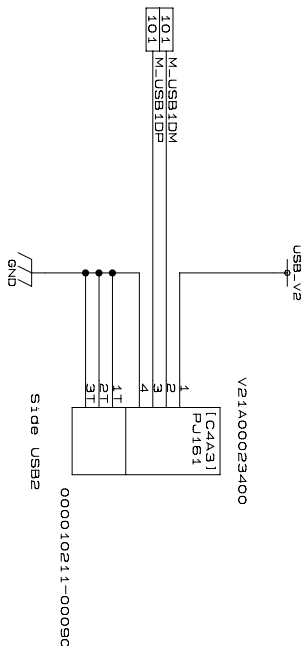
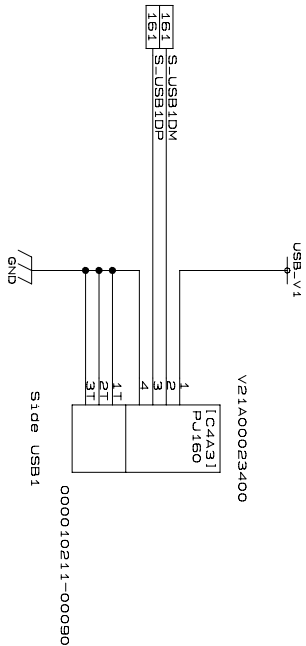
TOSHIBA CORPORATION





DESIGNED BY MASAKI	2011/01/05	TITLE MAIN UNIT	FUNCTION WLAN POWER	SH. NO. 154	PAGE NO. 32	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14	10:26						TOSHIBA CORPORATION

12345678

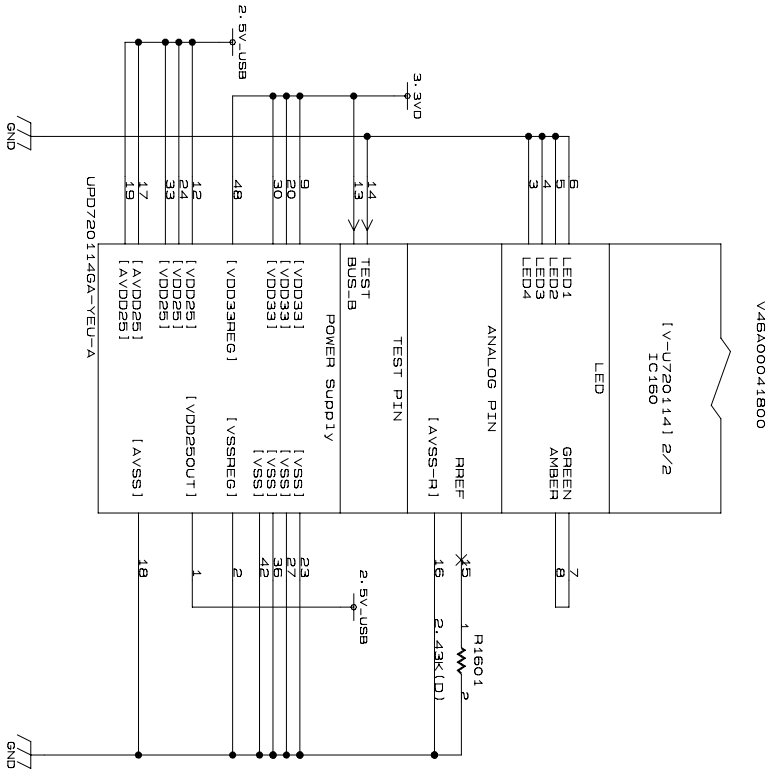


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KOJIMA	MAIN UNIT	USB CONNECTOR	160	33	PP	PE0982

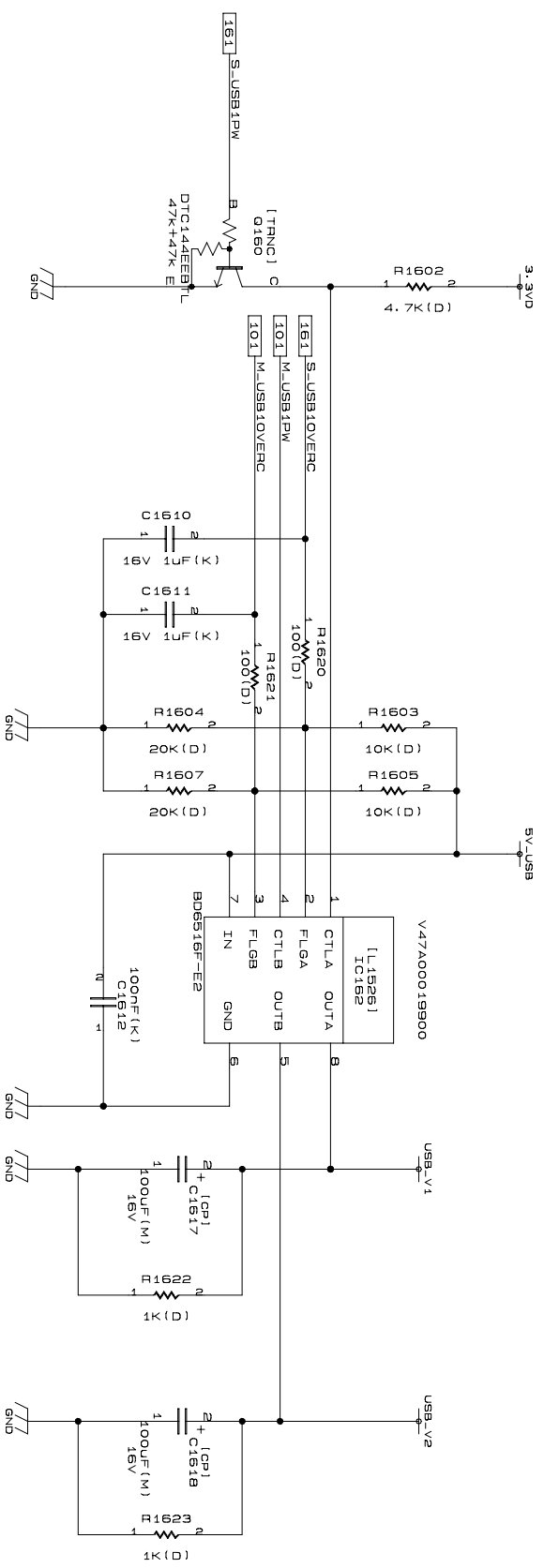
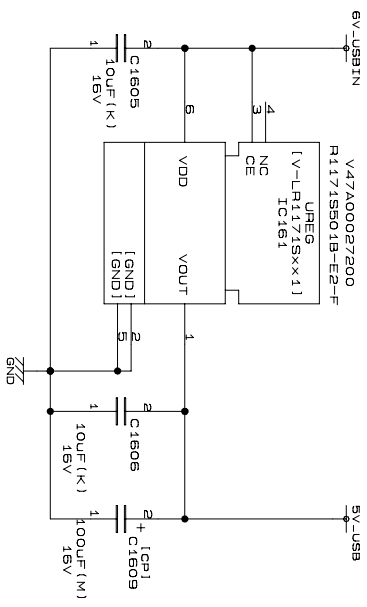
2011.01.14 10:27

TOSHIBA CORPORATION

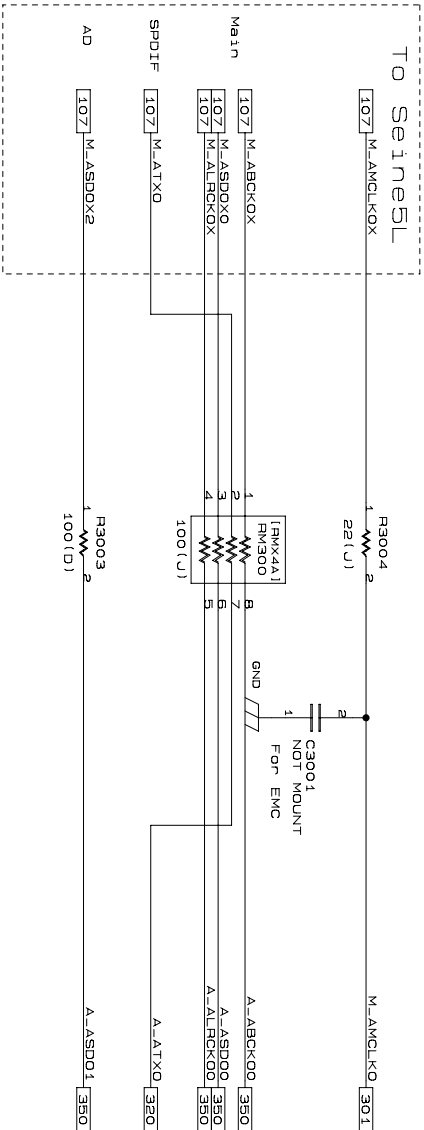
TOSHIBA CORPORATION



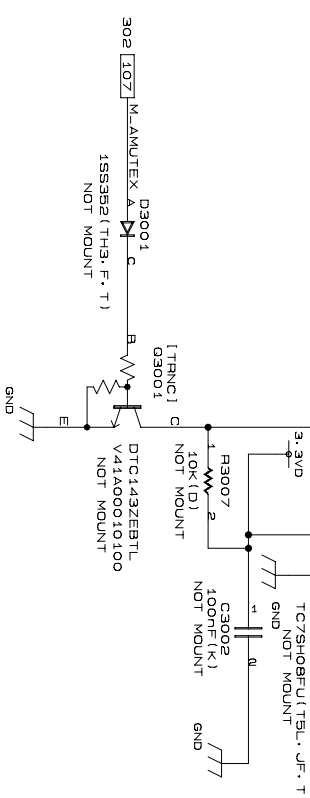
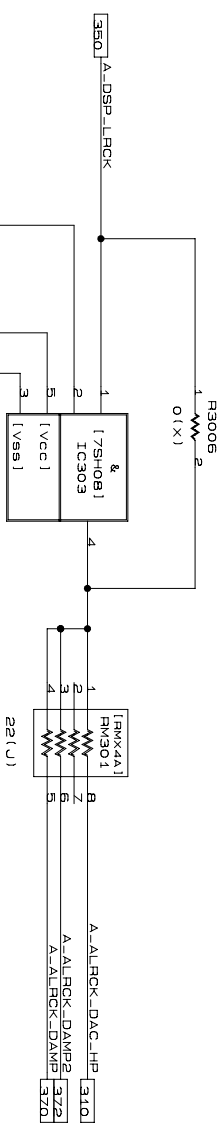
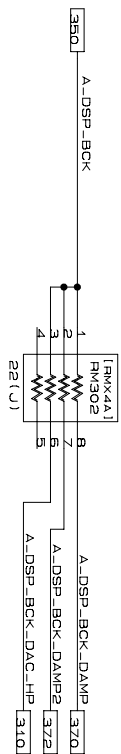
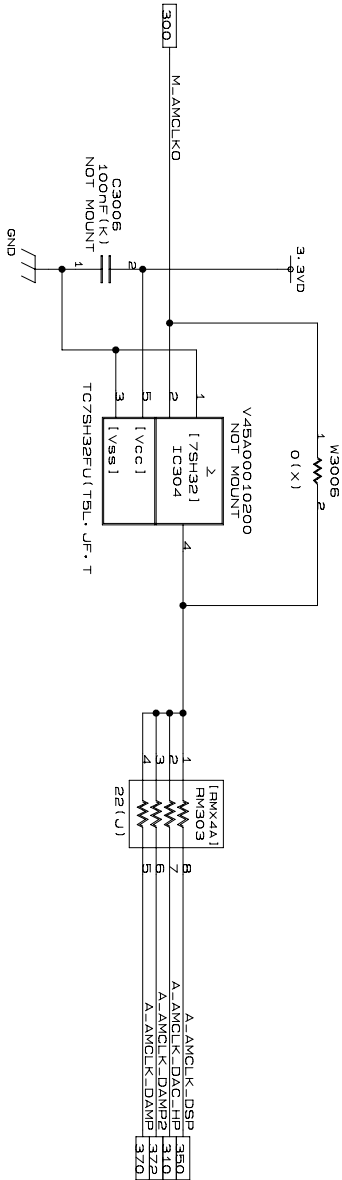
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KOJIMA	MAIN UNIT	USB HUB	162	35	PP	PE0982
2011.01.14	10:27					



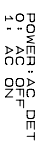
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KOJIMA	MAIN UNIT	USB POWER	163	36	PP	PE0982
2011.01.14	10:27					



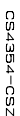
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ENDO	MAIN UNIT	AUDIO RESISTOR	300	37	PP	PE0982

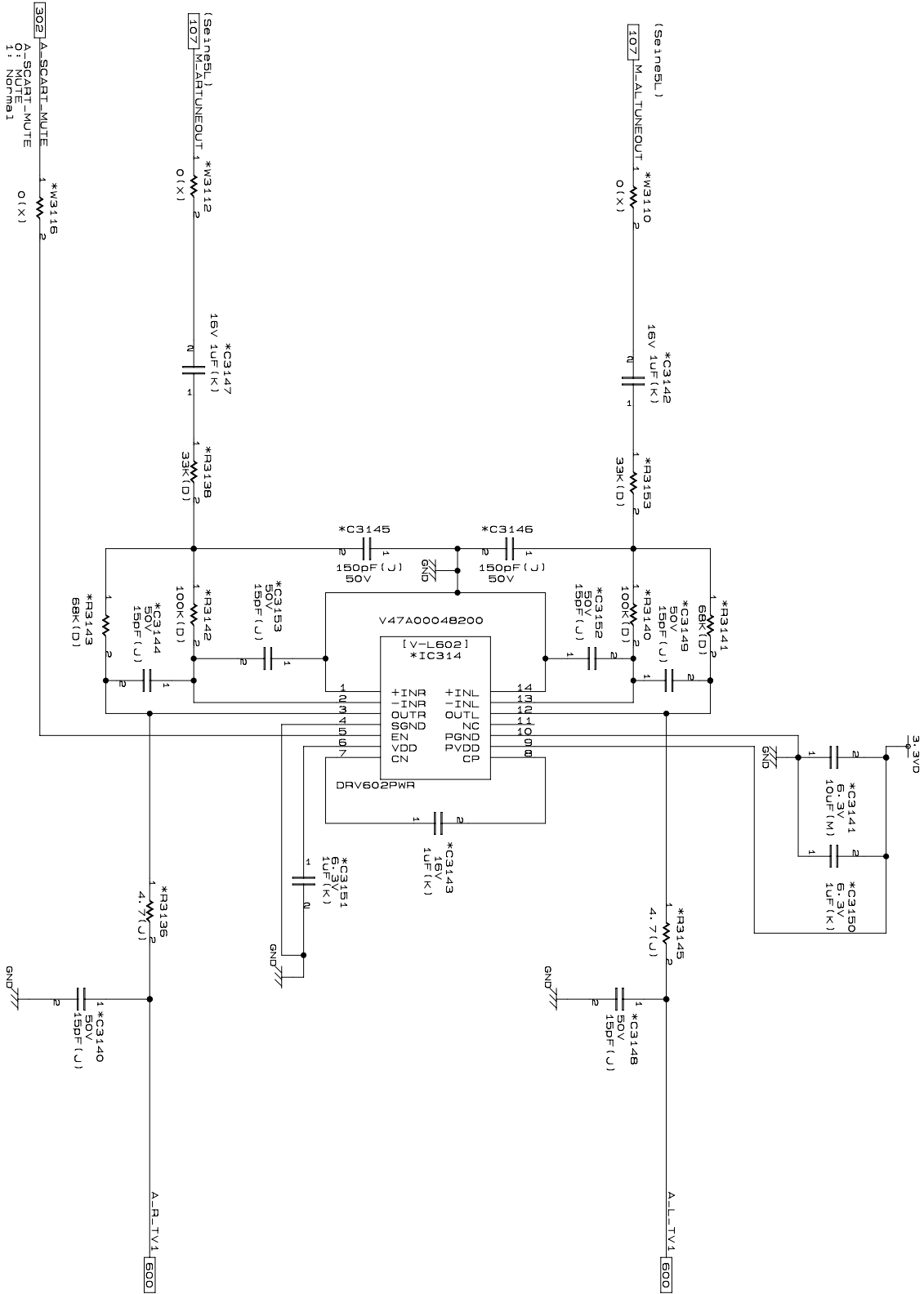


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ENDO	MAIN UNIT	AUDIO I2S CLK	301	38	PP	PE0982
2011.01.14	10:27					TOSHIBA CORPORATION

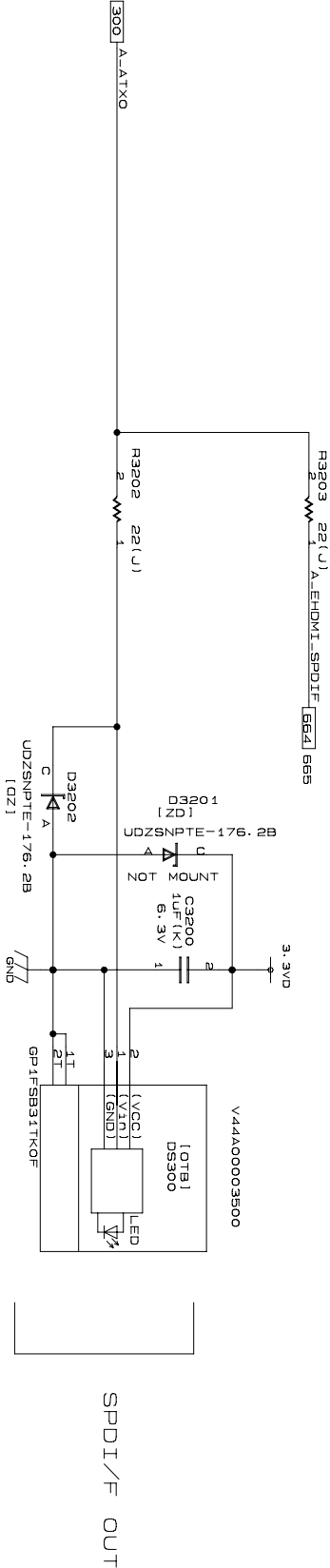
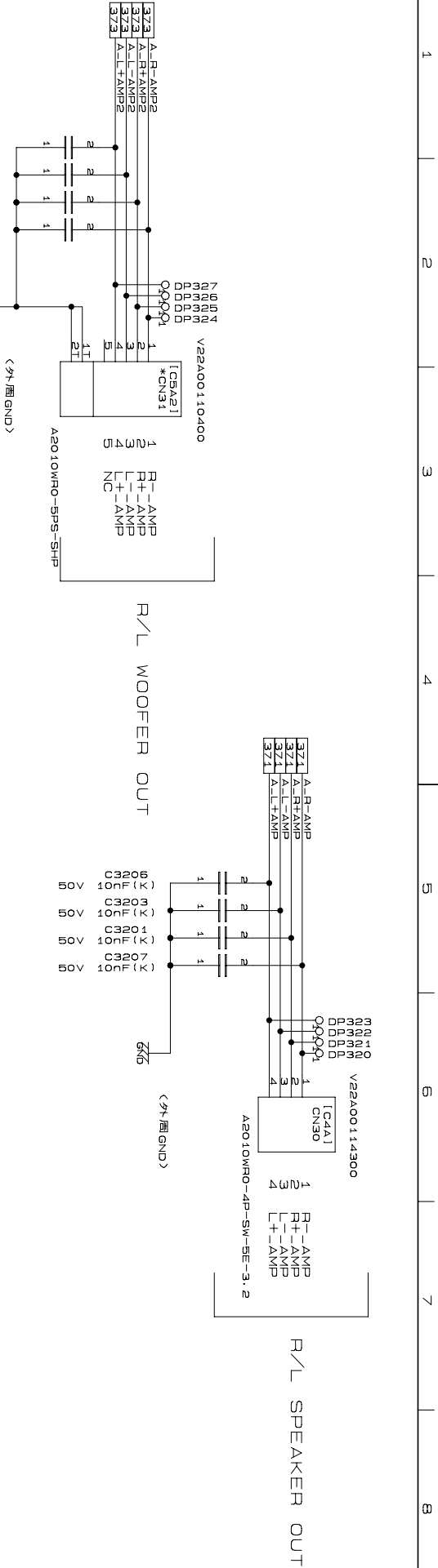
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DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ENDO	MAIN UNIT	AUDIO Linedriver (TVOUT)	312	41	PP	PE0982
2011.01.14	10:27					



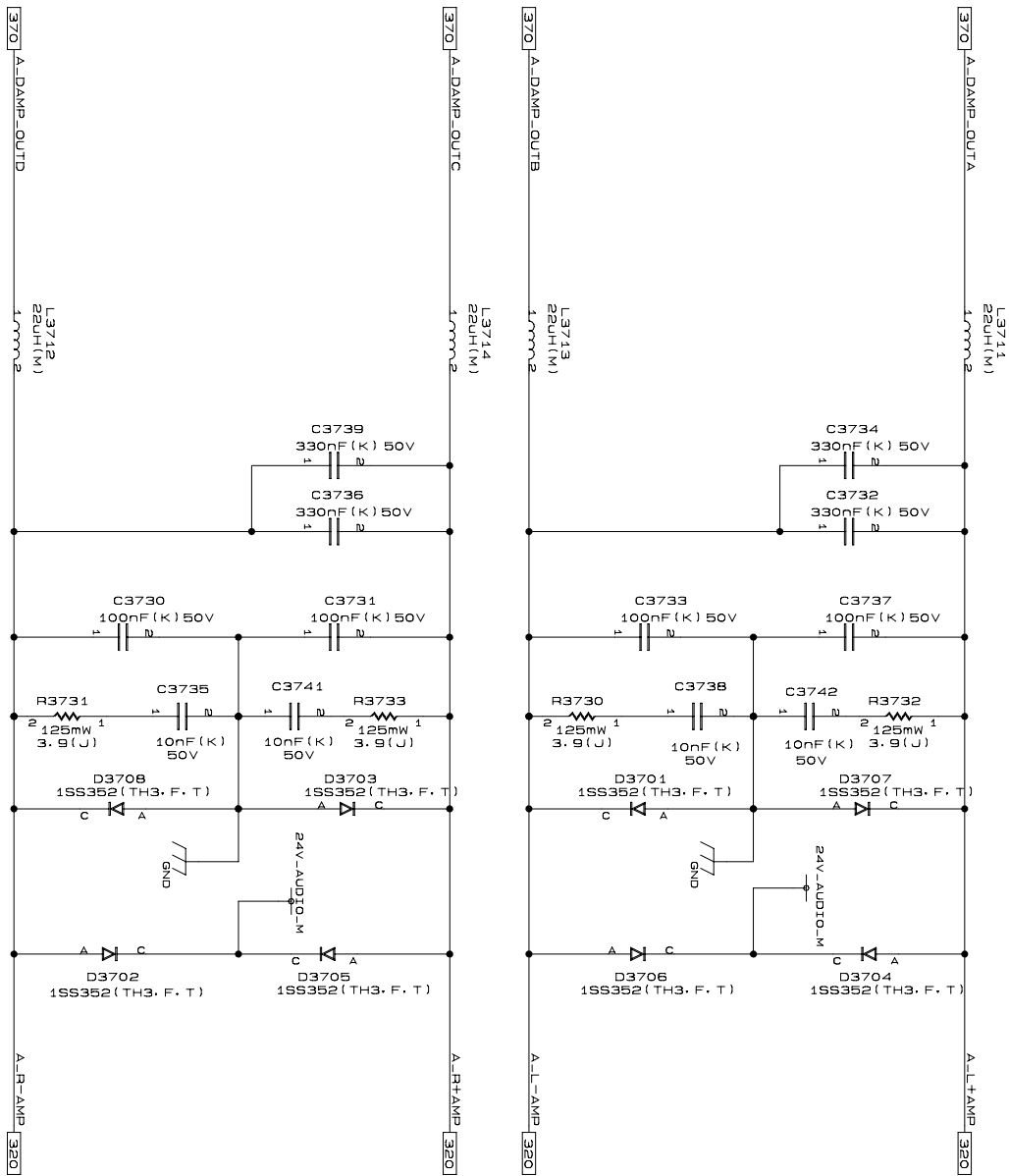
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ENDO	MAIN UNIT	AUDIO CONNECTOR	320	42	PP	PE0982
2011.01.14	10:27					



DRAWING. NO.  
PEO982

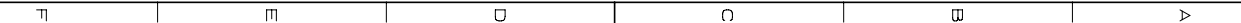
FEOSN

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DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ENDO	MAIN UNIT	AUDIO AMP FILTER	371	45	PP	PE0982

2011.01.14 10:27



V47A00038100  
TAS5708PMPR

0 (TP1) DP303	1	14
FAULT		
PDN		
RESET		
SDA		
SCL		

AUDIO\_D-AMP  
[VLTAS708]  
\*ICS71

U	
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III	
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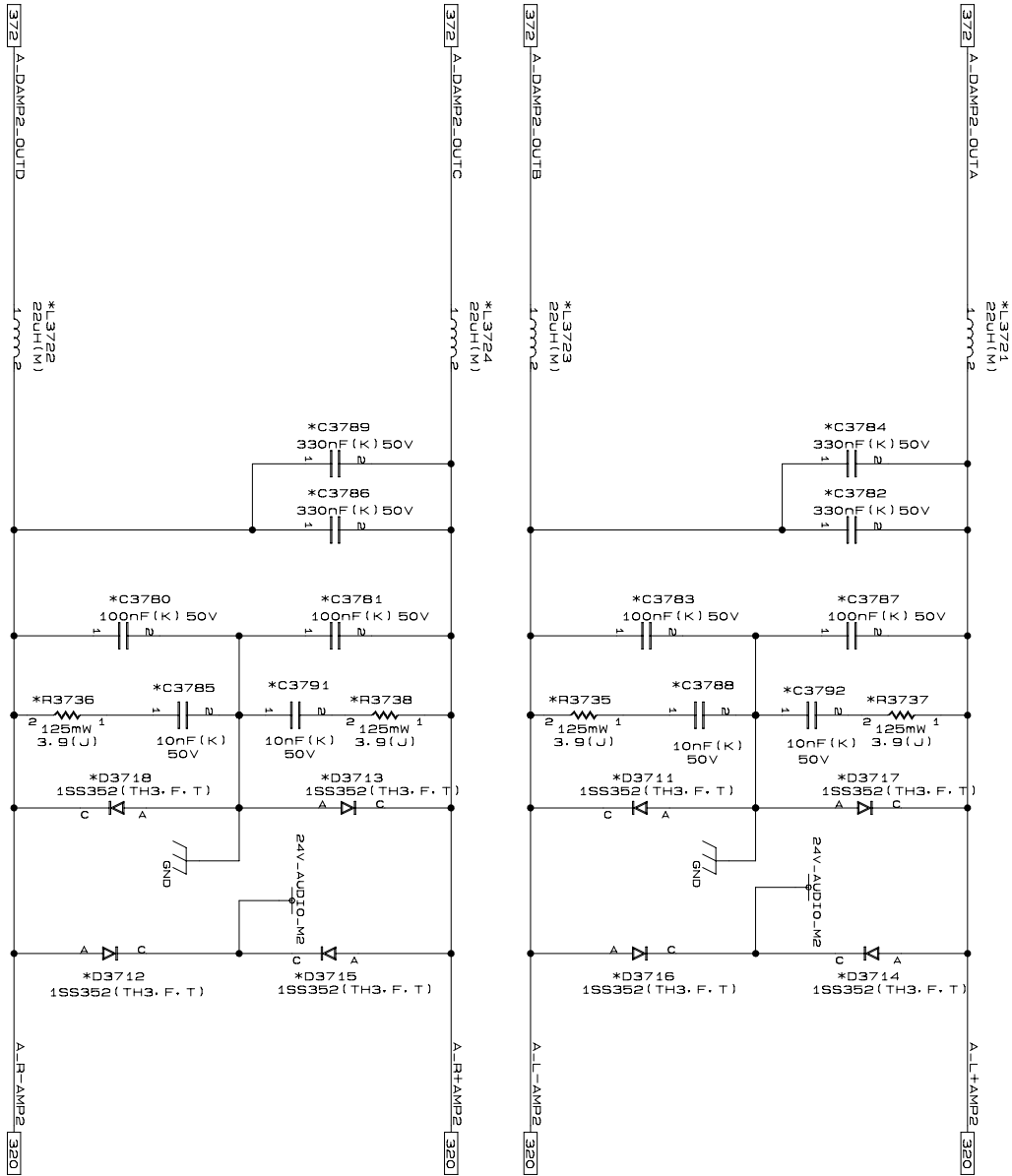
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ING. NO.  
PEO982

TOSHIBA CORPORATION

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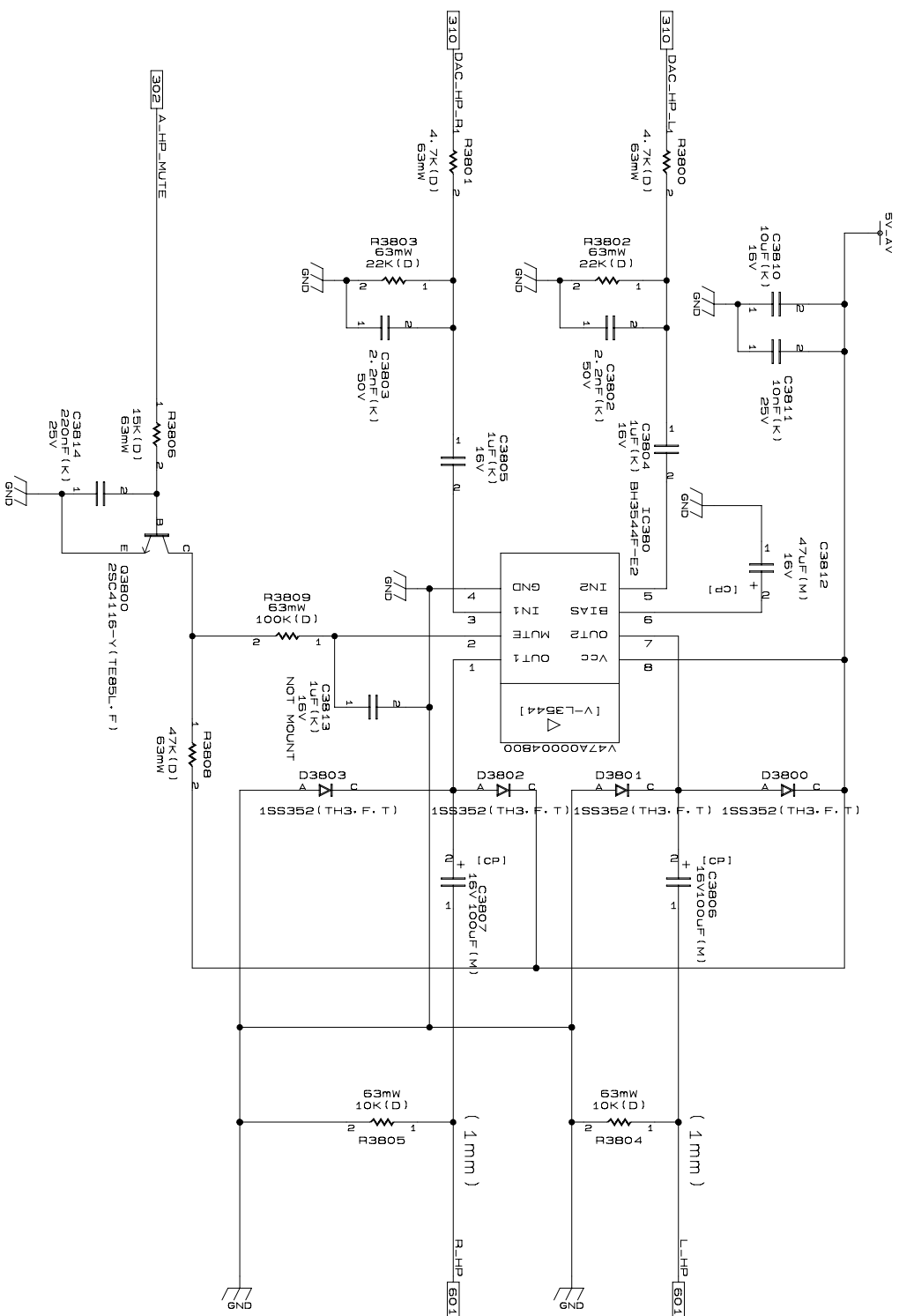


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ENDO	MAIN UNIT	AUDIO AMP FILTER (W0)	373	47	PP	PE0982

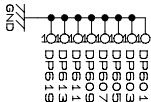
2011.01.14 10:27

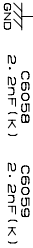
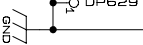
TOSHIBA CORPORATION

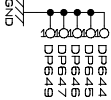


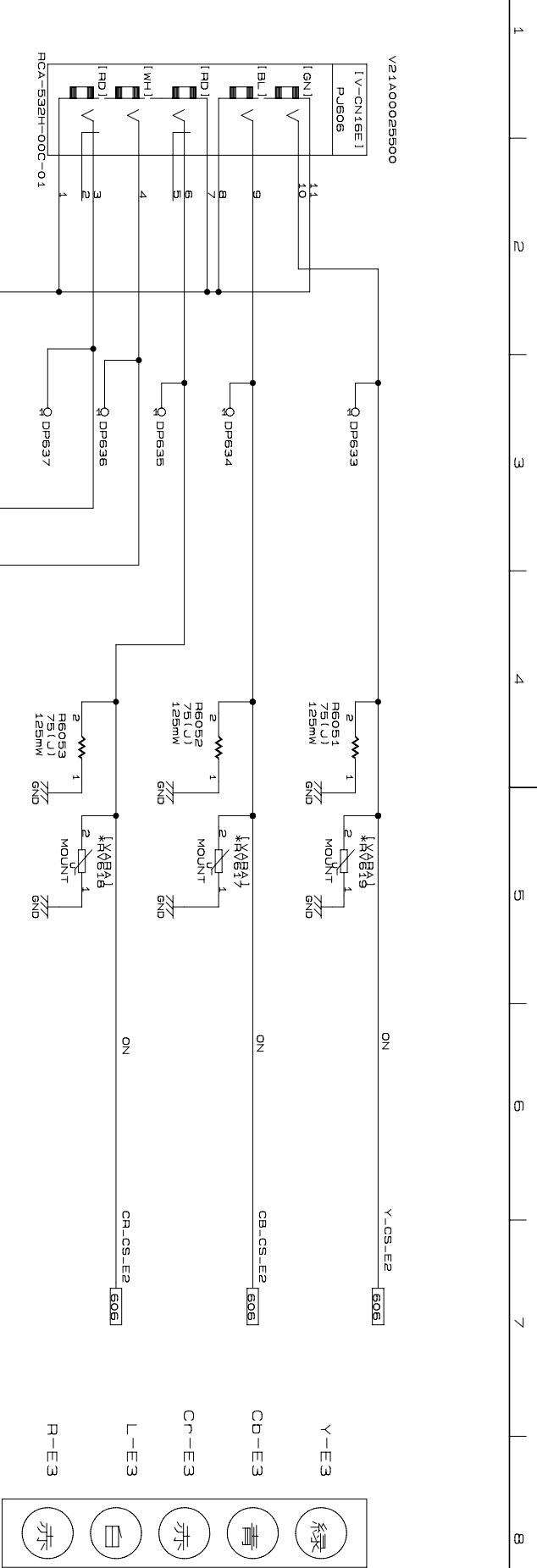


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Endo	MAIN UNIT	Headphone AMP	380	48	PP	PE0982
2011.01.14	10:27	TOSHIBA CORPORATION				

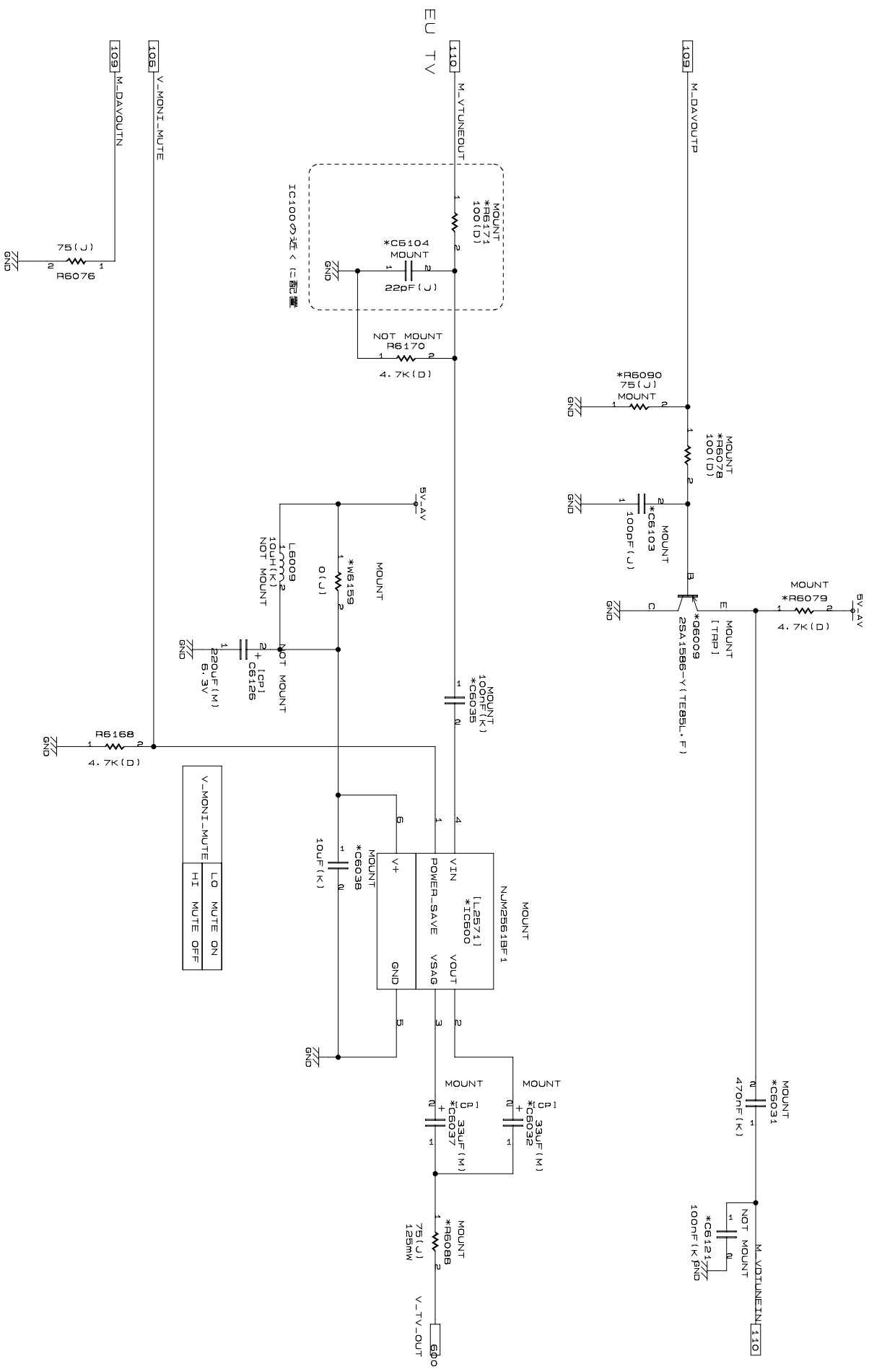
TOSHIBA CORPORATION

TOSHIBA CORPORATION

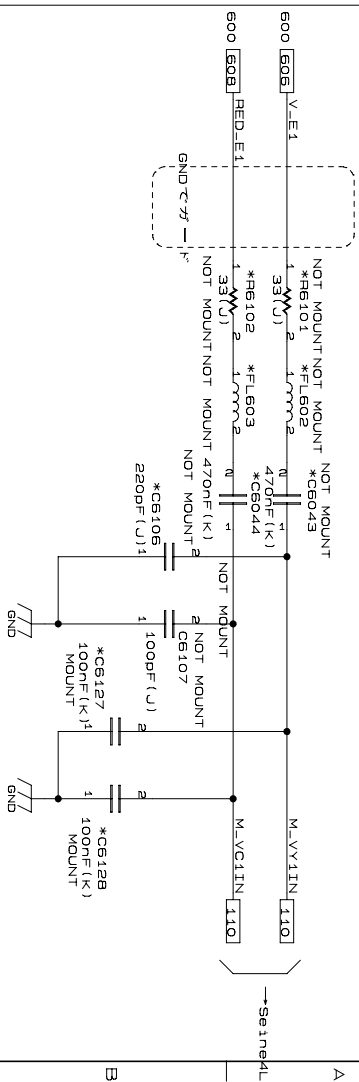
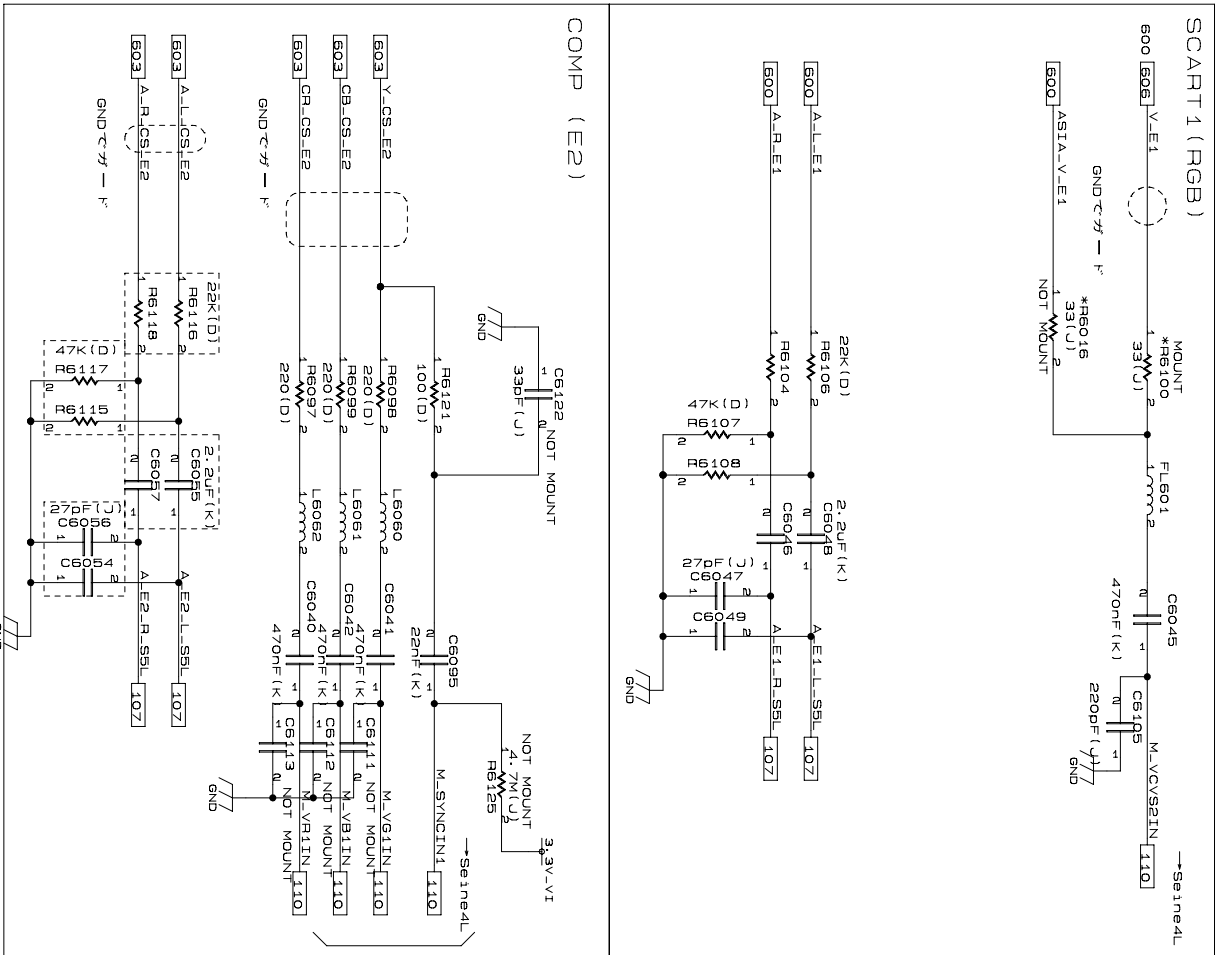
TOSHIBA CORPORATION



DESIGNED BY SHIBATA/ENDO  
TITLE MAIN UNIT  
FUNCTION EURO COMPONENT IN  
SH. NO. 603  
PAGE NO. 52  
REV. MARK PP  
DRAWING. NO. PE0982



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
SHIBATA	MAIN UNIT	EURO TV OUT	605	53	PP	PE0982
2011.01.14	10:27	TOSHIBA CORPORATION				

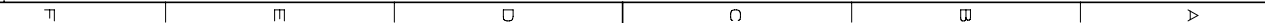


DESIGNED BY SHIBATA/Endo	TITLE MAIN UNIT	FUNCTION AV INPUT 1	SH. NO. 606	PAGE NO. 54	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14 10:27			TOSHIBA CORPORATION			



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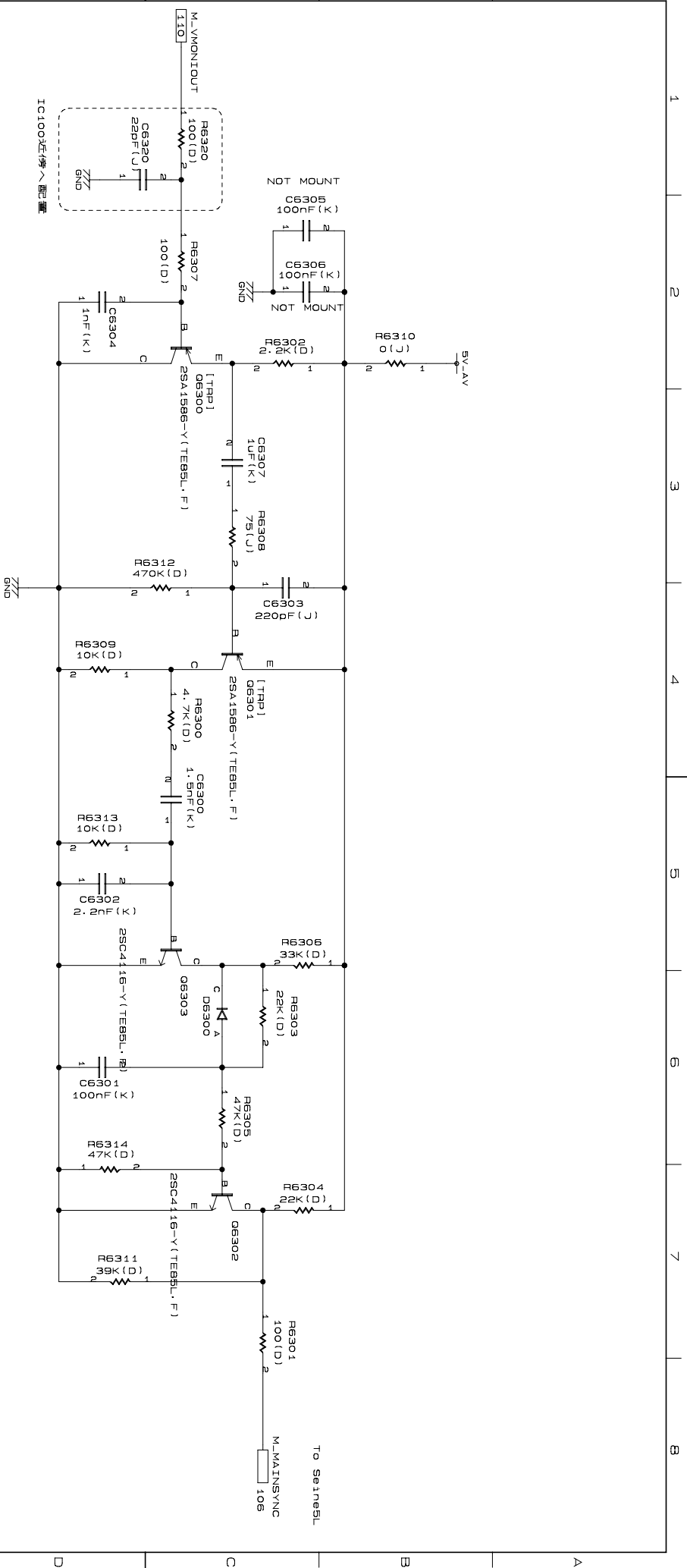


DRAWING. NO.

FEOSUN

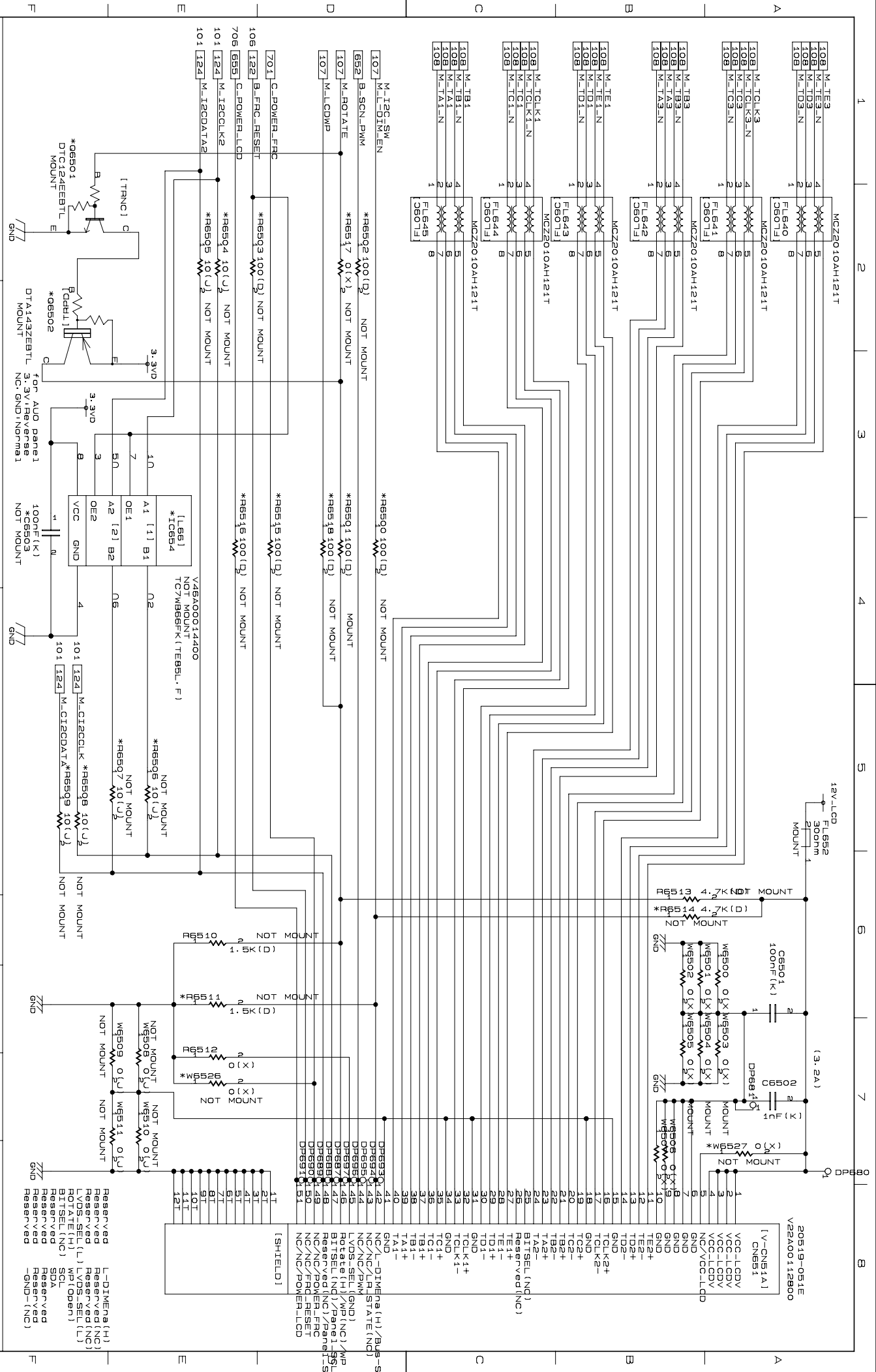
10:27

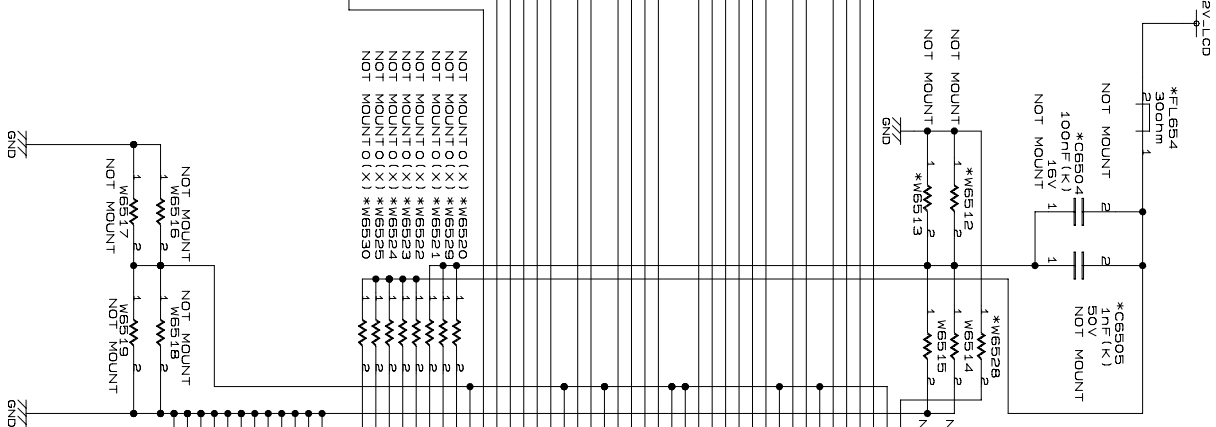
TOSHIBA CORPORATION



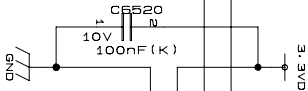
IC100近隣へ配置

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
INOSE	MAIN UNIT	SYNC DETECT	630	57	PP	PE0982

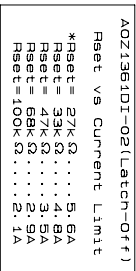


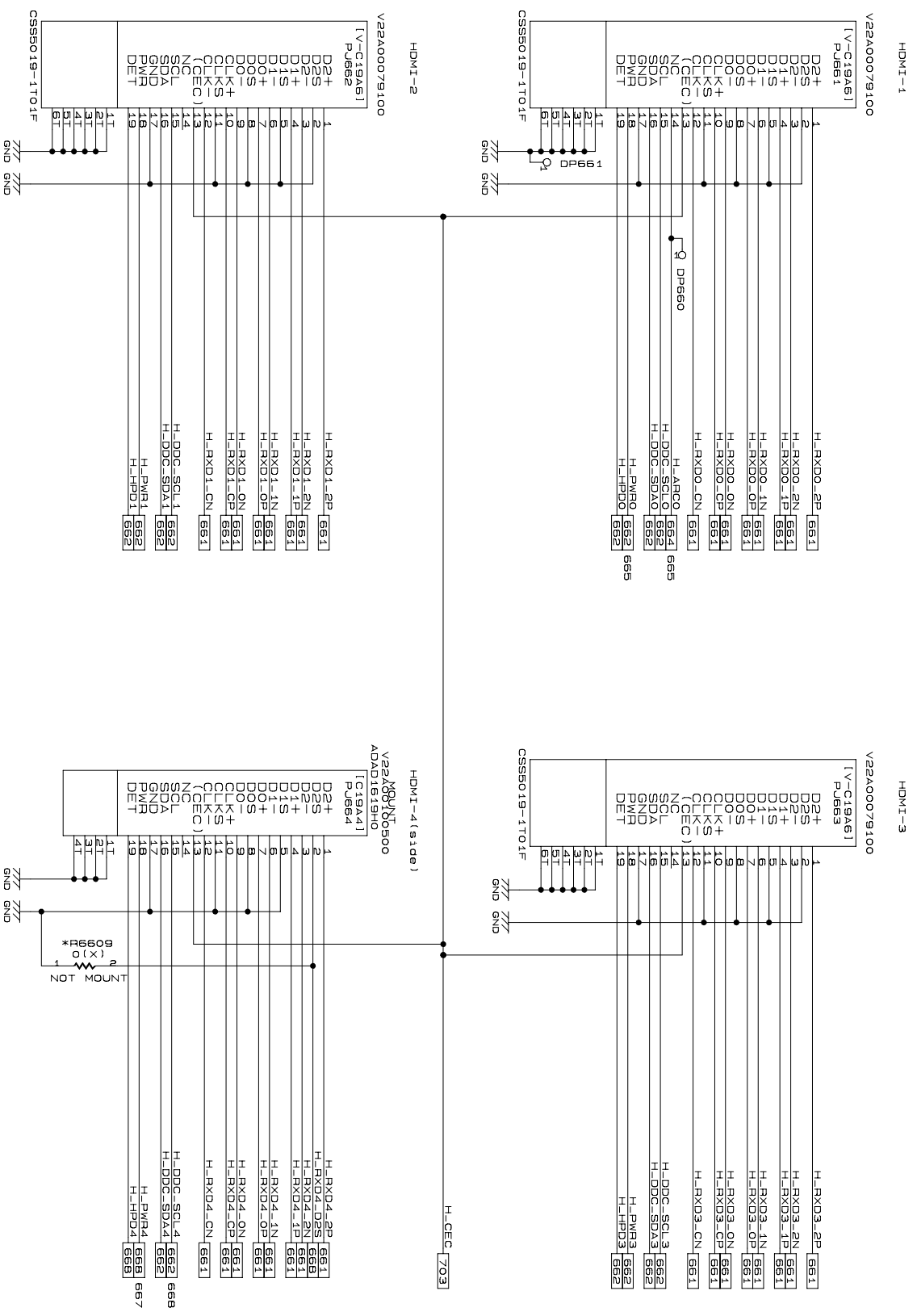


DESIGNED BY	2011/01/05	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KOMIYA		MAIN UNIT	PANEL I/F (LVDS OUT)	651	59	PP	PE0982
2011.01.14	10:27	TOSHIBA CORPORATION					



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KOMIYA	MAIN UNIT	PANEL I/F Dimmer ctr1	652	60	PP	PE0982
2011.01.14	10:27	TOSHIBA CORPORATION				

TOSHIBA CORPORATION



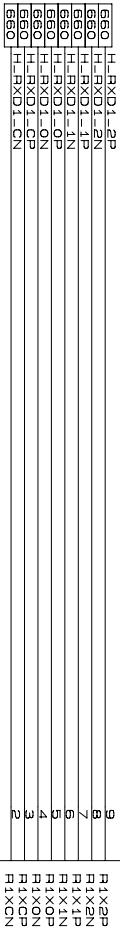
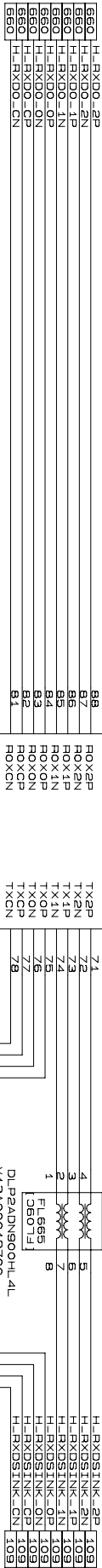
DESIGNED BY ANDO	TITLE MAIN UNIT	FUNCTION HDMI connector	SH. NO. 660	PAGE NO. 62	REV. MARK PP	DRAWING. NO. PEO982
2011.01.14 10:27		TOSHIBA CORPORATION				

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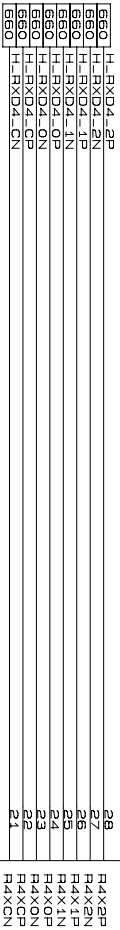
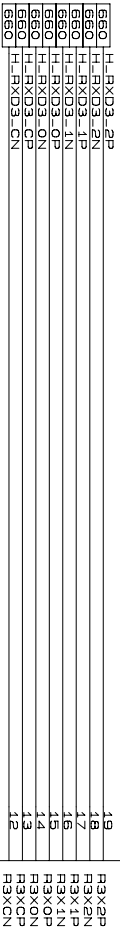
V45A00060900  
S1193B1A

HDMI-Switch 1/4  
[V-U93B1]  
1C860

to SeineSL



DLP2ADN900HL4L  
V13A00019700



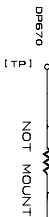
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
ANDO	MAIN UNIT	HDMI SW (1/4)	661	63	PP	PE0982

2011.01.14 10:27

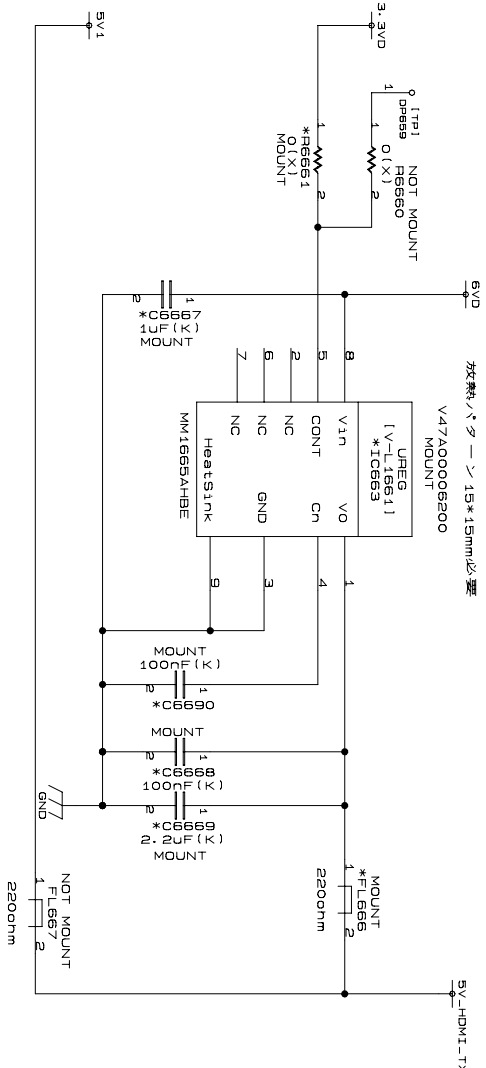
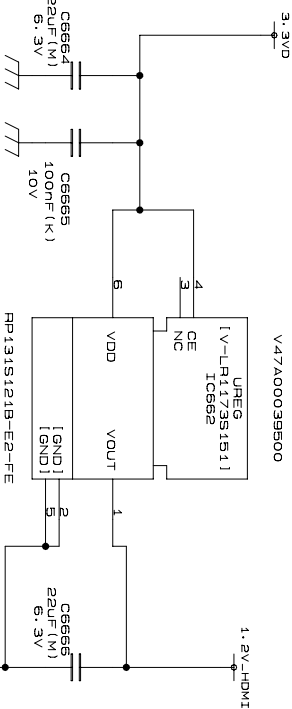
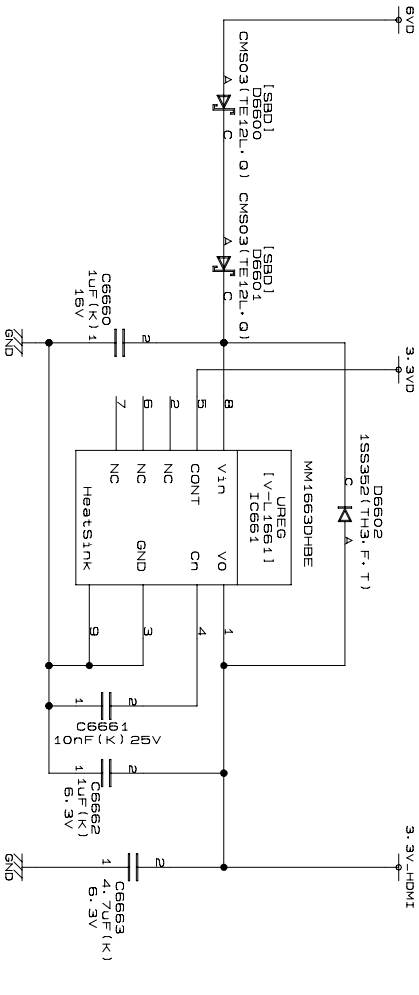




TOSHIBA CORPORATION

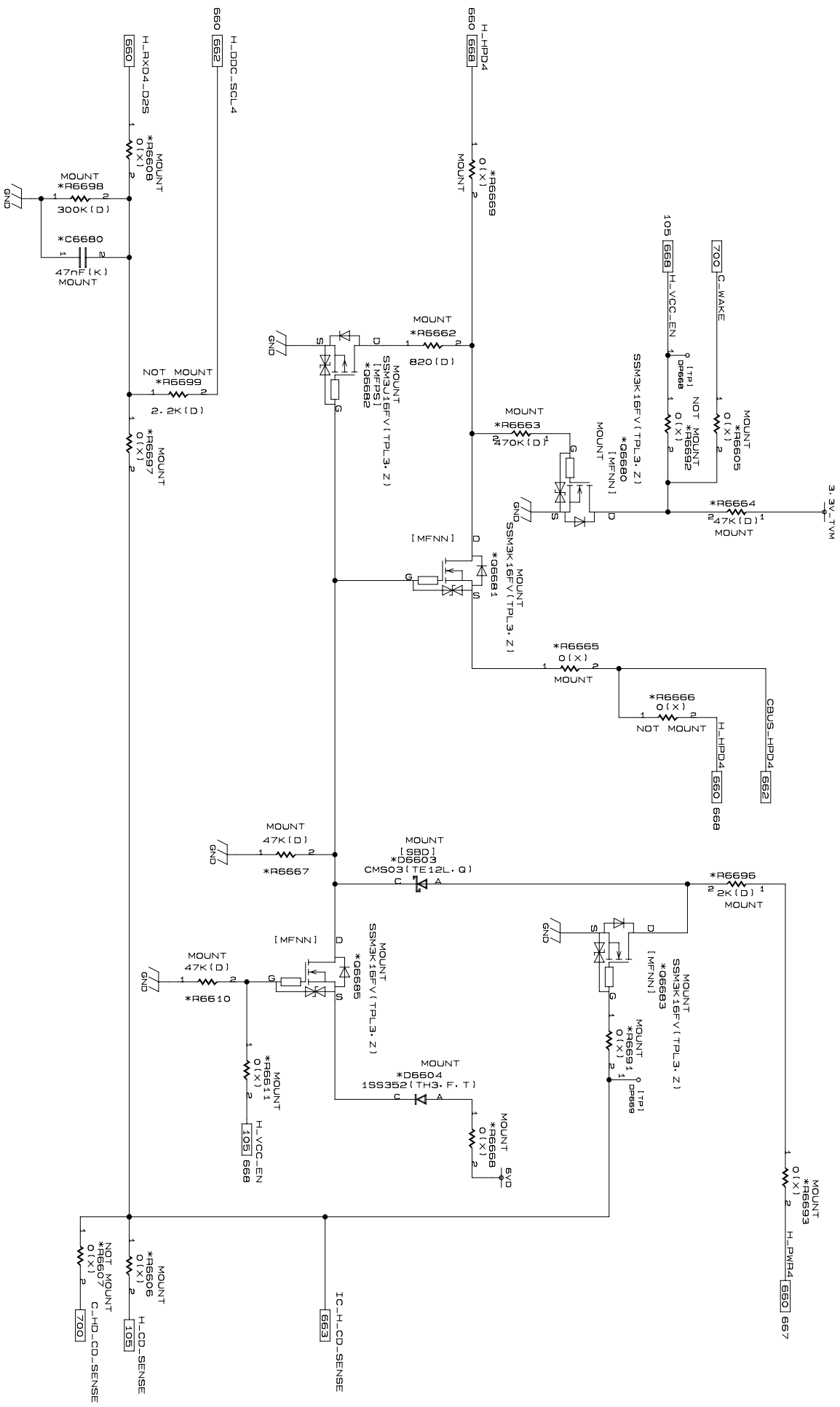
2011.01.14 10:27

2011.01.14 10:27



DESIGNED BY ANDO	2011/01/05	TITLE MAIN UNIT	FUNCTION HDMI POWER	SH. NO. 666	PAGE NO. 68	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14	10:27						TOSHIBA CORPORATION

TOSHIBA CORPORATION

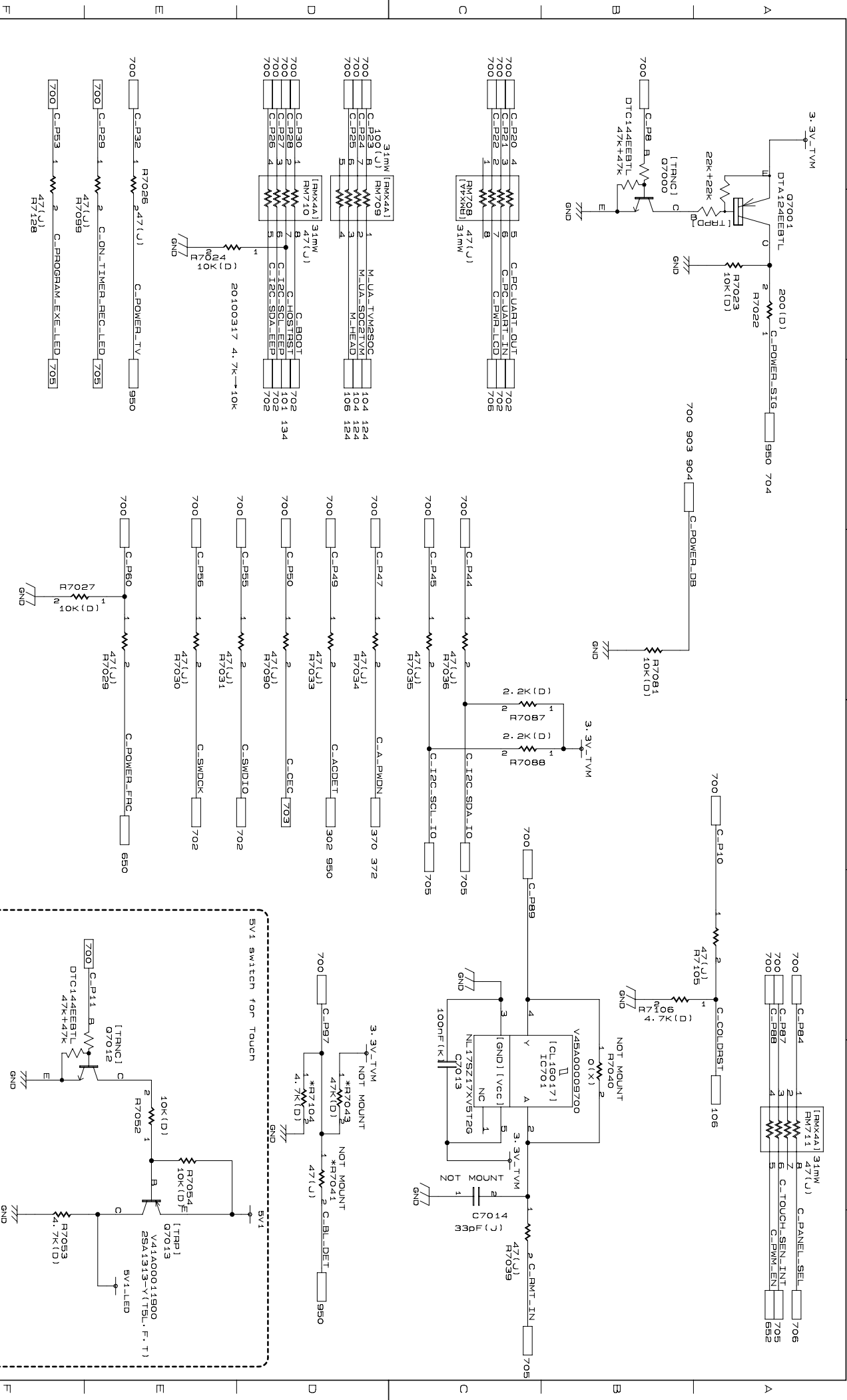


DESIGNED BY ANDO	TITLE MAIN UNIT	FUNCTION HDMI CBUS HPD	SH. NO. 668	PAGE NO. 70	REV. MARK PP	DRAWING. NO. PEO982
2011.01.14 10:27		TOSHIBA CORPORATION				



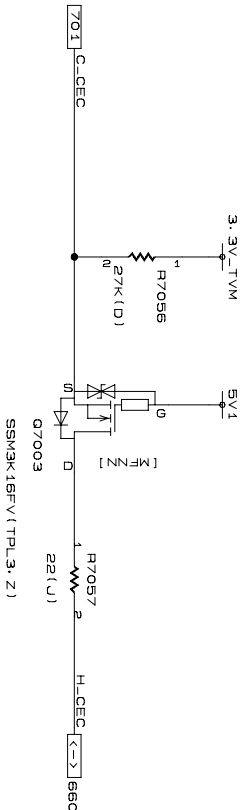
TOSHIBA CORPORATION





TOSHIBA CORPORATION

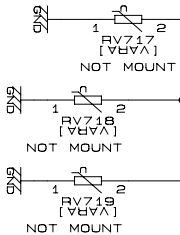
12345678



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KONTANI	MAIN UNIT	TVM UART/CEC	703	74	PP	PE0982

2011.01.14 10:27

TOSHIBA CORPORATION

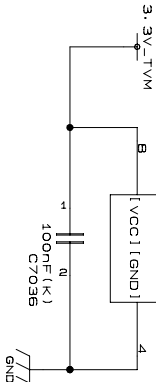


## A1257WR0-15PS

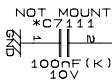
PCB layout for the power supply section. It shows a 5V regulator (7805) and a 27V regulator (7827) with their respective decoupling capacitors (C7031, C7032) and ground connections. The layout includes labels for component placement (MOUNT, NOT MOUNT) and pin numbers (1, 2).

	W	2	C	OP	IN	ZOO
O (X)						
R7092	350	750	750	765	770	775 372 101 124 707
1	W	2				
O (X)						
NOT MOUNT						
R7093						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
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O (X)						
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1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
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O (X)						
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O (X)						
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1	W	2				
O (X)						
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NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						
NOT MOUNT						
1	W	2				
O (X)						

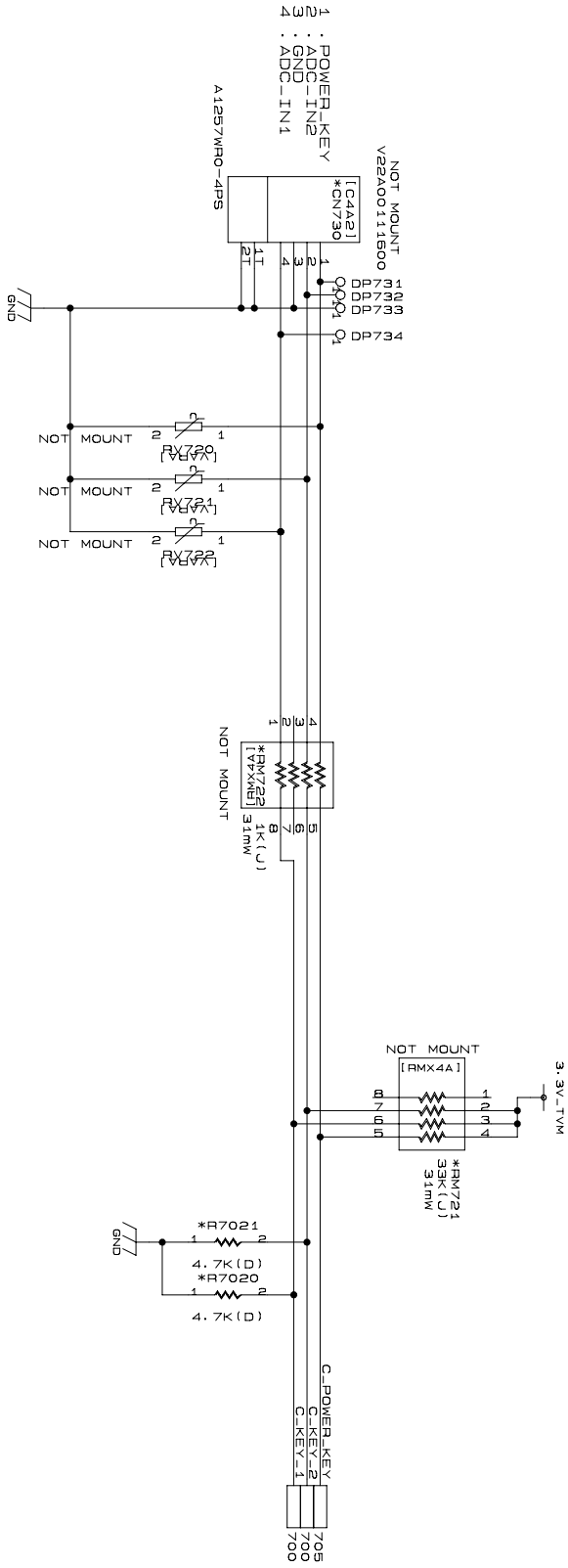
DESIGNED BY KONTANI	TITLE MAIN UNIT	FUNCTION TVM LED/RMT/OP/IR	SH. NO. 705	PAGE NO. 76	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14 10:27			TOSHIBA CORPORATION			



DESIGNED BY KONTANI	TITLE MAIN UNIT	FUNCTION TYM PANEL I/F	SH. NO. 706	PAGE NO. 77	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14 10:27		TOSHIBA CORPORATION				

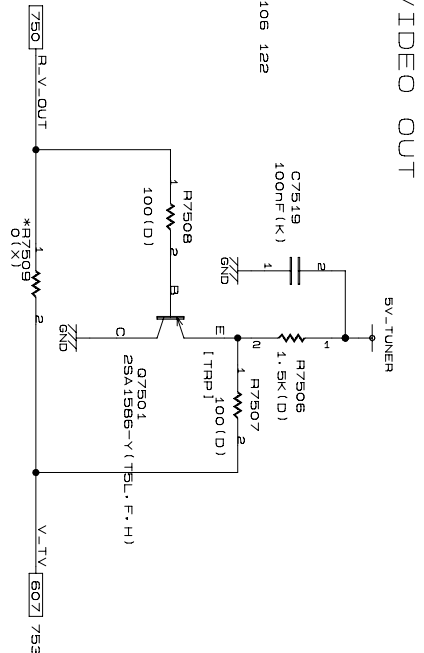
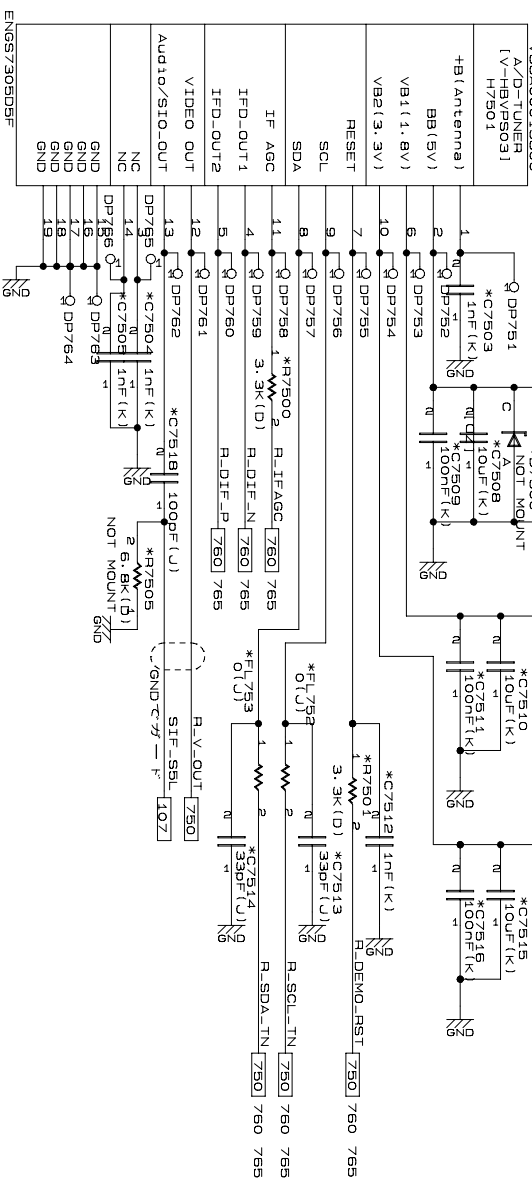


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KONTANI	MAIN UNIT	TVM PIR	707	78	PP	PE0982
2011.01.14	10:27	TOSHIBA CORPORATION				

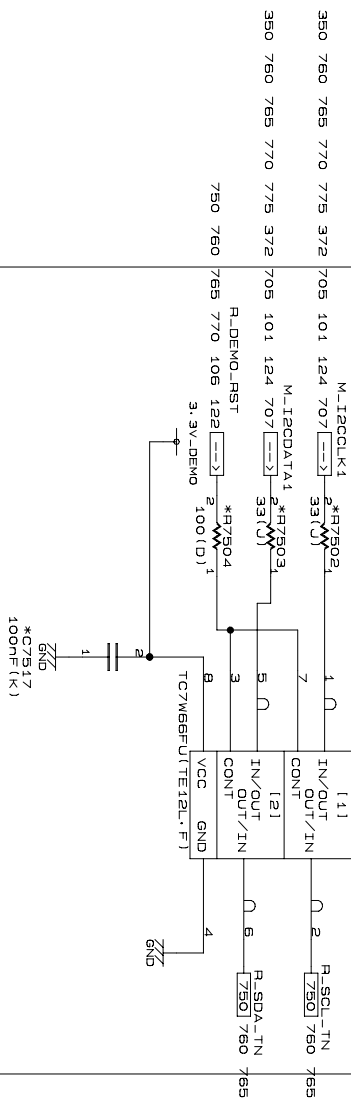


DESIGNED BY KONTANI	TITLE MAIN UNIT	FUNCTION TYM (KEY CONNECTOR)	SH. NO. 708	PAGE NO. 79	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14	10:27					TOSHIBA CORPORATION

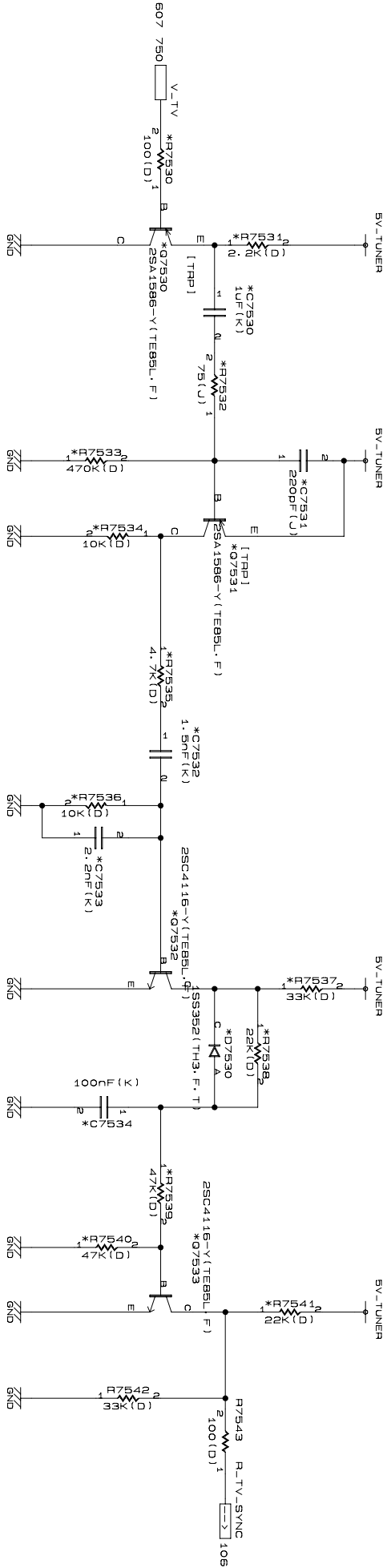




I2C for Analog model v45A00010300



12345678

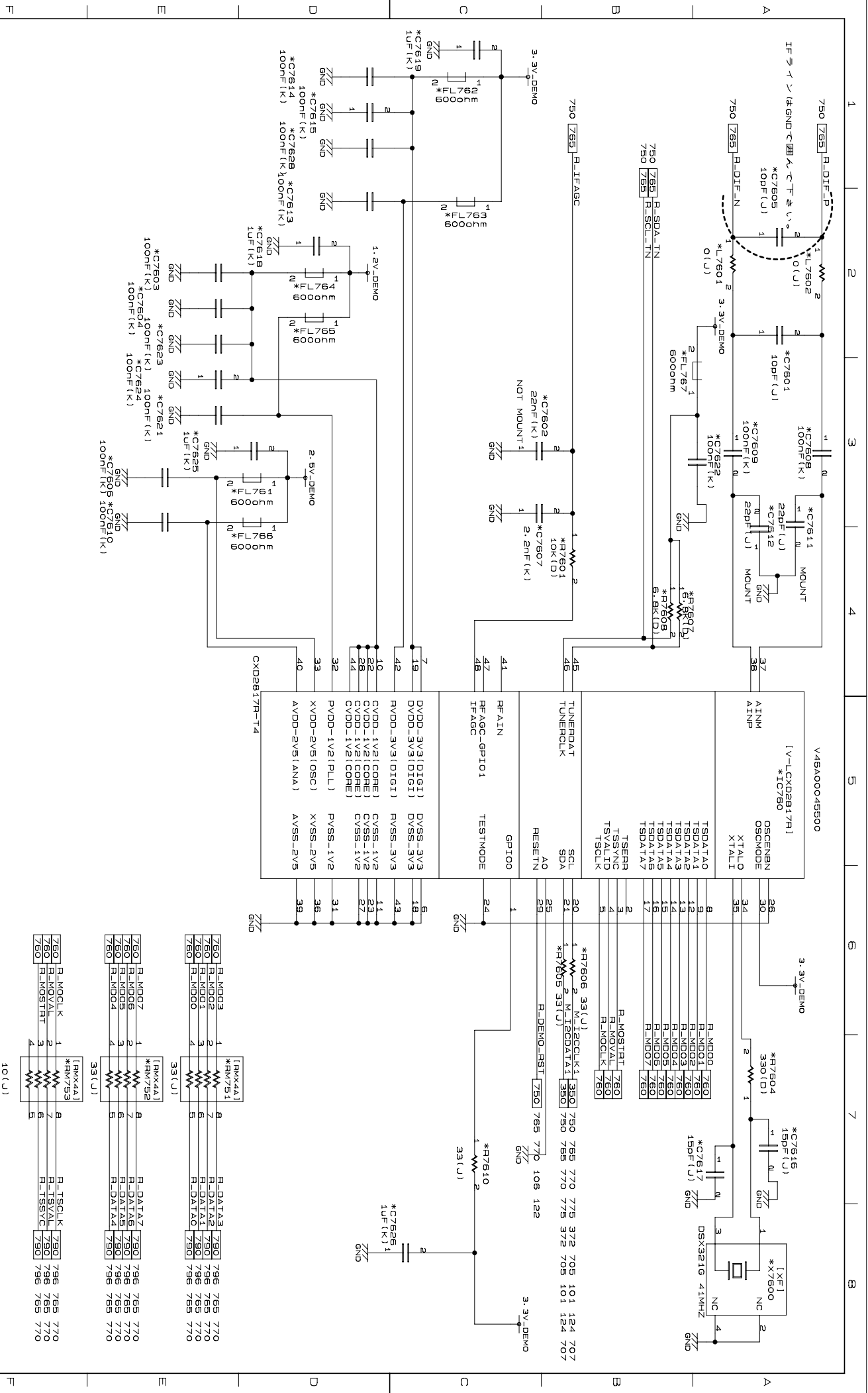


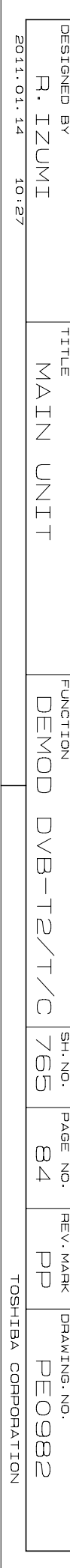
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R. IZUMI	MAIN UNIT	SYNC-DET (TV-OUT)	753	81	PP	PE0982

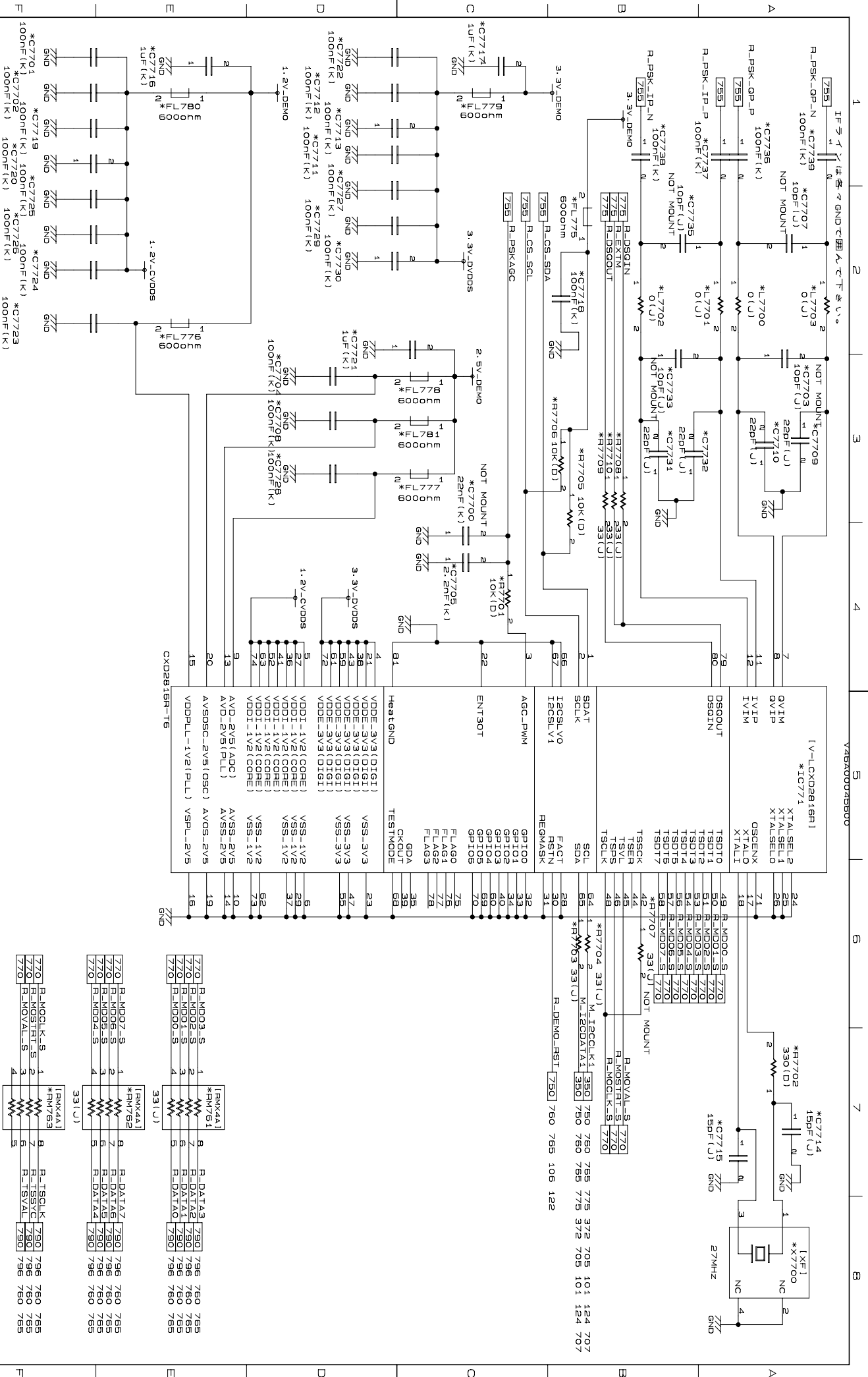
2011.01.14 10:27

TOSHIBA CORPORATION









10:27

# MAIN UNIT

DEMOD DVB-S2/S

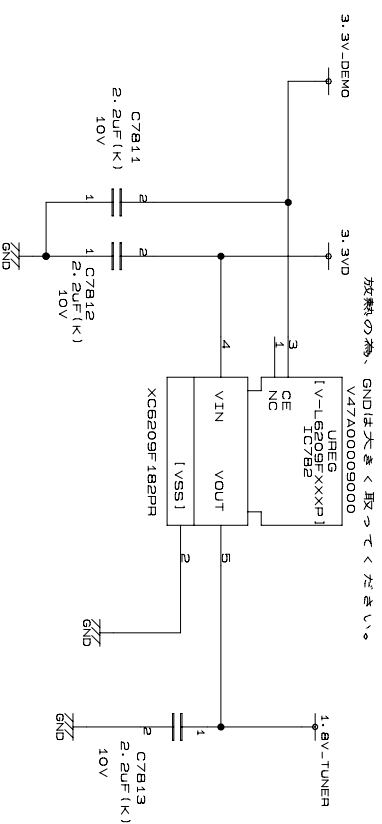
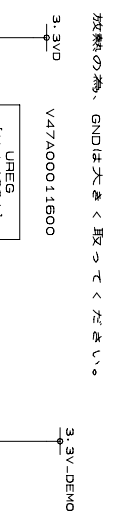
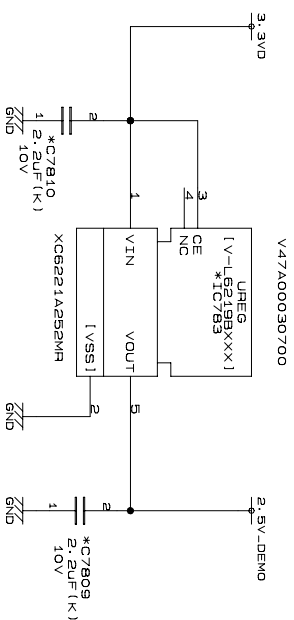
SH. NO.  
770

07 08

• MARR  
PDP

ING. No.  
PEO982

2011.01.14 10:27

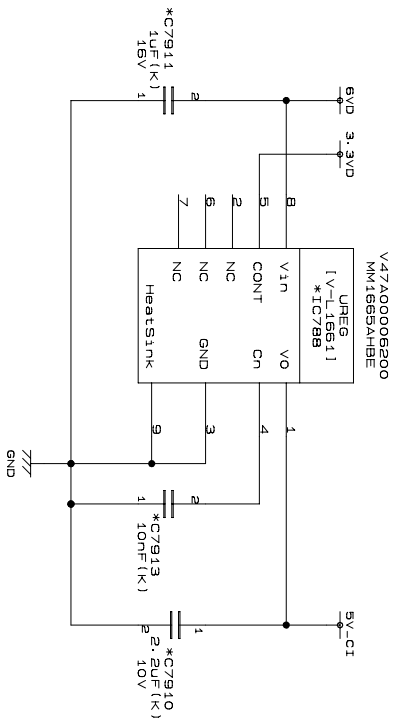


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R. IZUMI	MAIN UNIT	TUNER POWER	780	87	PP	PE0982
2011.01.14	10:27	TOSHIBA CORPORATION				

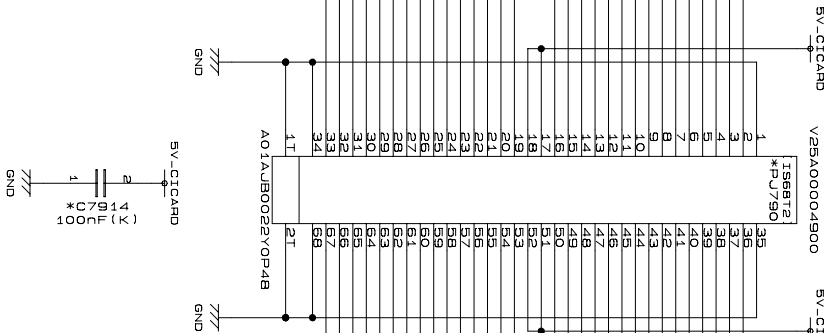
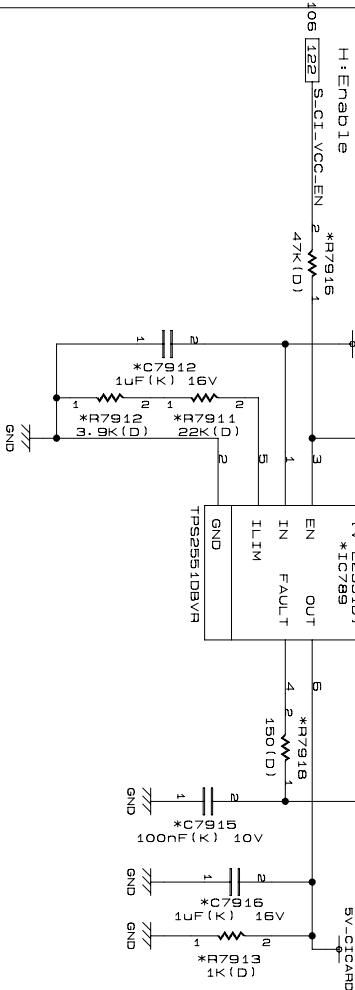
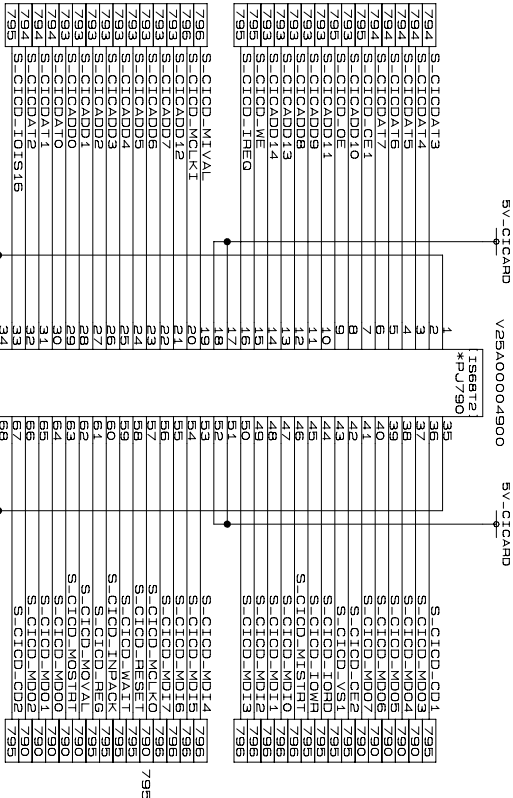


DRAWING NO. PEO982

CI POWER



CI CONNECTOR



DESIGNED BY  
T. SHIMIZU

TITLE  
MAIN UNIT

FUNCTION  
CI POWER/CONNECTOR

SH. NO.  
791

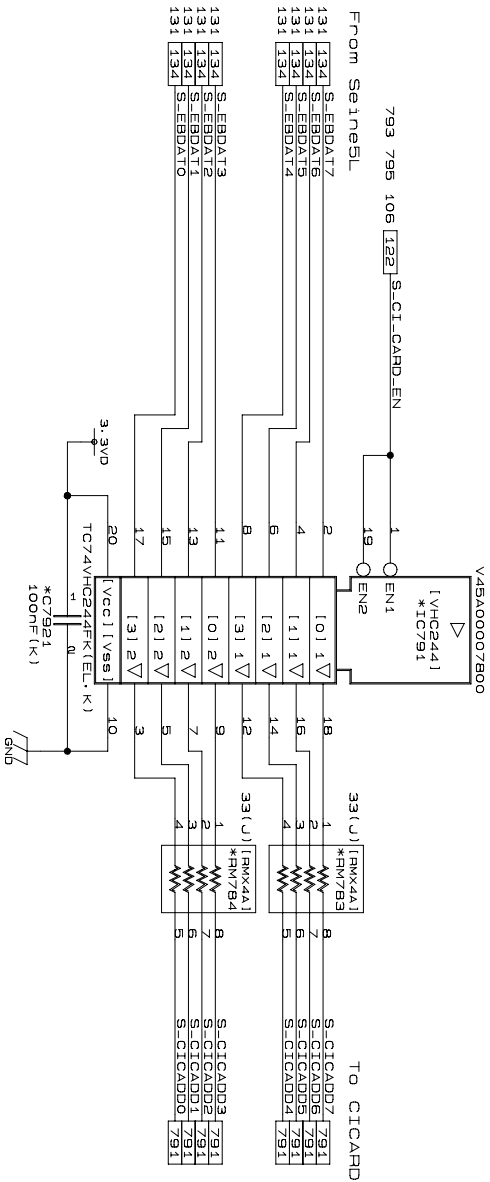
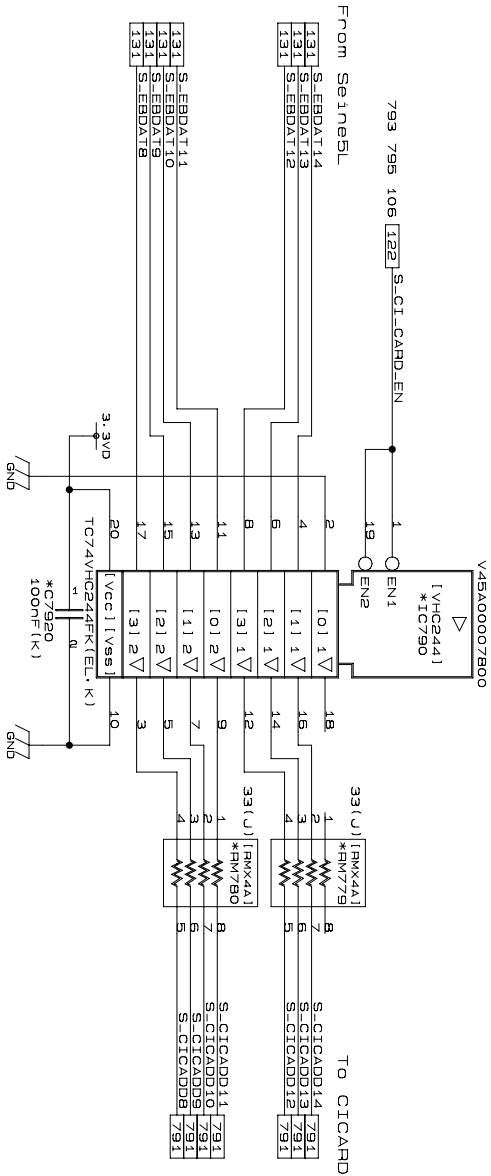
PAGE NO.  
89

REV. MARK  
PP

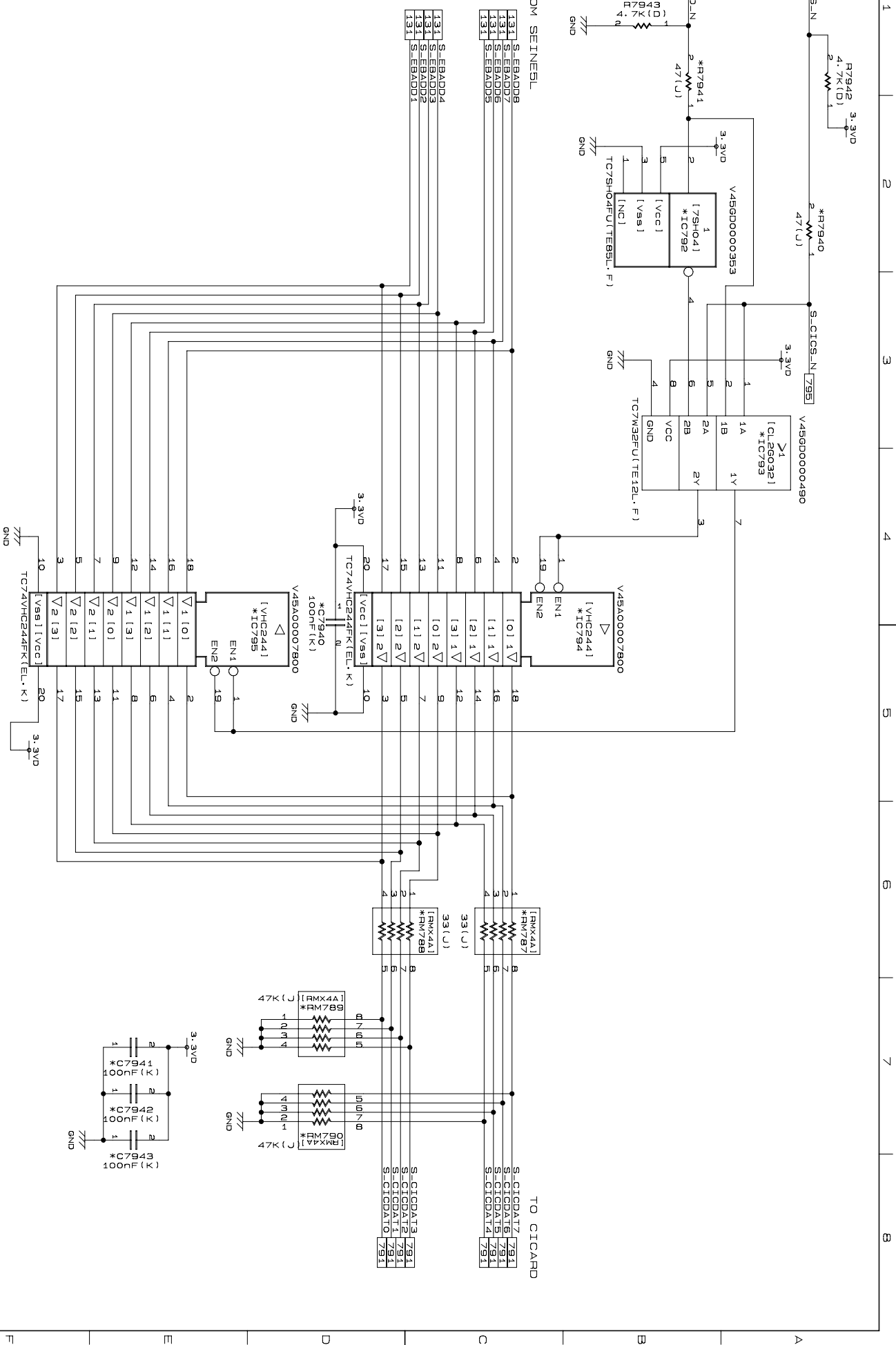
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2011.01.14 10:27

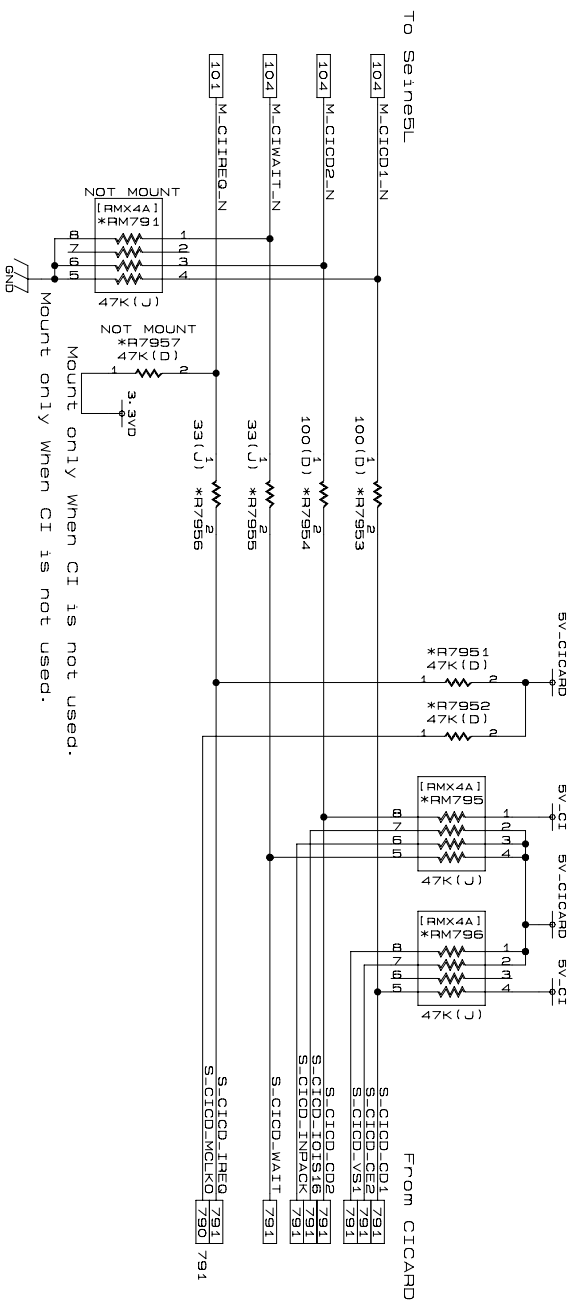
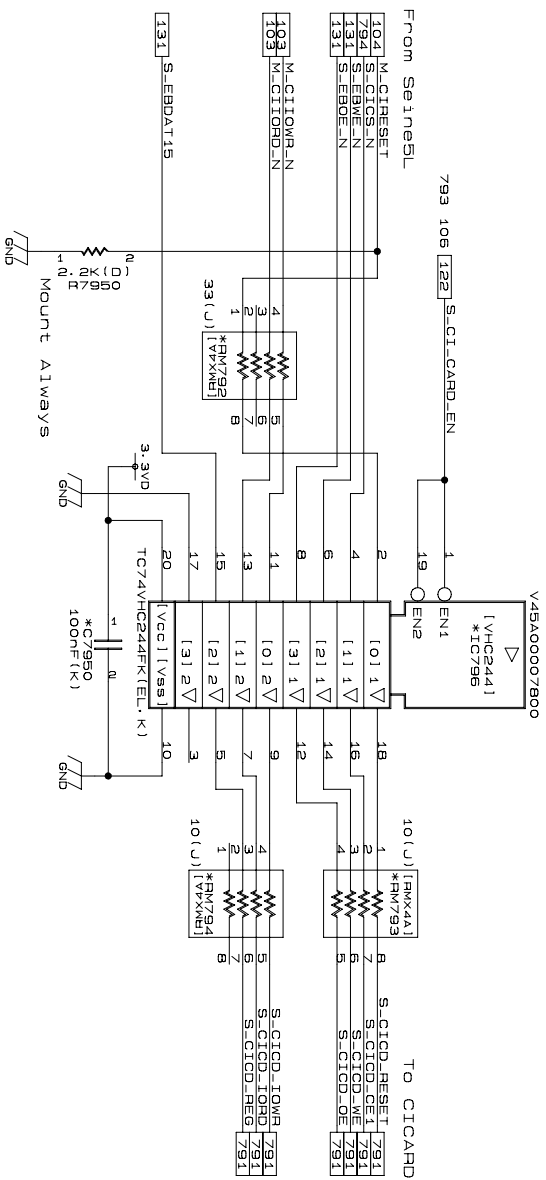
TOSHIBA CORPORATION



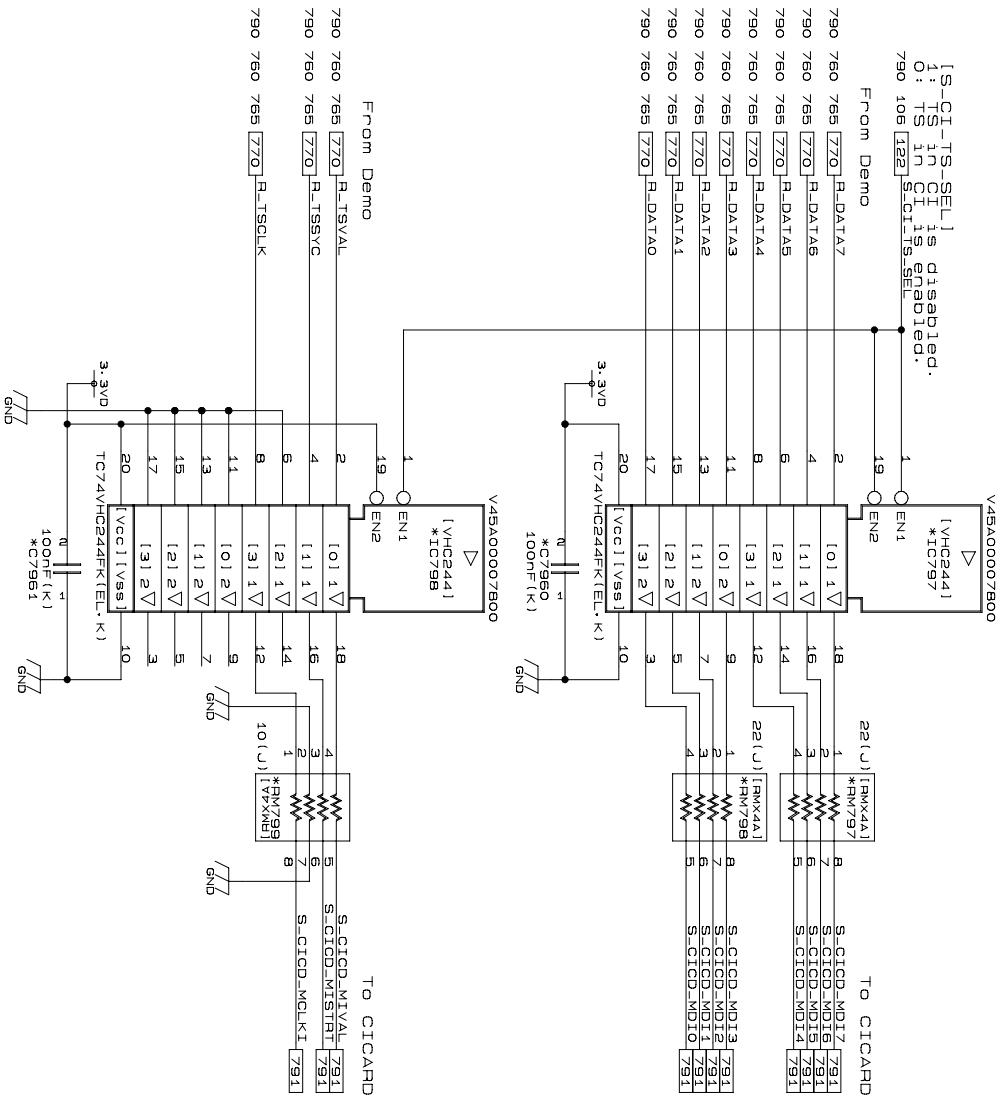
DESIGNED BY T. SHIMIZU		TITLE MAIN UNIT		FUNCTION CI ADDRESS BUFFER		SH. NO. 793	PAGE NO. 90	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14		10:28				TOSHIBA CORPORATION			



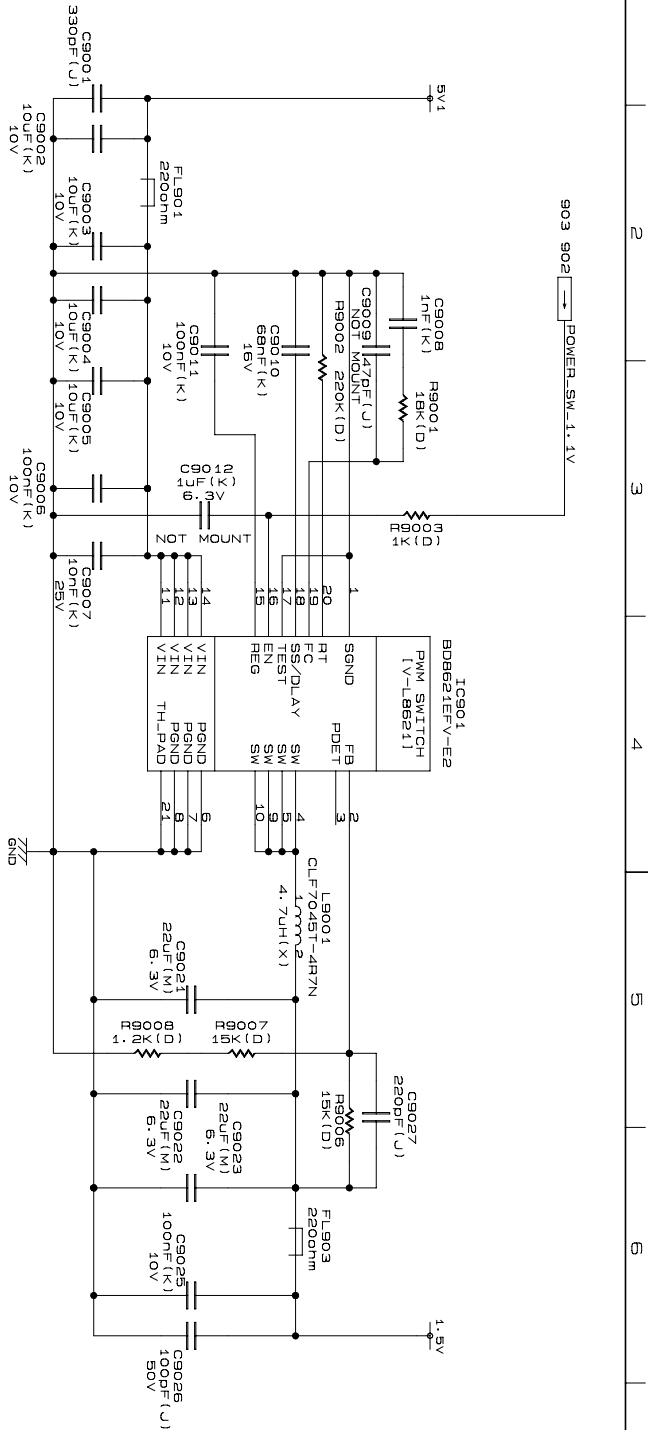
TOSHIBA CORPORATION



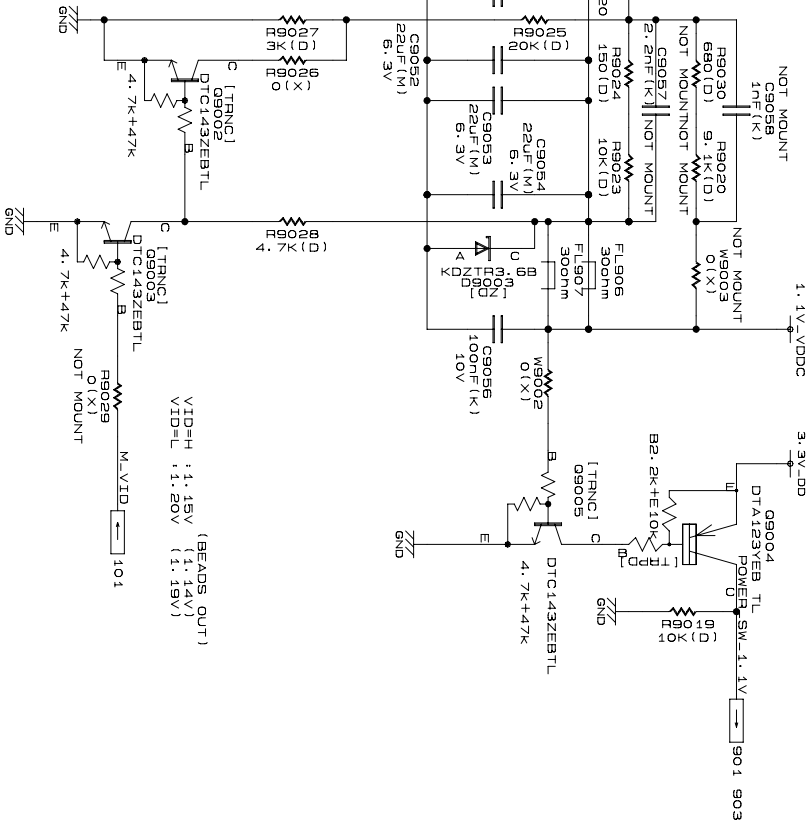
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU	MAIN UNIT	CI CONTROL BUFFER	795	92	PP	PE0982
2011.01.14	10:28					



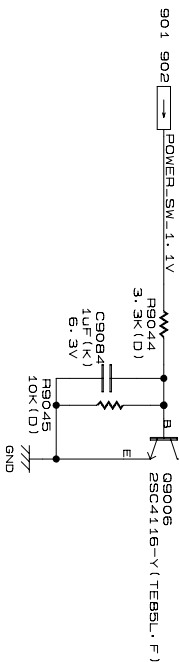
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. SHIMIZU	MAIN UNIT	CI TS OUTPUT BUFFER	796	93	PP	PE0982
2011.01.14	10:28					

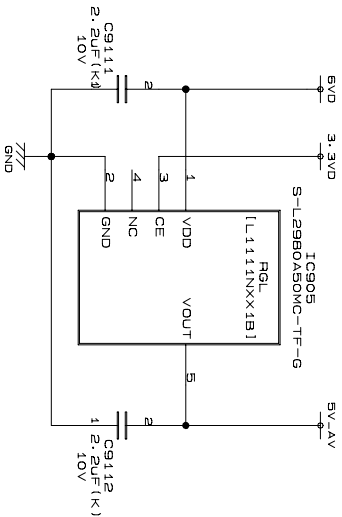
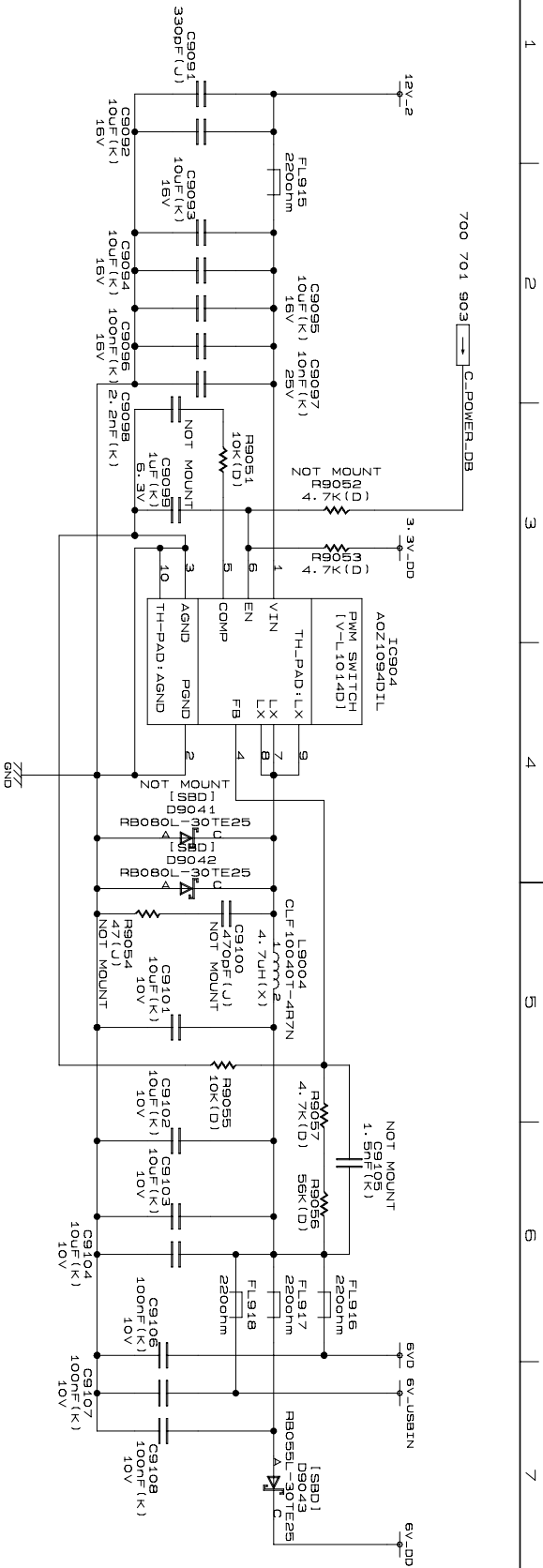


DESIGNED BY		TITLE		FUNCTION		SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Nagaoka/Nakano		MAIN UNIT		POWER 1.5V		901	94	PP	PE0982
2011.01.14		10:28				TOSHIBA CORPORATION			

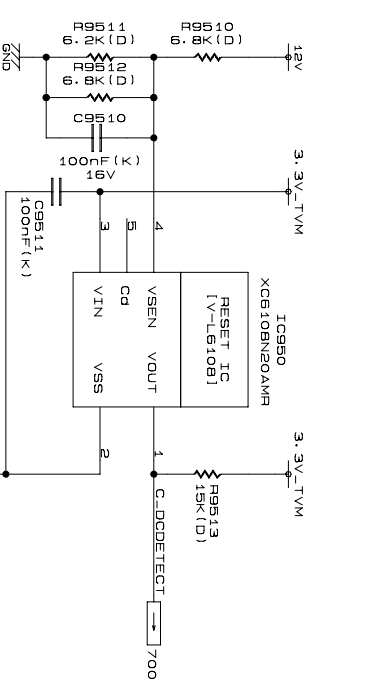
2011.01.14 10:28



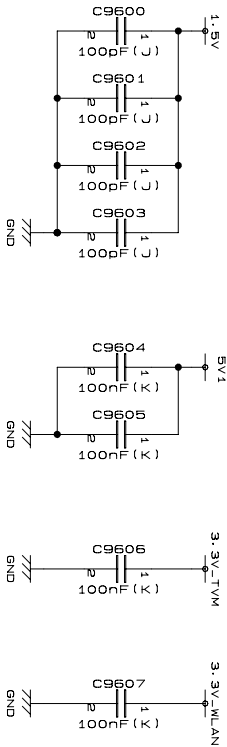




DESIGNED BY	2011/01/05	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Nagaoka/Nakano		MAIN UNIT	POWER 6V/5V	904	97	PP	PE0982
2011.01.14	10:28						



ATTION



DESIGNED BY T. SHIMIZU		TITLE MAIN UNIT		FUNCTION EMC CONTROL		SH. NO. 960	PAGE NO. 99	REV. MARK PP	DRAWING. NO. PE0982
2011.01.14		10:28		TOSHIBA CORPORATION					