

RESET

RESET

+5V_STB

R596 100KΩ

D110 BAV99

C573 2.2μF

R597 10KΩ

Q125 MMBT3906

R1668 1KΩ

R598 100KΩ

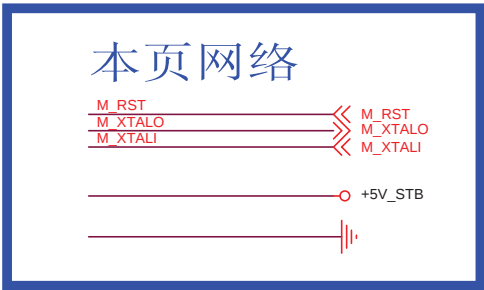
C575 1nF

CE11 NC/100μF

C574 10μF

M_RST

根据实际电源上电时序调整



Crystal

NOTE: 晶体频偏需要小于30PPM

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Size A	Document Number Other	Rev 1.0	
Date:	Wednesday, January 15, 2014	Sheet	1 of 24

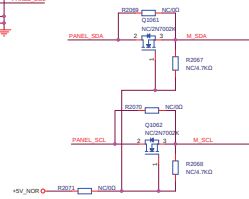
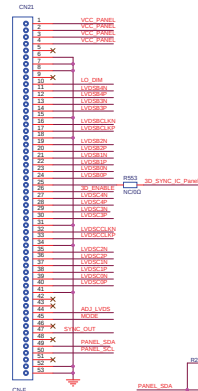
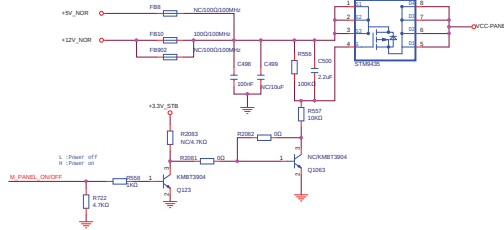
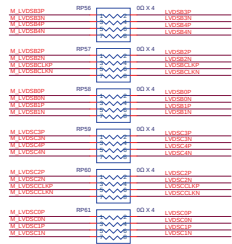
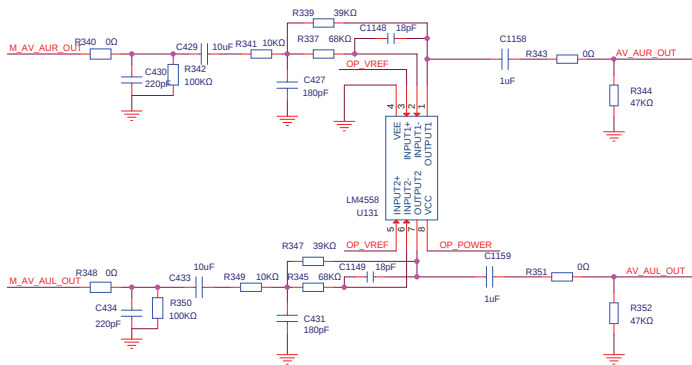


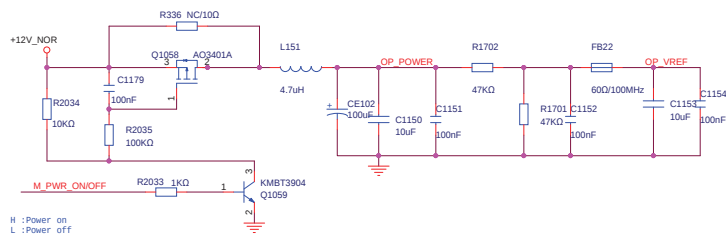
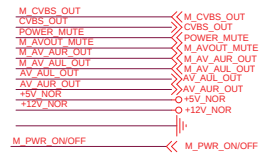
Diagram showing connections for pins 1 through 10:

- Pin 1: M_MODE to M_MODE
- Pin 2: M_LO_DIM to M_LO_DIM
- Pin 3: M_3D_ENABLE to M_3D_ENABLE
- Pin 4: M_PANEL_ON/OFF to M_PANEL_ON/OFF
- Pin 5: M_SDA to M_SDA
- Pin 6: M_SCL to M_SCL
- Pin 7: BL_A03_LVDS to BL_A03_LVDS
- Pin 8: 3D_LR_SYNC_OUT to 3D_LR_SYNC_OUT
- Pin 9: 3D_SYNC_IC_Panell to 3D_SYNC_IC_Panell
- Pin 10: +5V_NDR
- Pin 11: +12V_NDR
- Pin 12: +3.3V_NDR

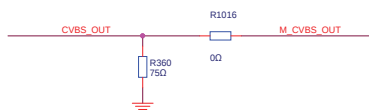
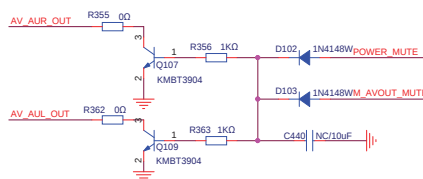
AUDIO OP



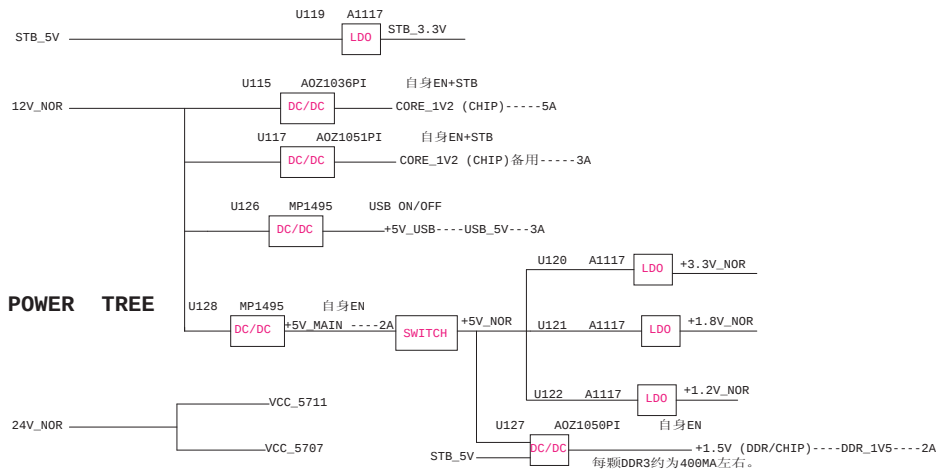
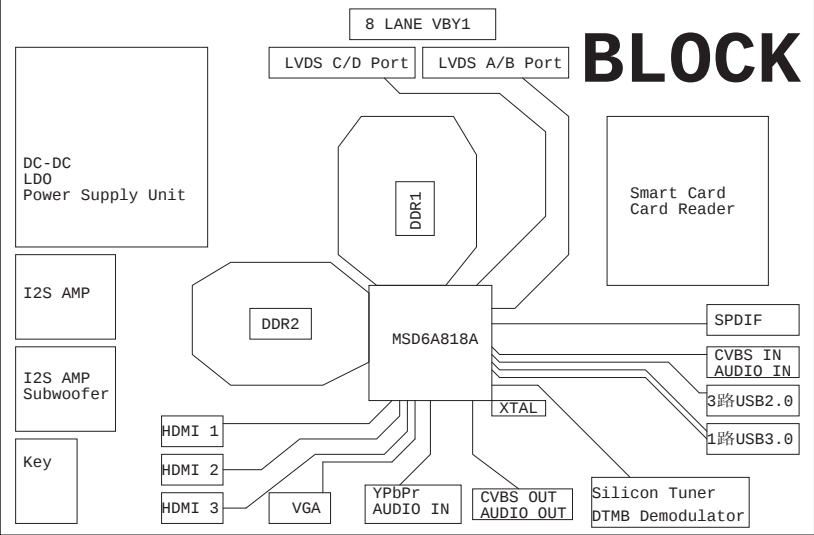
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AUDIO OUT MUTE OP

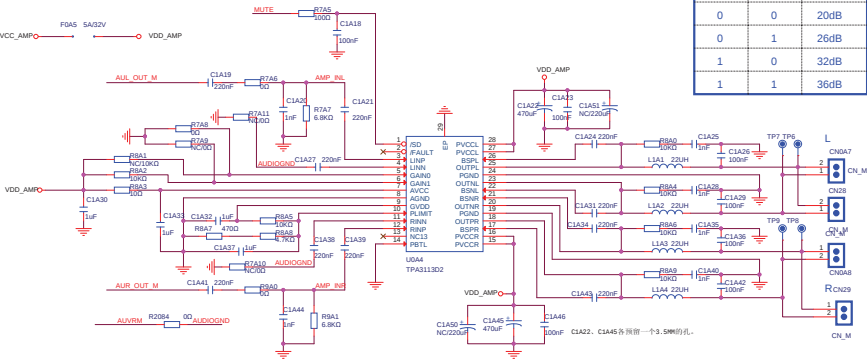


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File MSD018A	
Size B	Document Number AV_OUT
Date: Wednesday, January 15, 2014	Sheet 1 of 24
	Rev 1.0



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File	MS0818A		
Rev	Document Number	Rev	1.0
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Date	Wednesday, January 15, 2014	Sheet	1 of 24

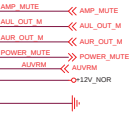
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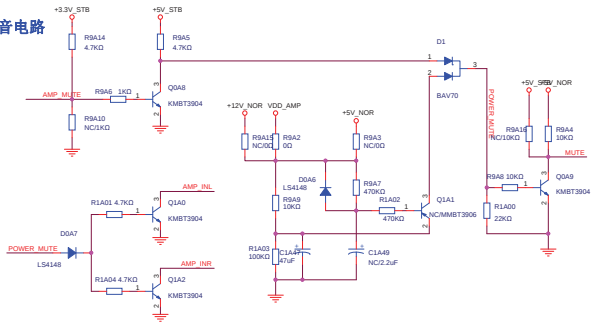
增益设置

GAIN1	GAIN0	AMP_GAIN
0	0	20dB
0	1	26dB
1	0	32dB
1	1	36dB

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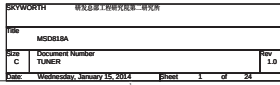


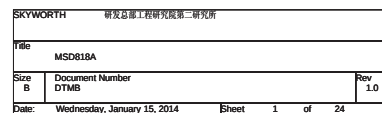
静音电路



说明:

- 1、在实际应用中，直流保护功能有可能被触发导致功放无输出，无法自动恢复，必须断电重启。为了避免这种应用问题，在电路设计初期需要注意功放输入部分的阻抗匹配。
- 2、可通过设定PLIMIT引脚上的电压来控制最大占空比的值，来设定最大输出功率。
- 3、左右声道间的电感在PCB板板时需保持尽可能大的距离，至少10mm，防止通道间串扰。



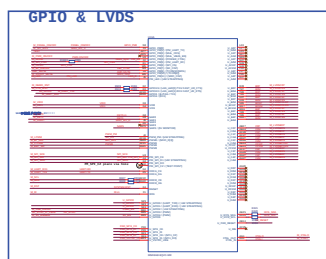
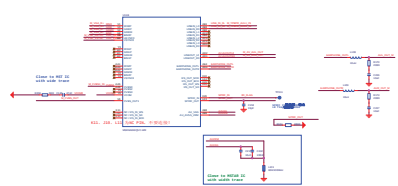
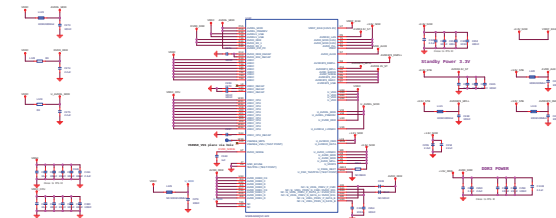
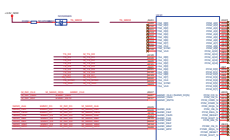
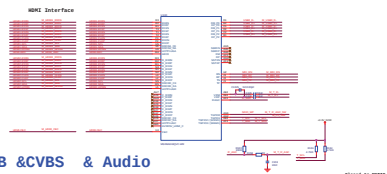


HDMI & LAN & USB & Front End

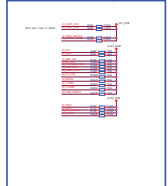
NAND & CI & TS & Front End

Power interface

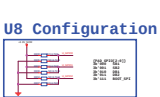
RGB & CVBS & Audio



GPIO Pull Up/Down

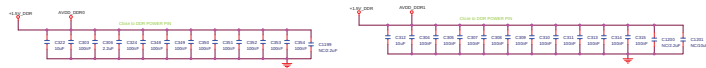


U8_CONFIGURATION



Mode Selection





MIU ONE BY ONE

